V54/7404 0/0560 V54H/74H04 €/5564 V54S/74S04 €/0565 V54S/74S04A €/0565 V54LS/74LS040/0563

1 14 Vcc
2 13
3 12
4 7 11
5 7 10
6 9

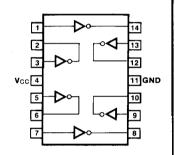
CONNECTION DIAGRAMS
PINOUT A

ORDERING CODE: See Section 9

	PIN	COMMERCIAL GRADE	MILITARY GRADE	PKG
PKGS	оит	$V_{CC} = +5.0 \text{ V } \pm 5\%,$ $T_A = 0^{\circ}\text{C to } +70^{\circ}\text{C}$	$V_{CC} = +5.0 \text{ V} \pm 10\%,$ $T_A = -55^{\circ} \text{ C} \text{ to } +125^{\circ} \text{ C}$	TYPE
Plastic DIP (P)	А	7404PC, 74H04PC 74S04PC, 74S04APC 74LS04PC		9A
Ceramic DIP (D)	А	7404DC, 74H04DC 74S04DC, 74S04ADC 74LS04DC	5404DM, 54H04DM 54S04DM, 54S04ADM 54LS04DM	6A
Flatpak (F)	A	74S04FC, 74S04AFC 74LS04FC	54S04FM, 54S04AFM 54LS04FM	31
	В	7404FC, 74H04FC	5404FM, 54H04FM	

HEX INVERTER

PINOUT B



INPUT LOADING/FAN-OUT: See Section 3 for U.L. definitions

PINS	54/74 (U.L.)	54/74H (U.L.)	54/74S (U.L.)	54/74LS (U.L.)		
	HIGH/LOW	HIGH/LOW	HIGH/LOW	HIGH/LOW		
Inputs Outputs	1.0/1.0 20/10	1.25/1.25 12.5/12.5	1.25/1.25 25/12.5	0.5/0.25 10/5.0 (2.5)		

DC AND AC CHARACTERISTICS: See Section 3*

SYMBOL	PARAMETER	54/74		54/74H		54/74S		54/74LS		UNITS	CONDITIONS	
J		Min	Max	Min	Max	Min	Max	Min	Max			
Іссн	Power Supply		12		26		24		2.4	mA	V _{IN} = Gnd	V _{CC} = Max
ICCL	Current		33		58		54		6.6		V _{IN} = Open	1
tpLH tpHL	Propagation Delay		22 15		10 10	2.0 2.0	4.5 .5.0		10 10	ns	Fig. 3-1, 3-4	4
tpLH tpHL	Propagation Delay (54/74S04A only)				,	1.0 1.0	3.5 4.0			ns	Fig. 3-1, 3-4	4

*DC limits apply over operating temperature range; AC limits apply at $T_A = +25$ °C and $V_{CC} = +5.0$ V.