Signetics

7433, LS33 Buffers

Quad Two-Input NOR Buffer (Open Collector)

Product Specification

Logic Products

TYPE	TYPICAL PROPAGATION DELAY	TYPICAL SUPPLY CURRENT (TOTAL)
7433	11ns	23mA
74LS33	19ns	4mA

ORDERING CODE

PACKAGES	COMMERCIAL RANGE V _{CC} = 5V ± 5%; T _A = 0°C to +70°C
Plastic DIP	N7433N, N74LS33N

NOTE:

For information regarding devices processed to Military Specifications, see the Signetics Military Products Data Manual.

FUNCTION TABLE

INP	JTS	OUTPUT
A	В	Y
L	L	н
Ł	Н	L
Н	L	L
н	н	L

H = HIGH voltage level

L = LOW voltage level

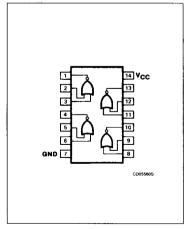
INPUT AND OUTPUT LOADING AND FAN-OUT TABLE

PINS	DESCRIPTION	74	74LS
A, B	Inputs	1ul	1LSul
Y	Output	30ul	10LSul

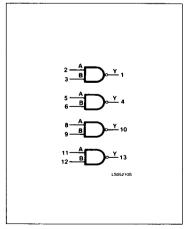
NOT

Where a 74 unit load (uI) is understood to be 40 μ A I $_{IH}$ and ~1.6mA I $_{IL}$, a 74LS unit load (LSuI) is 20 μ A I $_{IH}$ and ~0.4mA I $_{IL}$.

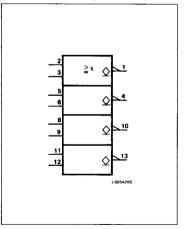
PIN CONFIGURATION



LOGIC SYMBOL



LOGIC SYMBOL (IEEE/IEC)



December 4, 1985

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853-0556 81501

Buffers

7433, LS33

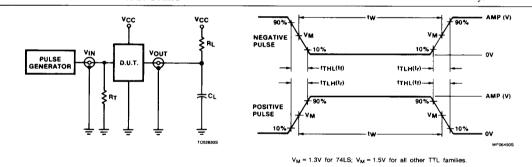
ABSOLUTE MAXIMUM RATINGS (Over operating free-air temperature range unless otherwise noted.)

	PARAMETER	74	74LS	UNIT
V _{CC}	Supply voltage	7.0	7.0	٧
ViN	Input voltage	-0.5 to +5.5	-0.5 to +7.0	V
I _{IN}	Input current	-30 to +5	-30 to +1	mA
V _{OUT}	Voltage applied to output in HIGH output state	-0.5 to +V _{CC}	-0.5 to +V _{CC}	٧
TA	Operating free-air temperature range	0 to	70	°C

RECOMMENDED OPERATING CONDITIONS

	PARAMETER	74			74LS			
	PARAMETER	Min	Nom	Max	Min	Nom	Max	UNIT
Vcc	Supply voltage	4.75	5.0	5.25	4.75	5.0	5.25	٧
V _{IH}	HIGH-level input voltage	2.0			2.0			٧
V _{IL}	LOW-level input voltage			+0.8			+ 0.8	٧
lικ	Input clamp current			-12			-18	mA
V _{OH}	HIGH-level output voltage			5.5			5.5	٧
loL	LOW-level output current			48			24	mA
T _A	Operating free-air temperature	0		70	0		70	°C

TEST CIRCUITS AND WAVEFORMS



Test Circuit For 74
Open Collector Outputs

Input Pulse Definition

DEFINITIONS

 R_L = Load resistor to V_{CC} ; see AC CHARACTERISTICS for value. C_L = Load capacitance includes jig and probe capacitance;

see AC CHARACTERISTICS for value. R_T = Termination resistance should be equal to Z_{OUT} of Pulse Generators.

 $t_{\mbox{\scriptsize TLH}},\,t_{\mbox{\scriptsize THL}}$ Values should be less than or equal to the table entries.

FARMIN	INPUT PULSE REQUIREMENTS				
FAMILY	Amplitude	Rep. Rate	Pulse Width	tTLH	t _{THL}
74	3.0V	1MHz	500ns	7ns	7ns
74LS	3.0V	1MHz	500ns	15ns	6ns
74S	3.0V	1MHz	500ns	2.5ns	2.5ns

Buffers 7433, LS33

DC ELECTRICAL CHARACTERISTICS (Over recommended operating free-air temperature range unless otherwise noted.)

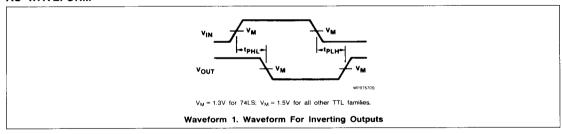
		GH-level Voc = MIN VII = MAX Vou = 5.5V		7433		74LS33				
	PARAMETER			Min	Typ ²	Max		Typ ²	Max	UNIT
Юн	HIGH-level output current					250			250	μΑ
	1 Old Inval	V - MINI	I _{OL} = MAX		0.2	0.4		0.35	0.5	V
V _{OL}	LOW-level output voltage	$V_{CC} = MIN,$ $V_{IH} = MIN$	1 _{OL} = 12mA (74LS)					0.25	0.4	٧
V _{IK}	Input clamp voltage	V _{CC} = N	MIN, II = IIK			-1.5			-1.5	٧
	Input current at maximum input voltage	V _{CC} = MAX	V ₁ = 5.5V			1.0				mA
lį			V _I = 7.0V						0.1	mA
	HIGH-level input	H-level input	V _I = 2.4V			40				μΑ
ΙΗ	current	$V_{CC} = MAX$	V _I = 2.7V						20	μΑ
IIL	LOW-level input current	$V_{CC} = MAX,$ $V_{I} = 0.4V$				-1.6			-0.4	mA
Icc	Supply current (total)	Supply current (total) V _{CC} = MAX	I _{CCH} Outputs HIGH		12	21		1.8	3.6	mA
			Outputs ICCL LOW		33	57		6.9	13.8	mA

NOTES:

1. For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions for the applicable type.

2. All typical values are at $V_{CC} = 5V$, $T_A = 25$ °C.

AC WAVEFORM



AC ELECTRICAL CHARACTERISTICS $T_A = 25$ °C, $V_{CC} = 5.0$ V

	PARAMETER TEST CONDITIONS		74 R _L = 133 Ω		74LS		
					C _L = 45pF,	UNIT	
			Min	Max	Min	Max	
t _{PLH}	Propagation delay	C _L = 50pF for 7433 Waveform 1		15 18		32 28	ns
t _{PLH}	Propagation delay	C _L = 150pF for 7433 Waveform 1		22 24			ns