chipKIT™ Basic I/O Shield Reference Design

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	Thuil D	921		10	Engineer:	NEA	
SHEET:	Connectors				Author:	GMA	
TITLE:	chipKIT Basic IO				Rev:	C.0	
Doc#:	500-216	Date:	07/03	1/2011	Sheet: 1/	1	



	1 O	8	IC3/PMCS2/PMA15/INT3/RD10
		34	PMRD/CN14/RD5
	3 6	9	0C4/RD3
	4 0	35	IC4/PMCS1/PMA14/INT4/RD11
	5 6	10	SS/RG9
		36	CN15/RD6
	7 0	11	MOSI
	8 0	37	CN16/RD7
I	3 6	12	MISO
IOH	100	38	U1RTS/RCLK1/SCK1/INT0/RF6
	111 6	13	SCK/RG6
	12 6	39	PMA9/U2RX/SDA2/CN17/RF4
	13 6	G	GND
	14 6	40	PMA8/U2TX/SCL2/CN18/RF5
	15 6	A	URFF+/ANØ
	16 O	41	URFF-/AN1
	10 0		
	J1		

	4.0	l a	LI1RX/SDI1//RF2
	1 O	26	PMD0/RE0
	3 6	1	U1TX/SD01/RF3
	³ 6	27	PMD1/RE1
	5 6	2	IC1/RTCC/INT1/RD8
	6 6	28	PMD2/RE2
	7 6	3	OC1/RDØ
	8 8	29	PMD3/RE3
\Box	ا ۾ ق	4	RF1
IOL	10 6	30	PMD4/RE4
		5	OC2/RD1
	12 6	31	PMD5/RE5
	13 6	6	OC3/RD2
	14 G	32	PMD6/RE6
	15 6	7	IC2/U1CTS/INT2/RD9
	16 6	33	PMD7/RE7
	10 0		
	J2		

	1 🔿	A0	AØ	
Б	² X	A1	A1	
Ō		A2	A2	
Analog	³ X	A3	A3	
Æ		A4	SDA1/RG3	
		A5	SCL1/RG2	
	0			
	J3			

WEEE

chipKIT

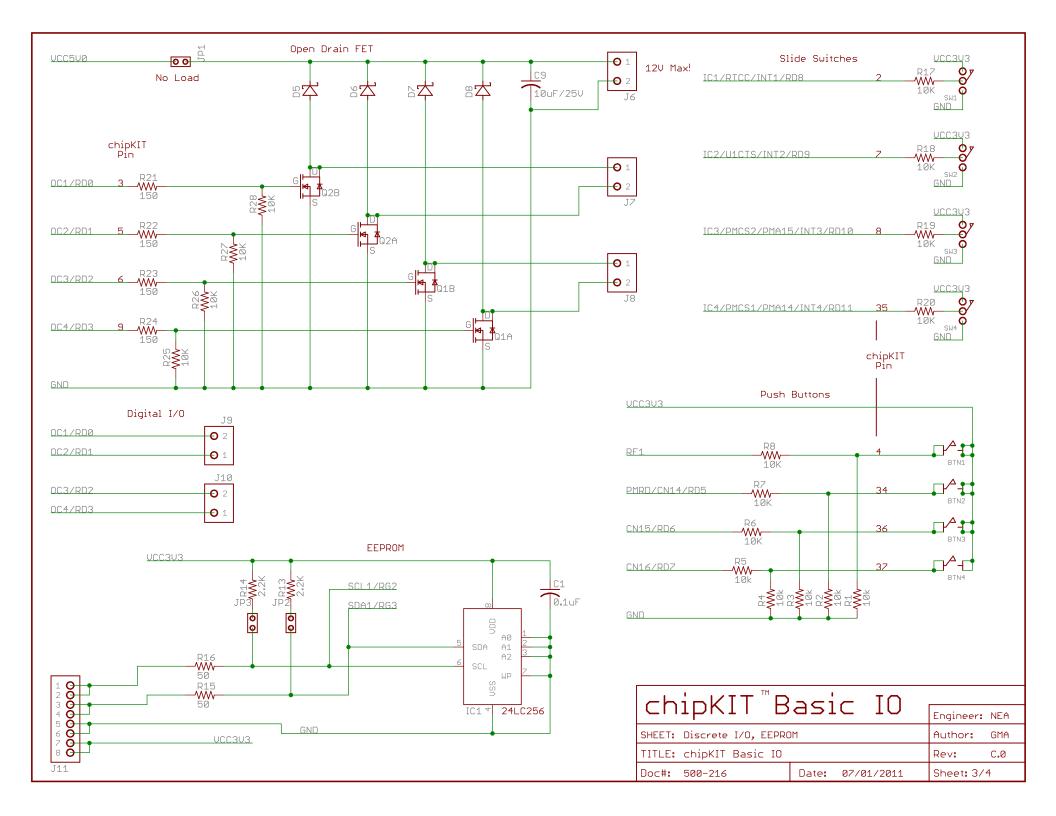
Digilent Inc.

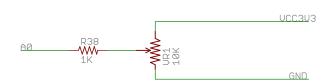
ROHS CHINA

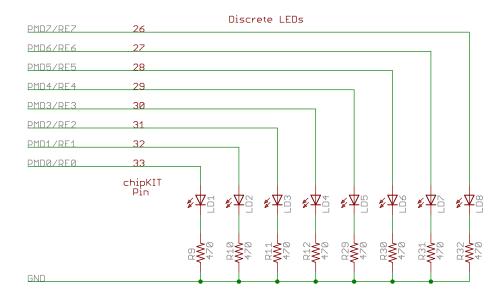
MISO	0.1	
SCK/RG6	0 2	_
MOSI	0 3	SP]
SS/RG9	ŏ ∮	
OINC.	O 6	
	J4	

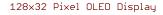
P32_MCLR UCC3U3 UCC5U0 GND UIN	1 2 3 4 5 6 000000	Power
	J5	

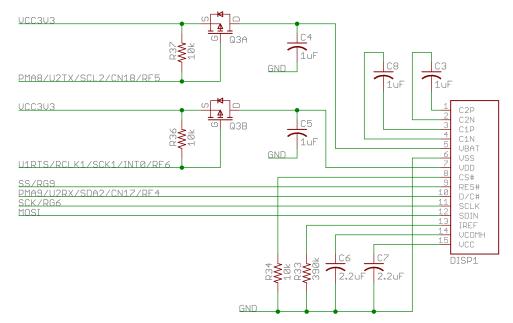
chipKIT Basic IO Footpeer: NEA						
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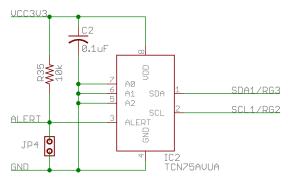












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CLITHYTI D	Engineer:	NEA				
SHEET: Discrete LEDs, OLE	Disp, Temp Sensor	Author:	GMA			
TITLE: chipKIT Basic IO		Rev:	C.0			
Doc#: 500-216	Sheet: 4/	4				