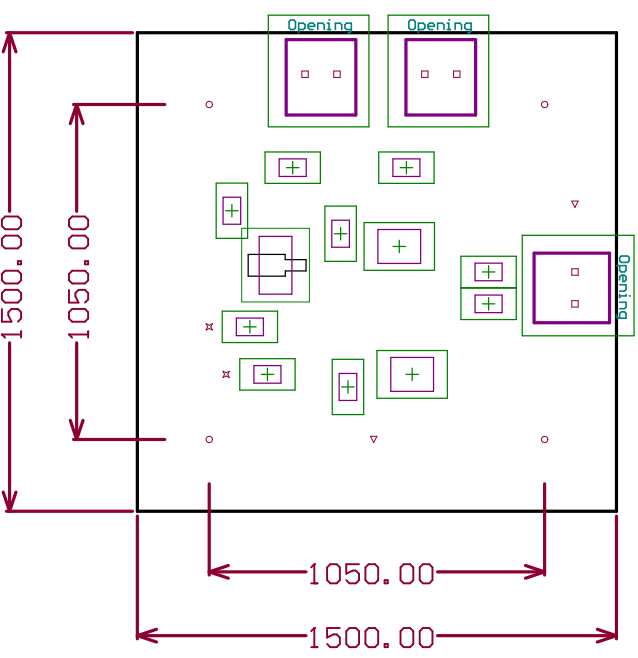


FABRICATION NOTES:

- 01. BOARD THICKNESS IS 0.062 IN. +/- 10%
- 02. WARP & TWIST SHALL NOT EXCEED 1%
- 03. ENIG FINISH
- 04. SOLDERMASK SHALL BE BLUE LOW GLOSS WITH WHITE EPOXY SILKSCREEN
- 05. ROHS LOGO SHALL BE PLACED IN SILK ON BOTTOM
- 06. LEAD FREE LOGO TO BE PLACED IN SILK ON BOTTOM
- 07. FAB HOUSE TO UPDATE SILKSCREEN FOR MANUFACTURE DATE
- 08. FAB HOUSE TO INCREMENT SERIAL NUMBER IN SILK, STARTING AT 00, FOR EACH BOARD
- 09. SMALLEST TRACE IS 25 MIL
- 10. SMALLEST DRILL IS 28 MIL
- 11. COPPER WEIGHT IS 2 OZ. INNER & OUTER
- 12. 100% ELECTRICAL TEST FOR OPENS & SHORTS
- 13. FINAL FINISH WILL HAVE NO NICKS OR CHIPS IN SOLDERMASK



Project #: N/A		Project Name: Detector Circuit	
Designer: Nathan A. Schram		Company: N/A	
REV #: 1.0	Date: 2018-FEB-03	Cage Code: N/A	
Notes:			

Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair	Via/Pad
⌘	2	28.00mil (0.711mm)	PTH	Round	TOP - BOTTOM	Via
▽	2	40.00mil (1.016mm)	PTH	Round	TOP - BOTTOM	Pad
□	6	45.28mil (1.150mm)	PTH	Round	TOP - BOTTOM	Pad
○	4	150.00mil (3.810mm)	PTH	Round	TOP - BOTTOM	Pad
	14 Total					

