Revision History/ Project Log

REV	COMMENTS	DATE	INITIALS
1.0	First pass of the schematic has been completed. Placement of the components has been completed. All passive component footprints are 0805(M). Top pour is GND, bottom pour is VCC.	2018-JAN-27	NAS
1.0	Still need to complete the MCU subsection and connections back to the PC.	2018-JAN-28	NAS
1.0	Schematic finished, PCB layout & routing finished. All components selected. Generating all outputs, including BOM and gettings quotes started for FAB.	2018-FEB-03	NAS

Schematic Documents

PAGE	PAGE NAME	
01	Detection_Circuit_History	
02	Detection_Circuit_Sheet_01	

Title Object Detection Circuit

Size Number N/A Revision
C N/A 1.0

Date: 2/3/2018 Sheet 1 of 2

File: FMCD Detection Circuit History Scholbgraum By: N. Schram

2 3 5

