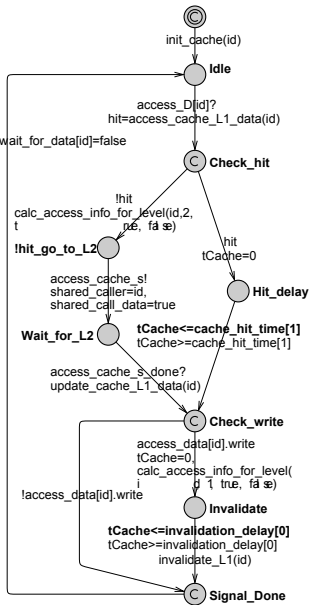


L1 Data Cache



L2 Cache

