instructionCacheMainMemory?
write_hit_wait −= 1

x <= CACHEFETCH

write_hit_wait >= 1
instructionCacheMainMemory!

write_hit_wait == 0 &&
x < CACHEFETCH

write_hit_wait != 0 ||
x == CACHEFETCH

x == CACHEFETCH

cache_contents(instrAdr) == −1
insert(instrAdr,1)

cache_contents(instrAdr) != −1
update(instrAdr, 1), x = 0

write_hit_wait == 0
instructionCacheWrite!

instructionCacheWrite?

A

initCaches?
initialiseDataCache()

instructionCacheRead?

instructionCacheRead!
write_hit_wait == 0

cache_contents(instrAdr) != −1
update(instrAdr, 0), x = 0

cache_contents(instrAdr) == −1
insert(instrAdr, 0)

write_hit_wait != 0 ||
x == CACHEFETCH

x == CACHEFETCH

write_hit_wait == 0 &&
x < CACHEFETCH

write_hit_wait >= 1
instructionCacheMainMemory!

instructionCacheMainMemory?
write_hit_wait −= 1

x <= CACHEFETCH