coherency	operation	execution time depending on number of active cores [us]							
		0	1	2	3	4	5	6	7
disabled	rr	69	70	69	70	70	71	71	72
	rw	69	69	71	89	86	83	83	84
	wr	60	62	62	62	63	63	64	64
	ww	60	60	75	91	92	93	94	97
static	rr	82	83	84	86	87	90	94	101
	rw	81	82	85	99	95	97	101	105
	wr	60	62	64	66	68	71	75	81
	ww	60	62	77	94	97	101	105	111
dynamic	rr	82	83	86	88	89	93	95	98
	rw	82	82	84	84	86	87	89	90
	wr	158	146	130	114	111	107	103	99
	ww	158	158	157	156	156	155	155	155