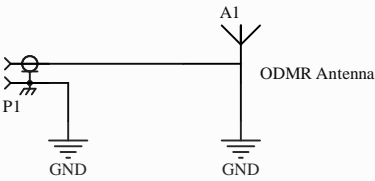


Microwave test board - antenna:

This board is designed to produce a field to excite NVPL for ODMR measurements.

There is one connector and an RF antenna.



Title		
Size Letter	Number	Revision
Date:	5/08/2024	Sheet of
File:	Z:\PCB Projects\...\microwaveBoardAntenna.sch	Drawn By:

A

B

C

D

E

F

1

1

2

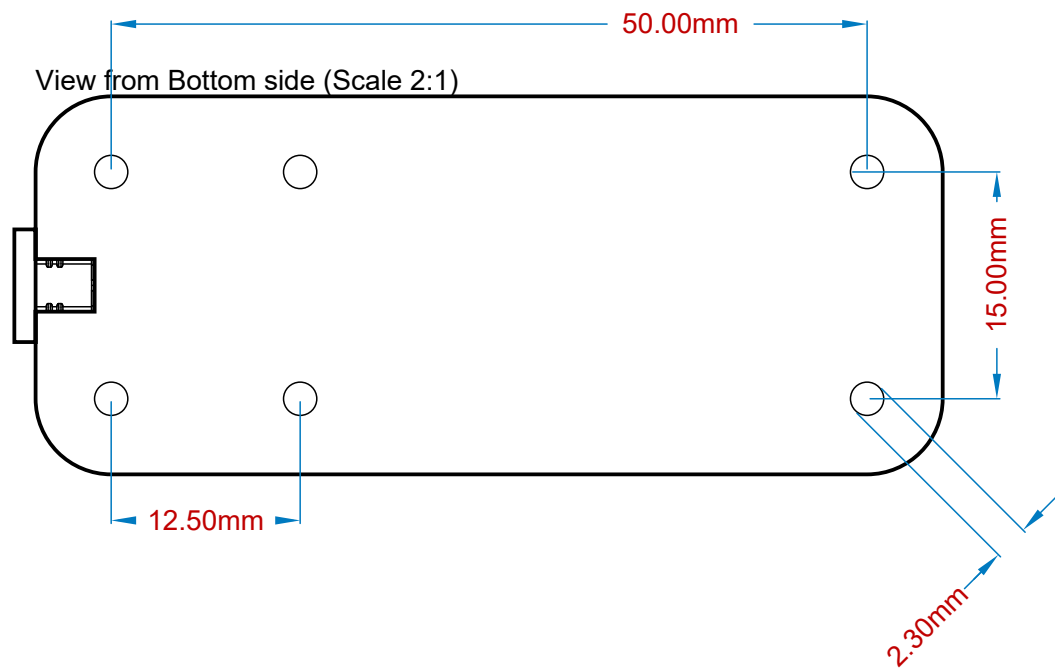
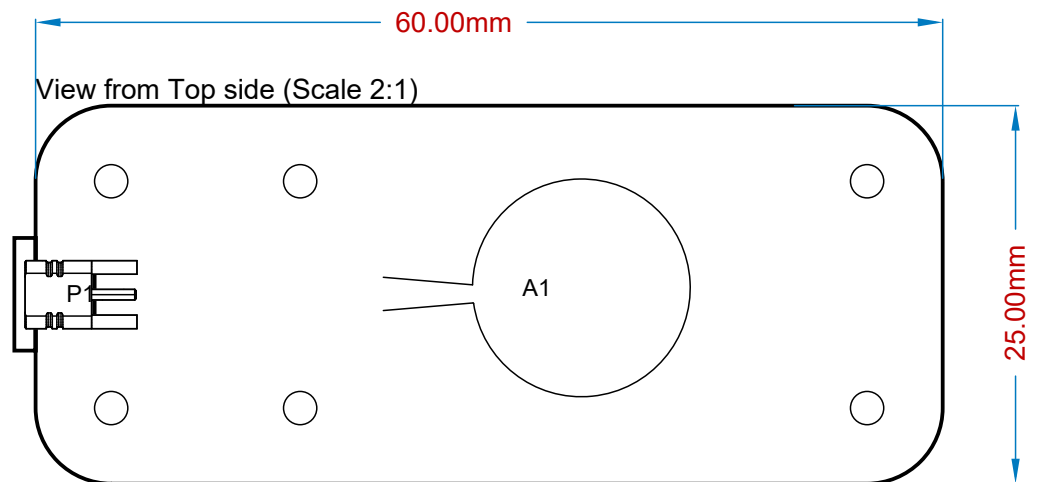
2

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4



A

B

C

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Comment	Description	Designator	Footprint	LibRef	Quantity
ODMR Antenna	antenna	A1	ODMR_Antenna	Patch Antenna	1
MMCXV-J-P-HF-ST-EM1	50 OHM MMCX HIGH-VIBRATION JACK,	P1	MMCXV-J-P-HF-ST-EM1	PXX-SAM-00013	1

Design Rules Verification Report

Filename : Z:\PCB Projects\microwaveBoardAntenna\microwaveBoardAntenna.PcbDoc

Warnings 0
Rule Violations 0

Warnings	
Total	0

Rule Violations	
Clearance Constraint (Gap=0.102mm) (All),(All)	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ((All))	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=1mm) (Max=3mm) (Preferred=3mm) (All)	0
Power Plane Connect Rule(Relief Connect)(Expansion=0.508mm) (Conductor Width=0.254mm)	0
Minimum Annular Ring (Minimum=0.15mm) (All)	0
Hole Size Constraint (Min=0.2mm) (Max=6.3mm) (All)	0
Hole To Hole Clearance (Gap=0.254mm) (All),(All)	0
Minimum Solder Mask Sliver (Gap=0.3mm) (All),(All)	0
Silk To Solder Mask (Clearance=0.254mm) (IsPad),(All)	0
Silk to Silk (Clearance=0mm) (All),(All)	0
Net Antennae (Tolerance=0mm) (All)	0
Board Clearance Constraint (Gap=0mm) (All)	0
Height Constraint (Min=0mm) (Max=25.4mm) (Preferred=12.7mm) (All)	0
Total	0