02E 17531

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TA7738P

AMPLIFIER SYSTEM FOR CASSETTE TAPE RECORDER.

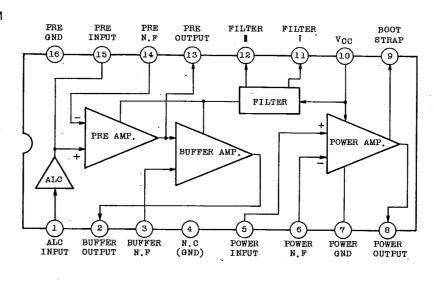
- . Recording Playback for Pre Amplifier
- .. Buffer Amplifier (Recording Amplifier)
- . Power Amplifier
- . Maximum Output Power : $P_{OM}\!\!=\!\!2.0W(\mbox{Typ.})$ at $V_{CC}\!\!=\!\!9V,$ THD=10%, $R_L\!\!=\!\!4\Omega,$ Attached P.C. Board
- . Low Distortion and Wide Dynamic Range
- . Without Turn-on "POP" for Muting Circuit
- . Operating Supply Voltage Range : $V_{\mbox{CC}}{=}3.5 \sim 9V$

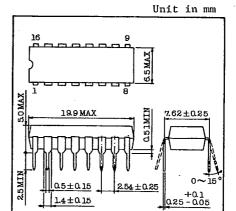
MAXIMUM RATINGS (Ta=25°C)

CHARACTERISTIC -	SYMBOL	RATING	UNIT	
Supply Voltage	v _{cc}	14		
Output Current	IO(Peak)	1.5	A	
Power Dissipation (Note)	PD	1200	mW	
Operating Temperature	Topr	-20 ~ 75	°C	
Storage Temperature	Tstg	-55 ~ 150	°C	

Note: Derated above Ta=25°C in the proportion of 9.6mW/°C.

BLOCK DIAGRAM





Lead pitch is 2.54 and tolerance is ± 0.25 against theoretical center of each lead that is obtained on the basis of No.1 and No.16 leads.

JEDEC	-	_
TOSHIBA	3D16A-P	

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ELECTRICAL CHARACTERISTICS

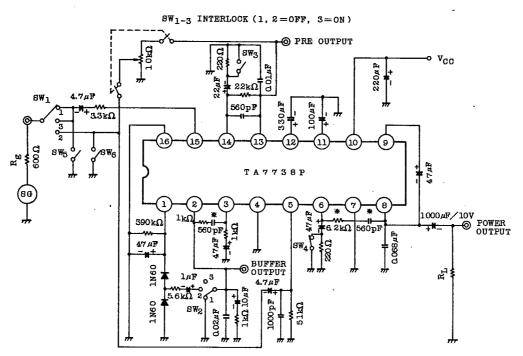
(Unless otherwise specified, V_{CC}=6V, f=1kHz, Ta=25°C)

(Autess ornerwise shectity	.eu, +66-\	J , 1-	-1KHZ, 14-25 0)				
CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	1	MIN.	TYP.	MAX.	UNIT
TOTAL							
Quiescent Current	I _{CCQ(1)}	_	V _{CC} =3.5V	7.5		-	mA
	ICCQ(2)		V _{CC} =6V	11		35	mA
PRE AMP.							
Open Loop Voltage Gain	G _{V01}	-	-	55	70		dB
Closed Loop Voltage Gain	GV1	-	-	-	40	_	dB
Maximum Output Voltage	V _{OUT1}	-	THD≤1%	-	0.7	-	Vrms
Input Resistance	R _{IN1}	-	-	_	30	-	kΩ
Equivalent Input Noise Voltage	v _{NI}	-	Rg=0	_	1.4	2.5	μV _{rms}
PRE AMP.+BUFFER AMP.							
Closed Loop Voltage Gain	G _{V2}	-	PRE AMP. Gy=40dB BUFFER AMP. Gy=20dB	-	60	_	dB
Maximum Output Voltage	V _{OUT2}	-	THD=3%	1.5	1.7	_	Vrms
Output Noise Voltage	v _{NO2}	-	Rg=0, GV2=60dB	-	1.2	2.5	mVrms
ALC Effect	ALC ₁	-	$V_{IN}=-60dBm \sim -20dBm$	-	2	-	dB
ALC Range	ALC2	-	RANGE OF THD≤1%	-	60	_	dB
POWER AMP.							
Open Loop Voltage Gain	GV03	-	-	60	70	_	dB
Closed Loop Voltage Gain	GV3	-	_	-	40	_	dB
Maximum Output Power	РОМ	-	R _L =4Ω, THD=10%	0.8	0.96	-	W
	FOM		$V_{CC}=9V, R_L=4\Omega, THD=10\%$] -	2.0	_	
Output Noise Voltage	V _{NO3}	-	Rg=0, GV=40dB	-	0.3	1.0	mVrms

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TEST CIRCUIT



*Use in measuring the output noise voltage.

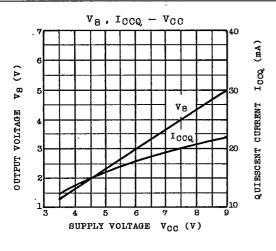
SWITCH OVER

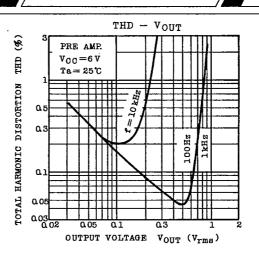
TEST ITEM	sw ₁	SW2	SW3	SW4	SW5	SW6
G _{V01}	1	1	ON	OFF	OFF	OFF
G _{V1}	1	1	OFF	OFF	OFF	OFF
V _{OUT1}	1	1	OFF	OFF	OFF	OFF
GV2	1	3	OFF	OFF	OFF	OFF
v _{out2}	1	3	OFF	OFF	OFF	OFF
V _{NO2}	1	3	OFF	OFF	ON	OFF
ALC	1	2	OFF	OFF	OFF	OFF
G _{V03}	2	1	OFF	ON	OFF	OFF
G _{V3}	2	1	OFF	OFF	OFF	OFF
Po	2	1	OFF	OFF	OFF	OFF
V _N 03	2	1	OFF	OFF	OFF	ON

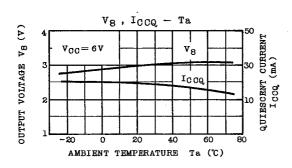
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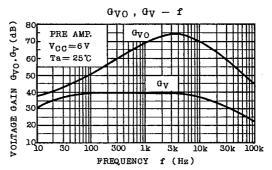
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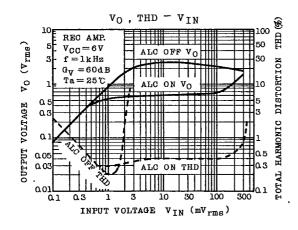
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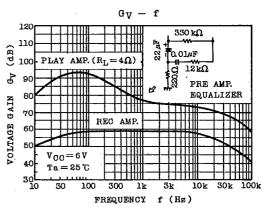










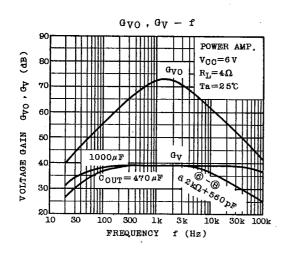


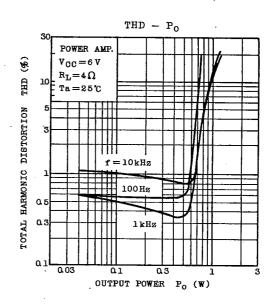
===AUDIO LINEAR IC==

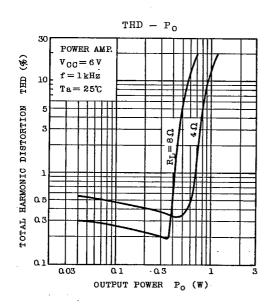
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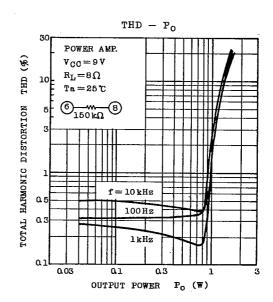
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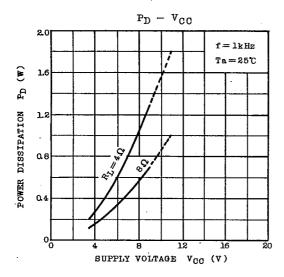


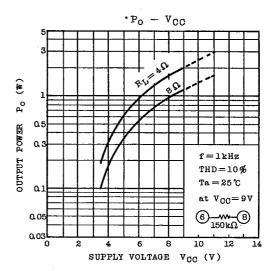


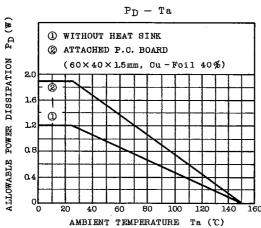
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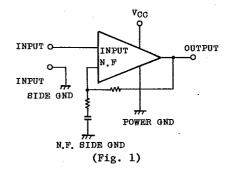
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PRECAUTION FOR USE

1. Precaution of GND Line

The GND points of input side GND and N.F. side GND in each amplifier must be arranged at the preamplifier side. (Fig.1)

The input side GND point and the N.F. side GND point must be arranged near each other to have no impedance.



2. About Radiation

- 1) The capacitance between the output and GND in preamplifier, buffer amplifier and power amplifier must be arranged near to this IC. The radiation loop must be as small as possible.
- 2) The voltage gain of high frequency in each amplifier must be reduced to reduce the noise with high frequency component. (In the application circuit, the feed back capacitor is used).

A 1000pF or same order capacitor must be connected from the input terminal of preamplifier and power amplifier to GND to prevent the interference of radiation.

- 3) It is better for this IC to separate adequately from the antenna in the radio cassette tape recorder applications.
- 4) The electrolytic capacitance $100 \, \mu F \sim 220 \, \mu F$ between VCC and GND must be arranged near to this IC.

3. To Prevent the Oscillation

- 1) The output terminal of preamplifier must be terminated to GND by the capacitance more than 5600pF. (The recommended value : $C=0.01\mu F$)
- 2) The output terminal of buffer amplifier must be terminated to GND by the capacitance more than $0.01\mu F$. (The recommended value : C=0.01 μF)

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3) The output terminal of power amplifier must be terminated to GND by the good temperature characteristic capacitance of $1\sim2.2\mu\text{F}$. The bootstrap terminal must be terminated to GND with the capacitance more than $0.47\mu\text{F}$.

4. Precaution of Preamplifier

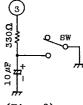
- 1) It is better that the coupling capacitor between the volume and the output of preamplifier is small. Recommended value : $C=0.47\mu F$
- 2) In recording mode, the signal source resistance must be more than $1k\Omega$ for ALC (Automatic Level Control) operation. When this resistance is small, the ALC range becomes narrow.

5. Buffer Amplifier

When the output signal is clipped in the buffer amplifier, this signal returns to preamplifier and influences to make THD bad.

To prevent this, in playback mode, the buffer amplifier is recommended to be cut-off by terminating the output terminal to GND

cut-off by terminating the output terminal to GND directly or by terminating the feedback terminal to GND through 330Ω or less than this shown in Fig. 2.



(Fig. 2)

6. Power Amplifier

1) In case of the battery use ($V_{CC} \le 6V$), this IC is happen to oscillate (blocking oscillation) when the impedance of power supply is high.

In this case, it is recommended to insert the resistance R_A of $500\,\Omega\sim lk\Omega$ between 10 pin and 12 pin (VCC-Filter II). By this method, this IC becomes very stable. But the output DC voltage is not center by the influence of R_A . Then the output wave shape is not symmetrical clipping wave and the maximum output voltage is reduced. So we recommend to insert the other resistance R_B between 6 pin and 8 pin for compensating the output DC voltage.

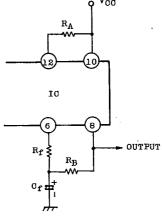
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- 2) When you use the IC at the supply voltage of more than 6V, it is better to insert the resistance R_B for compensating output DC voltage. (Fig. 3) The recommended value : $\begin{cases} R_B = 220 k\Omega & \text{at } V_{CC} = 7.5V \\ R_B = 150 k\Omega & \text{at } V_{CC} = 9V \end{cases}$
- 3) The output coupling capacitance and bootstrap capacitance is better to be large because of getting the maximum output power.
- 4) On the PC board, the interval between $V_{\mbox{\footnotesize CC}}$ and GND is better to be large.



(Fig. 3)