

# LM317M

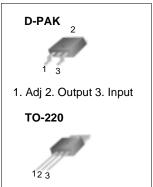
# 3-Terminal 0.5A Positive Adjustable Regulator

### **Features**

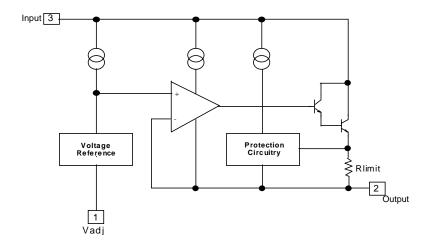
- Output Current in Excess of 0.5A
- Output Adjustable Between 1.2V and 37V
- Internal Thermal Overload Protection
- Internal Short Circuit Current Limiting
- Output Transistor Safe Area Compensation
- Floating Operation for High Voltage Applications

## **Description**

The LM317M is a 3-terminal adjustable positive voltage regulator capable of supplying in excess of 500mA over an output voltage range of 1.2V to 37V. This voltage regulator is exceptionally easy to use and requires only two external resistors to set the output voltage.



## **Internal Block Diagram**



## **Absolute Maximum Ratings**

Parameter	Symbol	Value	Unit
Input-Output Voltage Differential	VI - VO	40	V
Power Dissipation	PD	Internally Limited	W
Thermal Resistance Junction-Air D-PAK (Note1,2)	R <sub>θ</sub> JA	100	°C/W
Operating JunctionTemperature Range	Tj	0 ~ +125	°C
Storage Temperature Range	TSTG	-65 ~ +125	°C

### **Electrical Characteristics**

(VI-VO = 5V, IO = 0.1A,  $0^{\circ}$ C  $\leq$  TJ  $\leq$  +125 $^{\circ}$ C, PDMAX = 7.5W, unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Line Regulation (Note3)		$T_A = +25^{\circ}C, 3V \le V_I - V_O \le 40V$	-	0.01	0.04	%/ V
Line Regulation (Notes)	Rline	3V ≤ V <sub>I</sub> −V <sub>O</sub> ≤ 40V	-	0.02	0.07	
oad Regulation (Note3) Rload		$T_A$ =+25°C, 10mA $\leq I_O \leq 0.5A$ $V_O \leq 5V$ $V_O \geq 5V$	-	5 0.1	25 0.5	mV %/ VO
		$10mA \le I_O \le 0.5A$ $V_O \le 5V$ $V_O \ge 5V$	-	20 0.3	70 1.5	mV %/ VO
Adjustment Pin Current	IADJ	-	-	50	100	uA
Adjustment Pin Current Change	Δl <sub>AD</sub> J	$3V \le V_I - V_O \le 40V$ $10mA \le I_O \le 0.5A, P_D < P_{DMAX}$	-	0.2	5	uA
Reference Voltage	VREF	3V < V <sub>I</sub> - V <sub>O</sub> < 40V 10mA ≤ I <sub>O</sub> ≤ 0.5A, P <sub>D</sub> < P <sub>DMAX</sub>	1.20	1.25	1.30	V
Temperature Stability	STT	-	-	0.7	-	%/ Vo
Minimum Load Current to Maintain Regulation	IL(MIN)	VI - VO = 40V	-	3.5	10	mA
		VI - VO ≤ 15V, PD < PDMAX	0.5	0.9	-	
Maximum Output Current	IO(MAX)	V <sub>I</sub> - V <sub>O</sub> = 40V P <sub>D</sub> < P <sub>DMAX</sub> , T <sub>A</sub> =+25°C	0.15	0.25	-	А
RMS Noise, % of Vout	eN	T <sub>A</sub> = +25°C, 10Hz < f < 10KHz	-	0.003	-	%/ Vo
Ripple Rejection	RR	VO = 10V, f = 120Hz without CADJ CADJ = 10uF (Note4)	66	65 80	-	dB
Long-Term Stability	ST	T <sub>J</sub> =+125°C, 1000Hours	-	0.3	1	%/1000Hrs

### Note:

- Thermal resistance test board Size: 76.2mm \* 114.3mm \* 1.6mm(1S0P) JEDEC standard: JESD51-3, JESD51-7
- 2. Assume no ambient airflow.
- 3. Load and Line regulation are specified at constant junction temperature. Change in Vo due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.
- 4. CADJ, when used, is connected between the adjustment pin and ground.

## **Typical Perfomance Characteristics**

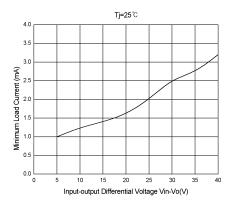


Figure 1. Minimum Load Current

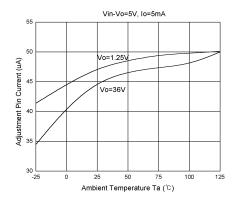


Figure 3. Adjustment Pin Current vs. Temperature

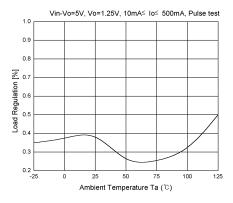


Figure 5. Load Regulation vs. Temperature

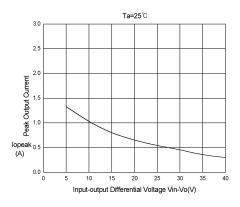


Figure 2. Peak Output Current vs. Input-Output Differential Voltage

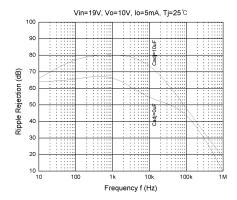


Figure 4. Ripple Rejection vs. Frequency

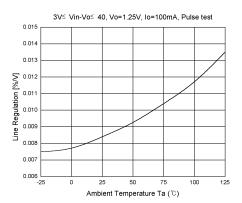


Figure 6. Line Regulation vs. Temperature

# **Typical Perfomance Characteristics** (Continued)

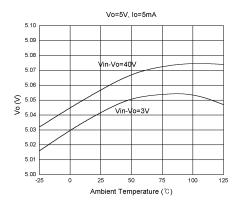


Figure 7. Outputvoltage vs. Temperature

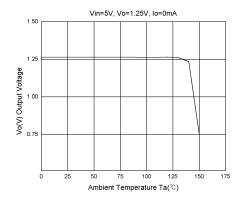


Figure 8. Thermal Shutdown

## **Typical Application**

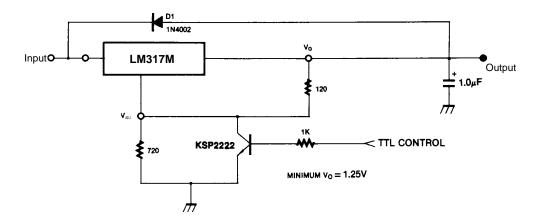


Figure 1. 1 5V Electronic Shutdown Regulator

D1 protects the device during an input short circuit.

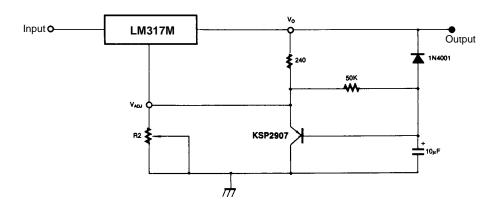


Figure 2. Slow Turn-On Regulator

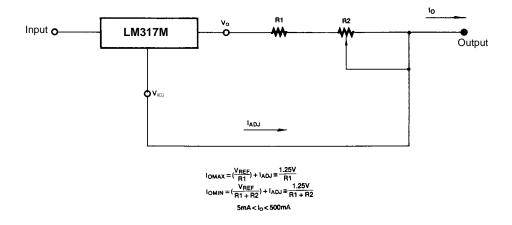
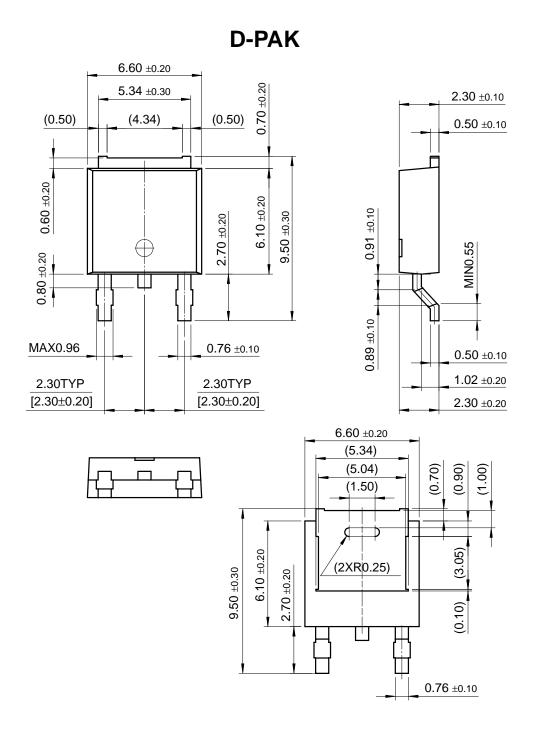


Figure 3. Current Regulator

### **Mechanical Dimensions**

## **Package**

### **Dimensions in millimeters**

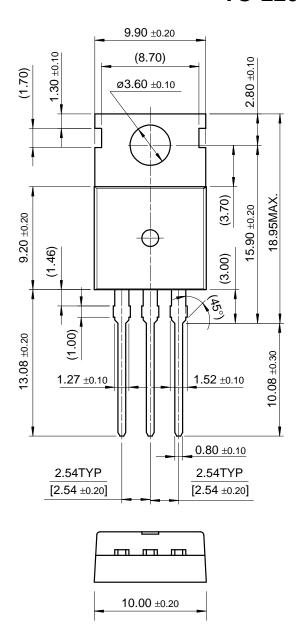


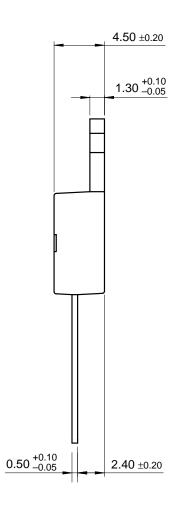
## **Mechanical Dimensions** (Continued)

## **Package**

### **Dimensions in millimeters**

**TO-220** 





## **Ordering Information**

Product Number	Package	Operating Temperature
LM317MDT	D-PAK	0 ~ 125°C
LM317MT	TO-220	0 ~ 123 6

### **DISCLAIMER**

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

#### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE PRESIDENT OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
- A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

www.fairchildsemi.com