1. Description

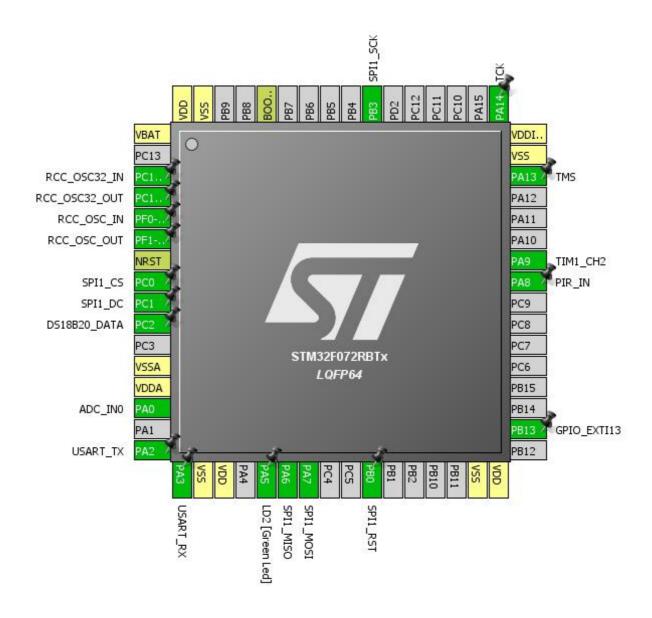
1.1. Project

Project Name	homekits
Generated with:	STM32CubeMX 4.6.0
Date	01/28/2015

1.2. MCU

MCU Serie	STM32F0
MCU Line	STM32F0x2
MCU name	STM32F072RBTx
MCU Package	LQFP64
MCU Pin number	64

2. Pinout Configuration



3. IPs and Middlewares Configuration

IP	Mode	Fonction	Pin
ADC	INO	ADC_IN0	PA0
	High Speed Clock (HSE):	RCC_OSC_IN	PF0-OSC_IN
200	Crystal/Ceramic Resonator	RCC_OSC_OUT	PF1-OSC_OUT
RCC	Low Speed Clock (LSE):	RCC_OSC32_IN	PC14-OSC32_IN
	Crystal/Ceramic Resonator	RCC_OSC32_OUT	PC15-OSC32_OUT
	Alarm A: Internal Alarm A	N/A	N/A
RTC	WakeUp: Internal WakeUp	N/A	N/A
		SPI1_MISO	PA6
SPI1	Mode:	SPI1_MOSI	PA7
	Full-Duplex Master	SPI1_SCK	PB3
0)/0	Oseful Wine Dahara	SYS_SWCLK	PA14
SYS	Serial-WireDebug	SYS_SWDIO	PA13
	Clock Source : Internal Clock	N/A	N/A
TIM1	Channel2: PWM Generation CH2	TIM1_CH2	PA9
TIM2	Clock Source : Internal Clock	N/A	N/A
HOARTO	Mode:	USART2_RX	PA3
USART2	Asynchronous	USART2_TX	PA2

4. Pins Configuration

Pin	Pos	Function(s)	Label
PC14-OSC32_IN	3	RCC_OSC32_IN	
PC15-OSC32_OUT	4	RCC_OSC32_OUT	
PF0-OSC_IN	5	RCC_OSC_IN	
PF1-OSC_OUT	6	RCC_OSC_OUT	
PC0 *	8	GPIO_Output	SPI1_CS
PC1 *	9	GPIO_Output	SPI1_DC
PC2 *	10	GPIO_Output	DS18B20_DATA
PA0	14	ADC_IN0	
PA2	16	USART2_TX	USART_TX
PA3	17	USART2_RX	USART_RX
PA5 *	21	GPIO_Output	LD2 [Green Led]
PA6	22	SPI1_MISO	
PA7	23	SPI1_MOSI	
PB0 *	26	GPIO_Output	SPI1_RST
PB13	34	GPIO_EXTI13	
PA8 *	41	GPIO_Input	PIR_IN
PA9	42	TIM1_CH2	
PA13	46	SYS_SWDIO	TMS
PA14	49	SYS_SWCLK	тск
PB3	55	SPI1_SCK	

^{*} The pin is affected with an I/O function

5. Power Plugin report

5.1. Microcontroller Selection

Serie	STM32F0
Line	STM32F0x2
мси	STM32F072RBTx
Datasheet	025004_Rev2

5.2. Parameter Selection

Temperature	25
Vdd	3.6

homekits Project

6. Software Project

6.1. Project Settings

Name	Value
Project Name	homekits
Project Folder	C:\Users\New\Documents\stm32\f072\homekits
Toolchain / IDE	MDK-ARM 4.73
Firmware Package Name and Version	STM32Cube FW_F0 V1.2.1

6.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power	No
consumption)	

6.3. Toolchains Settings

Name	Value
Compiler Optimizations	Balanced Size/Speed