

Secure Two Party Computation

A practical comparison of recent protocols

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- Recently many protocols have been proposed to use Yao Circuits to provide security in the presence of active adversaries.
- Our aim was to implement some of these recent protocols so we could compare their performance.

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 - ▶ Using these inputs the Executor can then evaluate the circuit and so the function.

Oblivious Transfer

Receiver

Inputs : $b \in \{0, 1\}$

Outputs : x_b

Sender

Inputs : $x_0, x_1 \in \{0, 1\}^l$

Outputs : \emptyset

Formal definition of the functionality of a one-out-of-two OT protocol. The Receiver should learn nothing about the value of x_{1-b} and the Sender should learn nothing about b .

We will not dwell on the details of Oblivious Transfer, suffice to say it is possible, if anyone is interested in seeing a concrete protocol I suggest the Naor-Pinkas Oblivious Transfer.

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 - ▶ Commit and Prove.

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- ✂ The Executor then picks a set of these circuits and asks the Builder to open them so they can be checked for correctness.
- ✂ If all check circuits pass then the rest of the circuits are evaluated.
- ✂ So a malicious Builder must now guess which circuits will be checked.

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 - ▶ Consistency of Executor's inputs.
 - ▶ Output determination.

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- So we need 130 circuits to achieve statistical security of 2^{-40} .

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- ✶ So it's expected to do well for large circuits, badly for small circuits.
- ✶ Statistical security 2^{-S} , so we need 40 circuits to achieve statistical security of 2^{-40} .

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- ✦ Statistical security $2^{-S+\log(S)}$, so we need 46 circuits to achieve statistical security of 2^{-40} .

Merging Lindell and HKE

- ✶ The obvious question raised by Lindell 2013 is can we improve it by changing

32-bit Addition Circuit Results

Builder	CPU Time	Wall Time	Bytes Sent	Bytes Recv
LP 2010	113.96	27.41	7,648,074	737,109
L 2013	171.21	42.03	4,693,761	980,193
HKE 2013	45.59	6.77	3,143,383	3,143,366
L-HKE	145.77	25.47	5,995,366	3,299,399

Executor	CPU Time	Wall Time	Bytes Sent	Bytes Recv
LP 2010	55.90	27.45	737,109	7,648,074
L 2013	101.69	42.05	980,193	4,693,761
HKE 2013	45.59	6.77	3,143,366	3,143,383
L-HKE	132.51	25.83	3,299,399	5,995,366

AES-128 Encryption Circuit Results

Builder	CPU Time	Wall Time	Bytes Sent	Bytes Recv
LP 2010	480.82	114.98	668,935,684	2,798,517
L 2013	399.27	119.25	210,537,538	1,609,692
HKE 2013	185.47	32.95	238,300,835	238,300,840
L-HKE	417.84	78.22	214,725,419	7,868,176

Executor	CPU Time	Wall Time	Bytes Sent	Bytes Recv
LP 2010	227.91	116.15	2,798,517	668,935,684
L 2013	270.99	119.27	1,609,692	210,537,538
HKE 2013	185.47	32.95	238,300,840	238,300,835
L-HKE	363.46	80.49	7,868,176	214,725,419