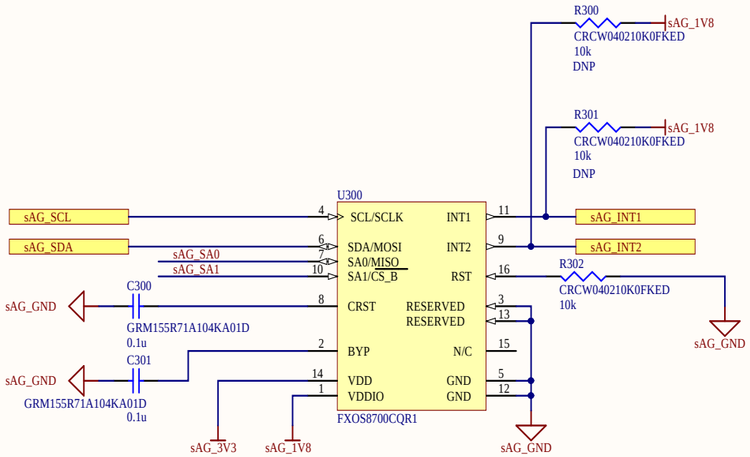
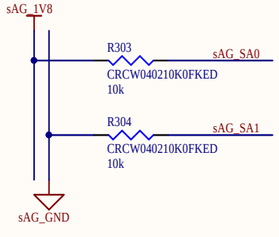


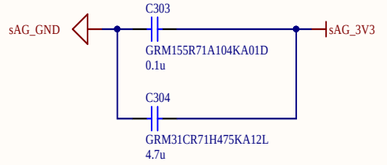


Layout Notes: The address is determined by what is connected to pins SA0-1. See lookup table for connections.

I2C Addres: 0x1D

SA1	SA0	Slave address
0	0	0x1E
0	1	0x1D
1	0	0x1C
1	1	0x1F

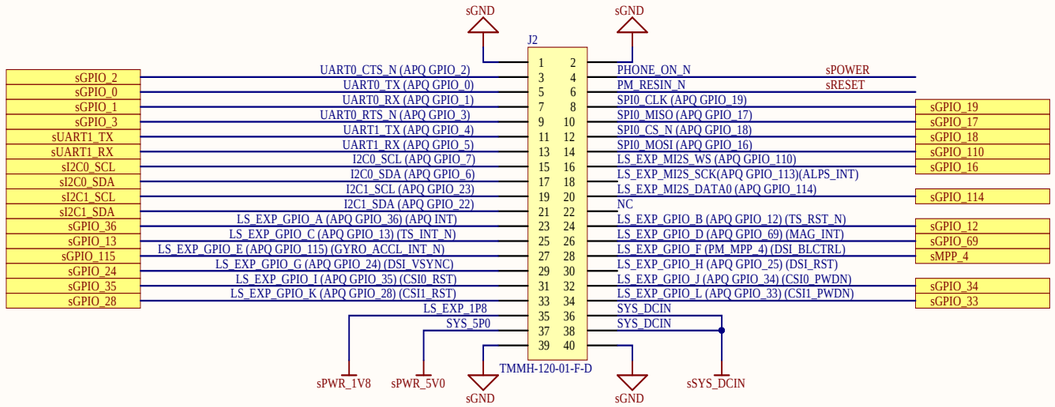
Layout Notes: Connect capacitance directly between VDDIO (Pin 1) and GND (Pin 5 or 12).

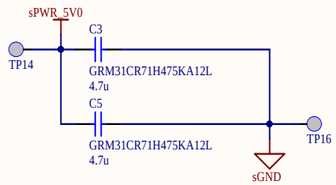


Layout Notes: Connect capacitance directly between VDD (Pin 14) and GND (Pin 5 or 12).

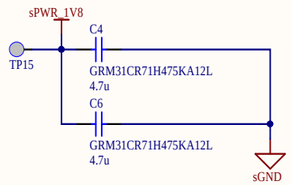
Title	ViSOR	
Size: Tabloid	Number: Accelerometer & Magnetometer	Revision: 2.0.0
Date: 9/24/2018	Sheet of	
File: C:\Users\..\accel_mag.SchDoc	Drawn By:	

Low Speed Connector

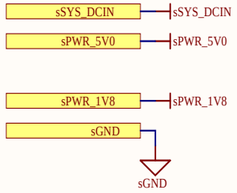




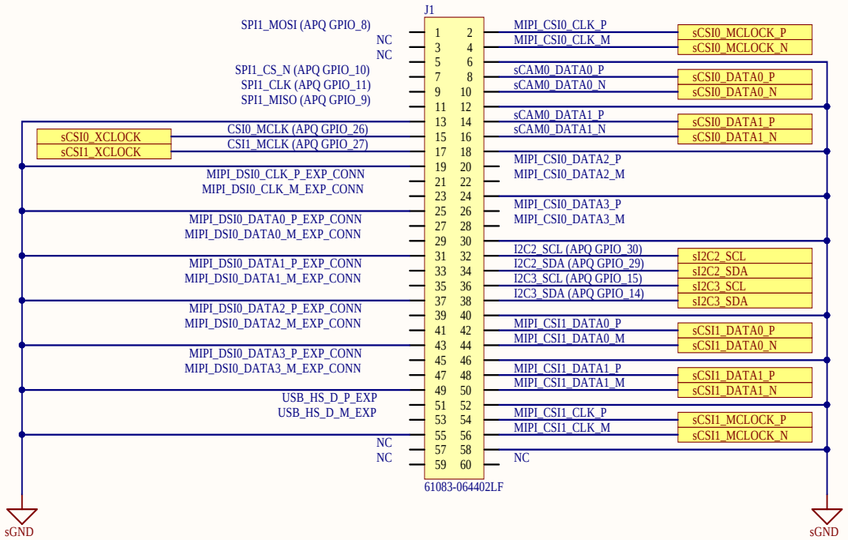
Layout Notes: The capacitor should be placed as close to the Pin 37 as possible.

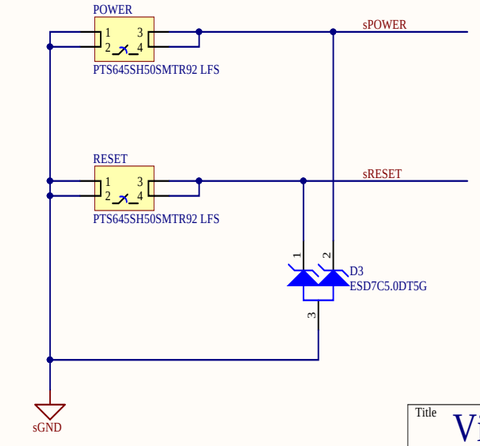


Layout Notes: The capacitor should be placed as close to the Pin 37 as possible.

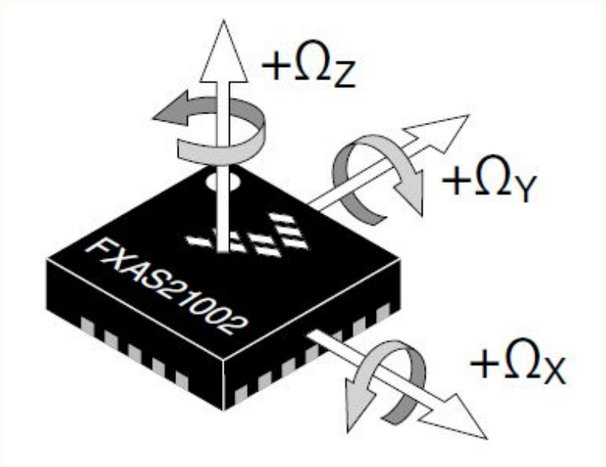


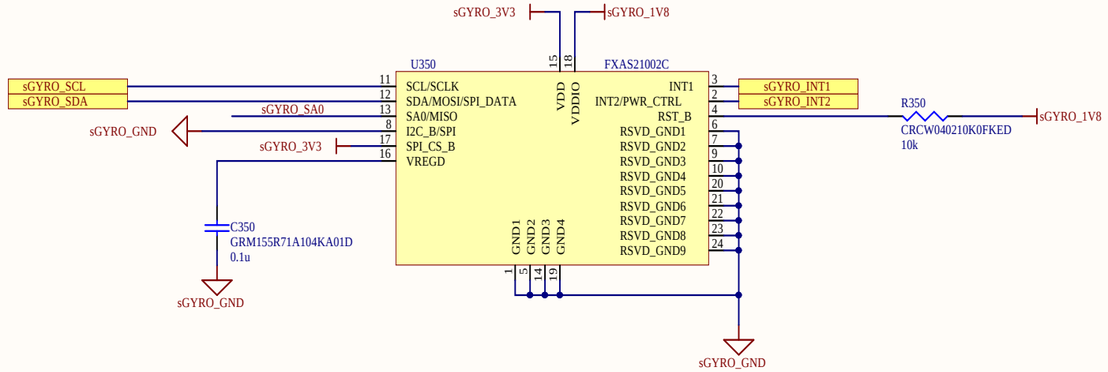
High Speed Connector





Title: ViSOR		
Size: Tabloid	Number: Dragonboard Connections	Revision: 2.0.0
Date: 9/24/2018	Sheet of	
File: C:\Users\..\dragonboard.SchDoc	Drawn By: Brandon P Dixon	

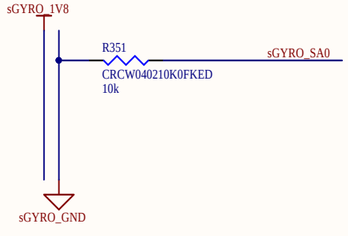


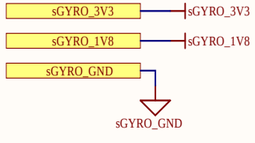


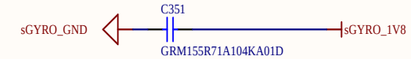
Layout Notes: The address is determined by what is connected to pin SA0. See lookup table for connections.

I2C Addres: 0x20

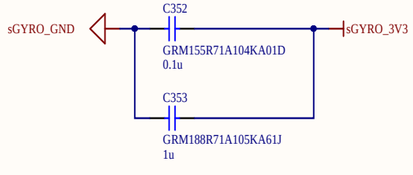
SA0	Slave address
0	0x20
1	0x21







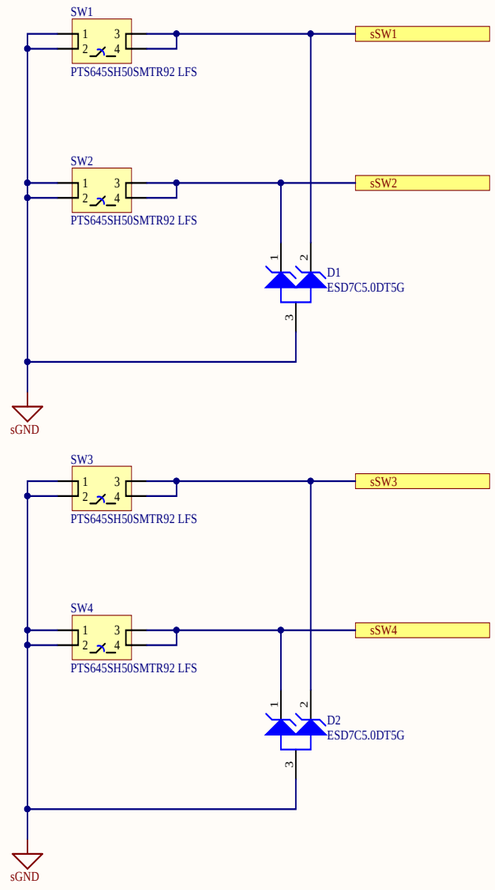
Layout Notes: Connect capacitance directly between VDDIO (Pin 18) and GND (Pin 19).

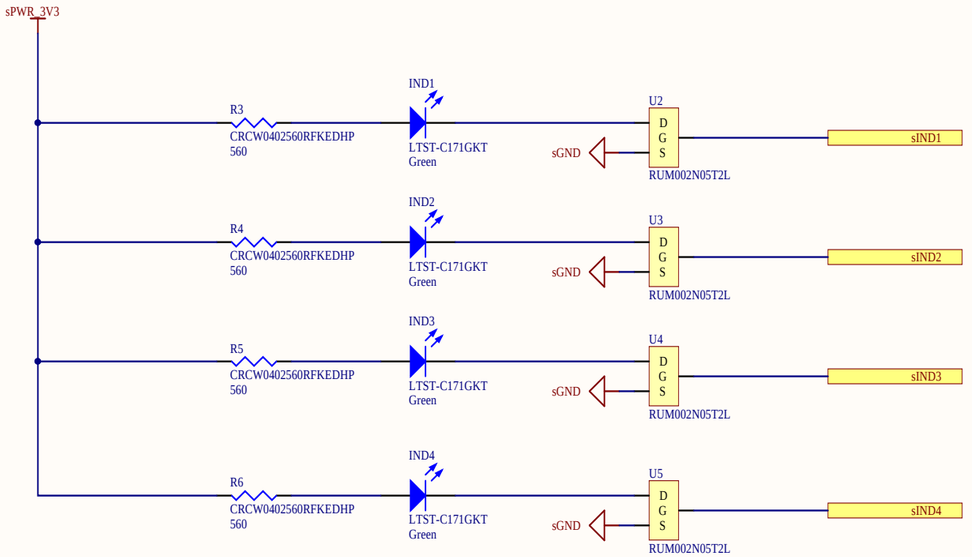


Layout Notes: Connect capacitance directly between VDD (Pin 15) and GND (Pin 14).

Title	ViSOR		
Size	Number		Revision
Tabloid	Gyroscope		2.0.0
Date:	9/24/2018	Sheet	of
File:	C:\Users\..\ gyroscope.SchDoc	Drawn By:	

The pins 1&2 OR 3&4 are internally connected, so if moving the connection make life easier, then it can move.

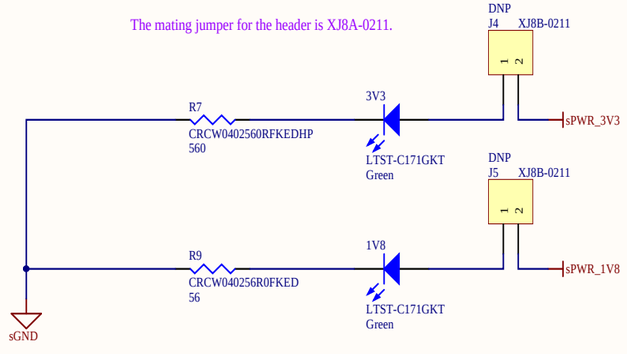




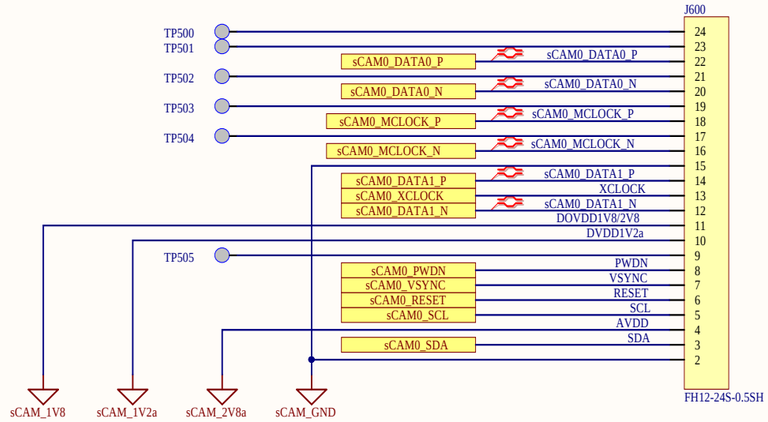
Designed to mate with cable part # TTL-232RGVSW5V-WE

Brown	RTS
Green	RX
Orange	TX
Yellow	VCC
Black	CTS
Red	GND

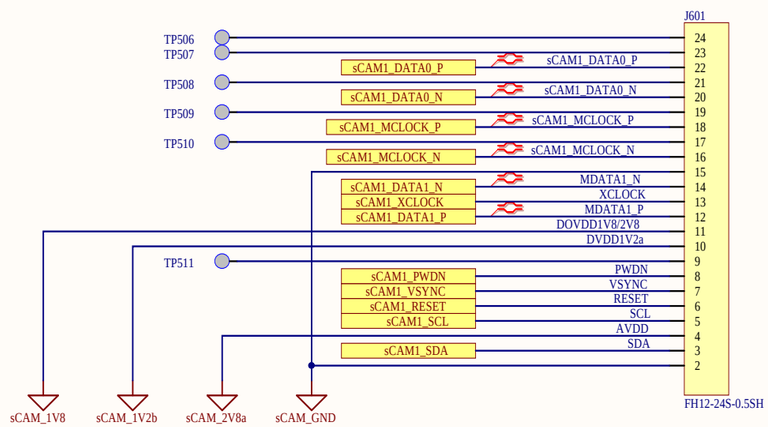
The mating jumper for the header is XJ8A-0211.



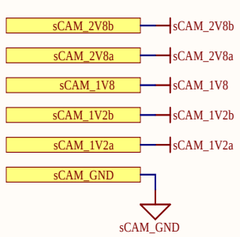
Title	ViSOR		
Size: Tabloid	Number: I/O		Revision: 2.0.0
Date: 9/24/2018		Sheet of	
File: C:\Users\.._io.SchDoc		Drawn By:	



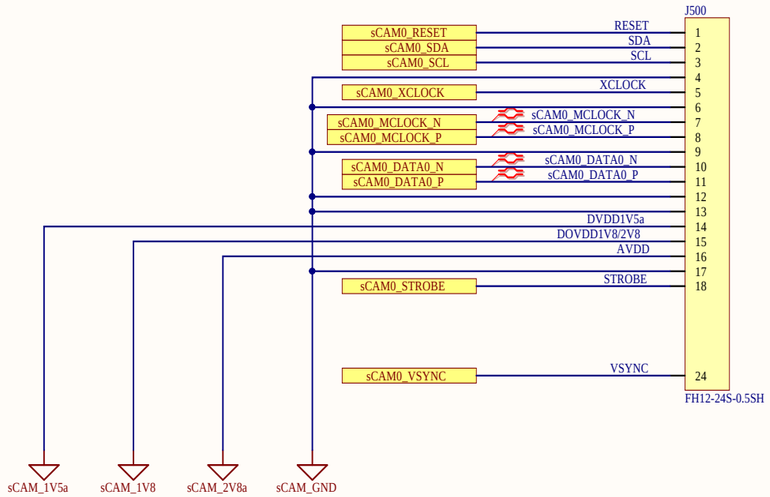
KLT-J4K-OV2732
CAMERA 0



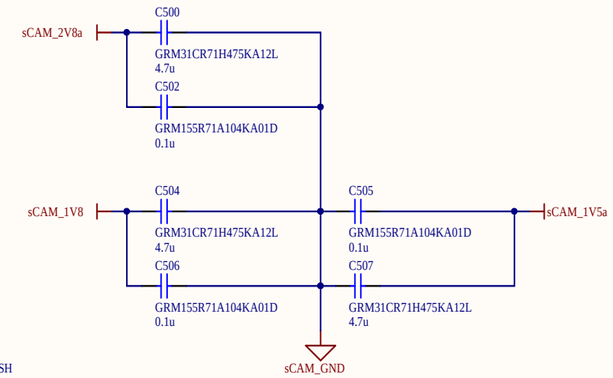
KLT-J4K-OV2732
CAMERA 1



Title: ViSOR		
Size: Tabloid	Number: KLT-J4K-OV2732	Revision: 2.0.0
Date: 9/24/2018		Sheet of
File: C:\Users\..\klt-j4k-ov2732.SchDoc		Drawn By:

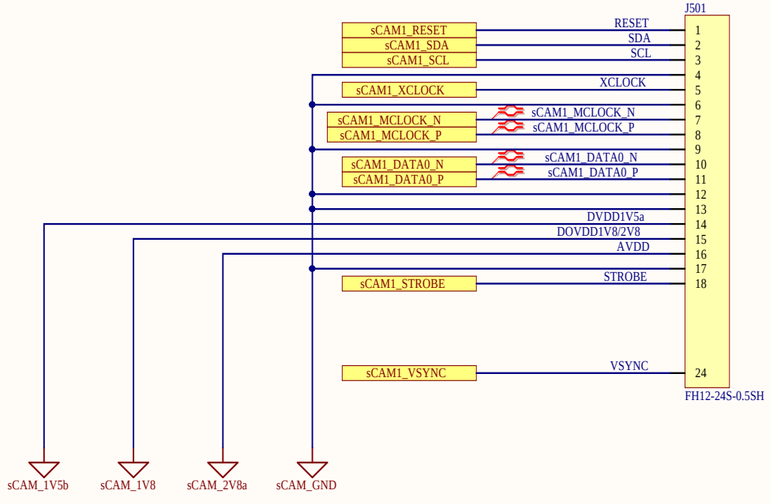


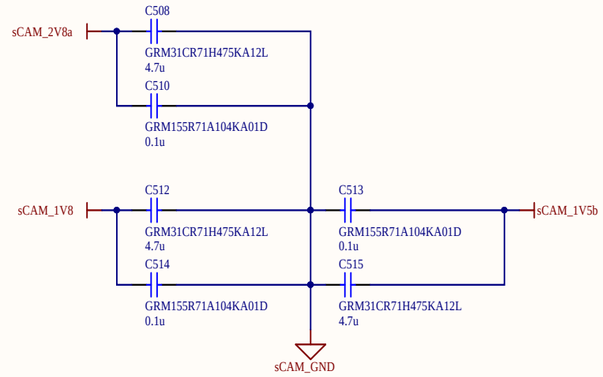
Layout Notes: The capacitor should be placed as close to the Camera 1 connector as possible.



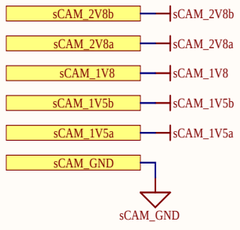
KLT-KR7-OV7251
CAMERA 0

Layout Notes: The capacitor should be placed as close to the Camera 2 connector as possible.





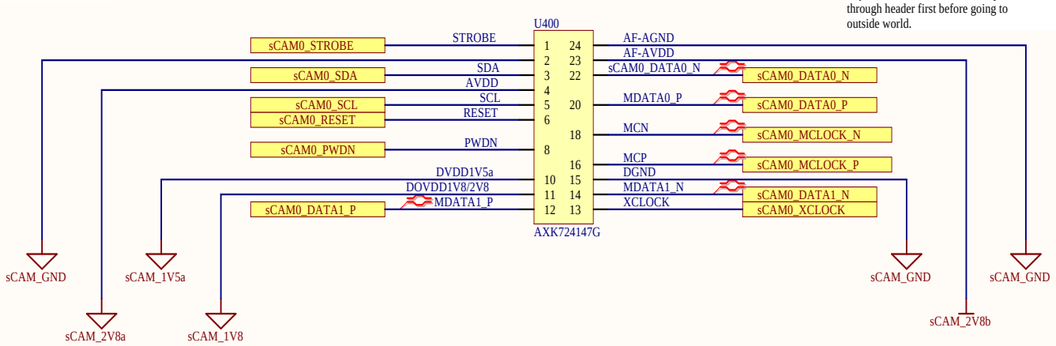
KLT-KR7-OV7251
CAMERA 1



Title: ViSOR		
Size: Tabloid	Number: KLT-KR7-OV7251	Revision: 2.0.0
Date: 9/24/2018		Sheet of
File: C:\Users\..\ klt-kr7-ov7251.SchDoc		Drawn By:

LI-OV5640-MIPI-AF
CAMERA 0

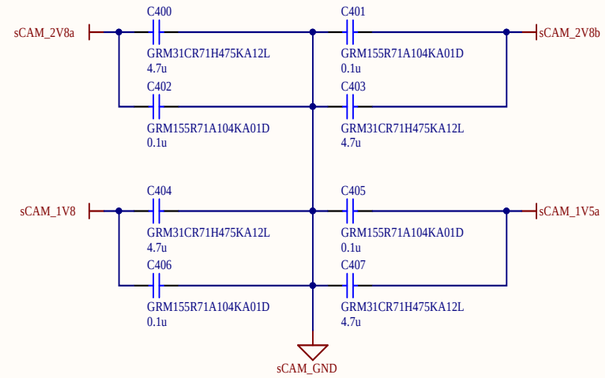
Layout Notes: Make sure traces pass through header first before going to outside world.



Layout Notes: Make sure traces pass through header first before going to outside world.

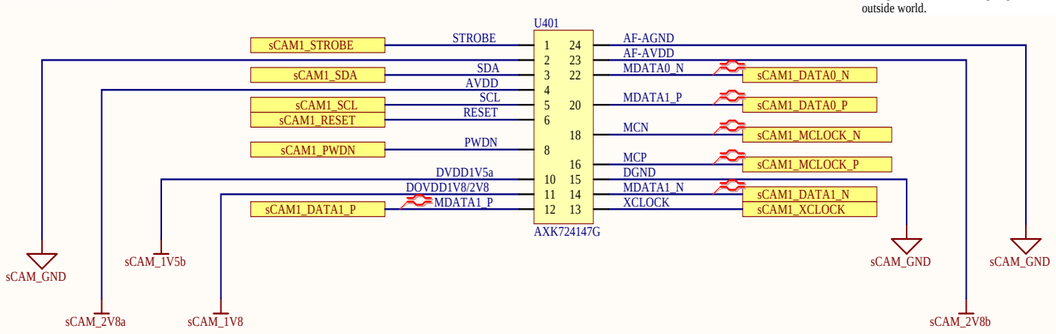
```
PINS 2 & 24 ARE AGND.  UNIFIED GROUND SCHEME.

PIN  1: STROBE OUTPUT
PIN  4: AVDD (ANALOG POWER)
PIN  6: RESET INPUT (ACTIVE HIGH)
PIN 10: DVDD 1.5V (POWER FOR DIGIAL CORE)
PIN 11: D0VDD (1.8V/2.8V) - SUPPLIED FROM DRAGONBARD (VDD_1V8)
PIN 13: XCLK INPUT (SYSTEM INPUT CLOCK)
PIN 23: AF-VCC2.8V (POWER FOR ANALOG CIRCUIT)

2V8a : 2.8 V for Analog Power
2V8b : 2.8 V for Auto Focus

1V5a : 1.5 V for Camera 1
1V5b : 1.5 V for Camera 2
```

Layout Notes: The capacitor should be placed as close to the Camera 1 connector as possible.

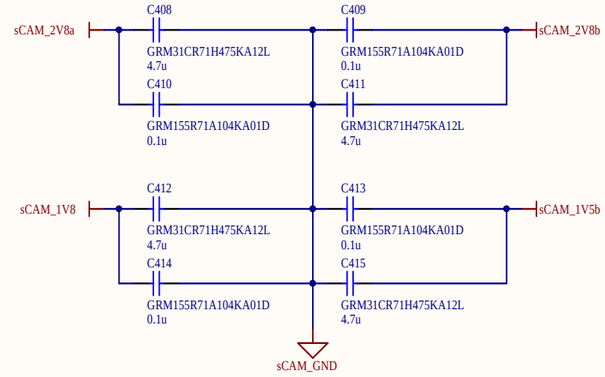


Layout Notes: Make sure traces pass through header first before going to outside world.

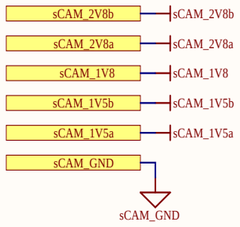
Layout Notes: Make sure traces pass through header first before going to outside world.



Layout Notes: The capacitor should be placed as close to the Camera 2 connector as possible.

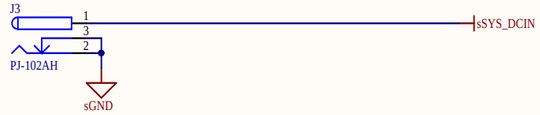


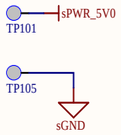
LI-OV5640-MIPI-AF
CAMERA 1



Title: ViSOR		
Size: Tabloid	Number: LI-OV5640-MIPI-AF	Revision: 2.0.0
Date: 9/24/2018		Sheet of
File: C:\Users\..\ li-ov5640-mipi-af.SchDoc		Drawn By:

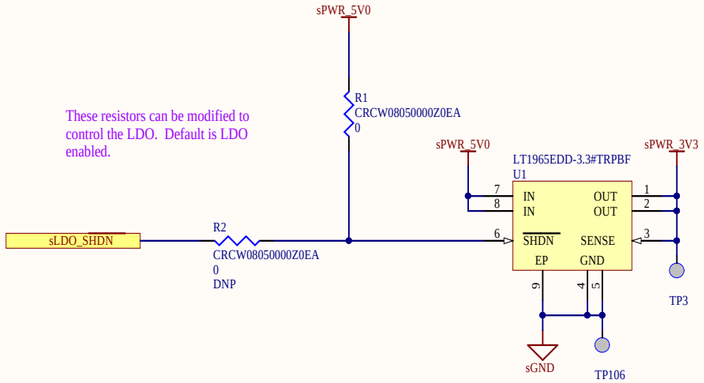
The resistor can be used to determine current draw. The current is 0.1V/A.

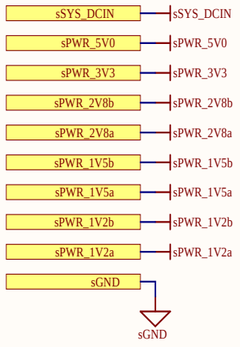




Layout Notes: Position the test points in close proximity to each other.

These resistors can be modified to control the LDO. Default is LDO enabled.





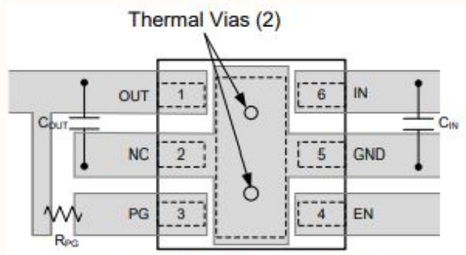
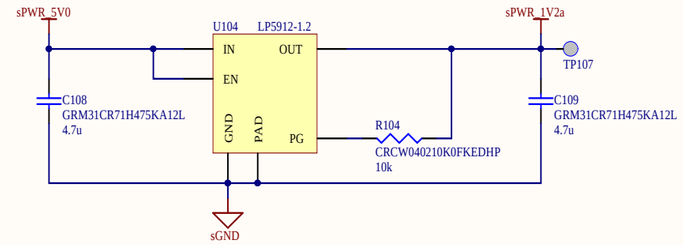
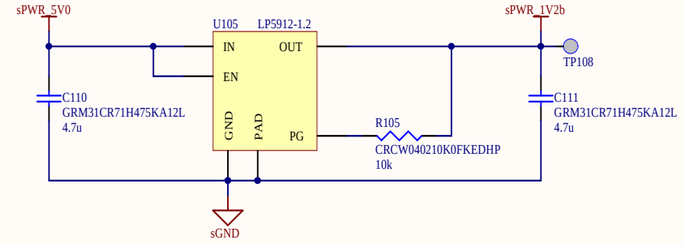


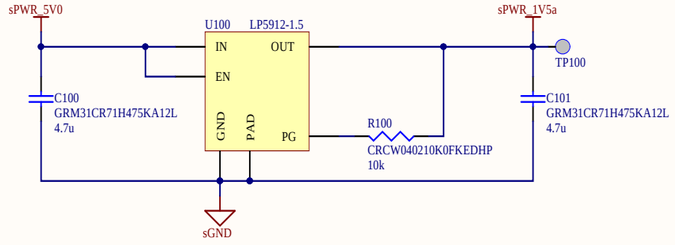
Figure 57. LP5912 Typical Layout



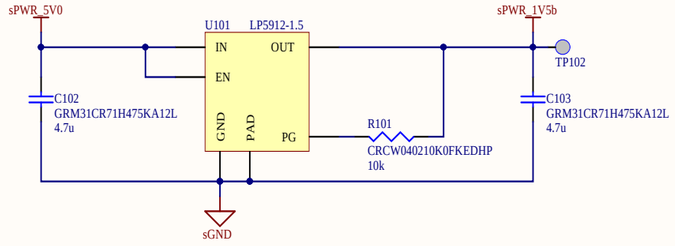
Layout Notes: The capacitors should be placed as close to the chip as possible.



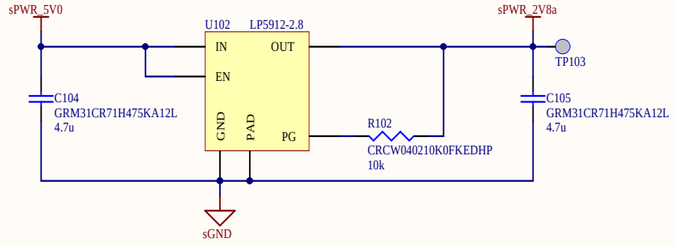
Layout Notes: The capacitors should be placed as close to the chip as possible.



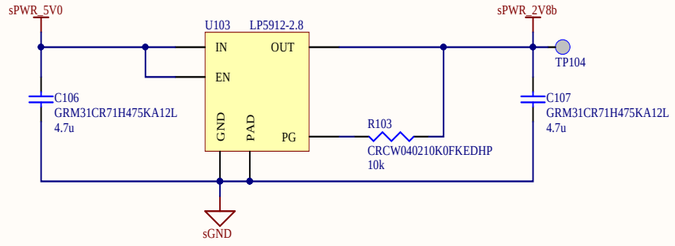
Layout Notes: The capacitors should be placed as close to the chip as possible.



Layout Notes: The capacitors should be placed as close to the chip as possible.

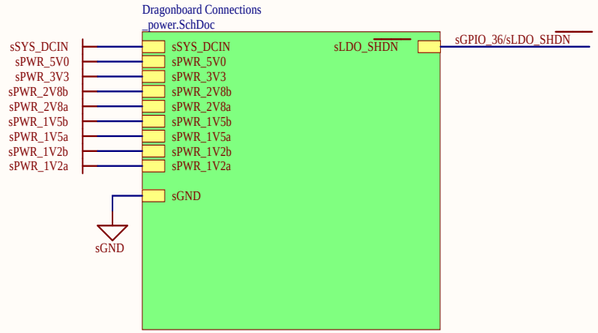


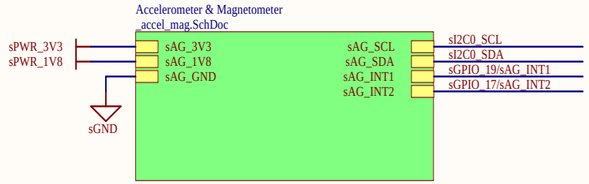
Layout Notes: The capacitors should be placed as close to the chip as possible.

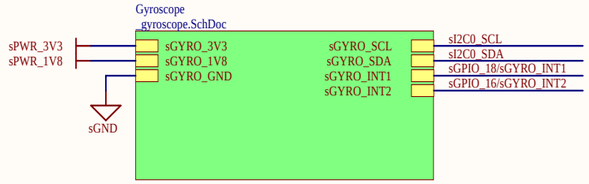


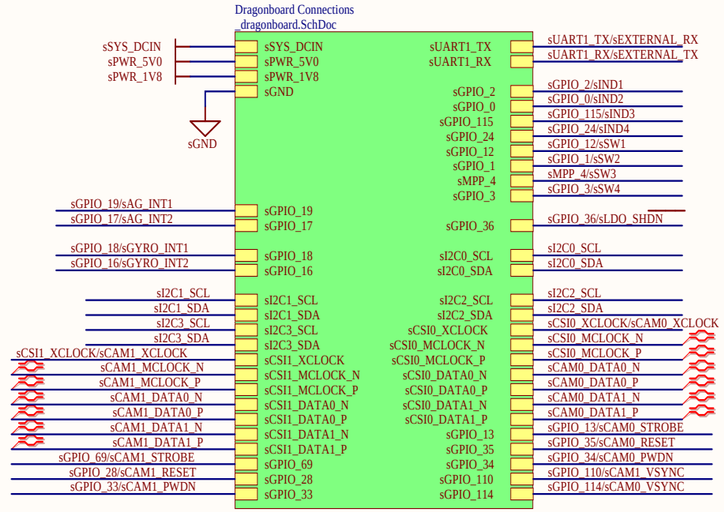
Layout Notes: The capacitors should be placed as close to the chip as possible.

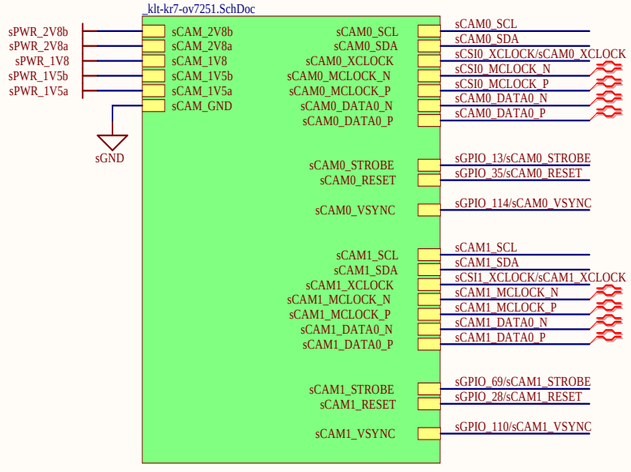
Title: ViSOR		
Size: Tabloid	Number: Power	Revision: 2.0.0
Date: 9/24/2018	Sheet of	
File: C:\Users\.._power.SchDoc	Drawn By:	

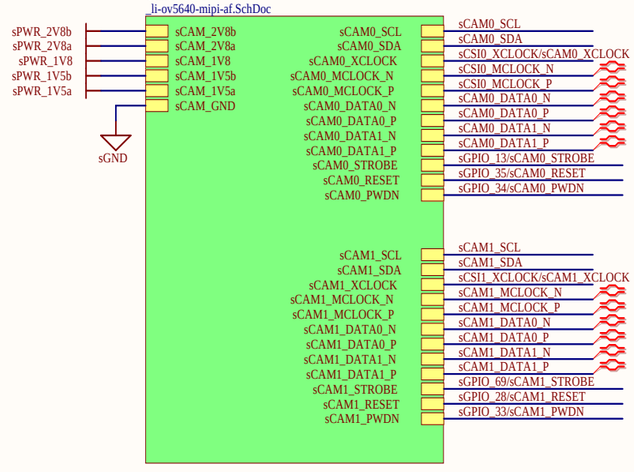


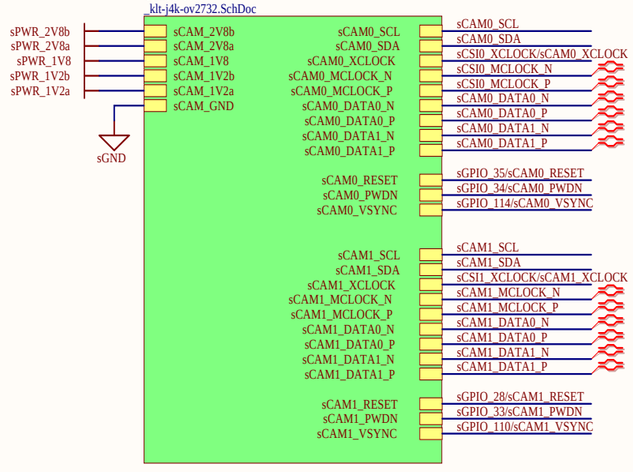


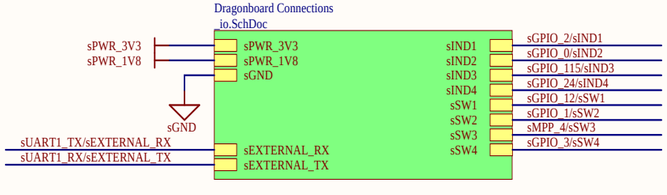


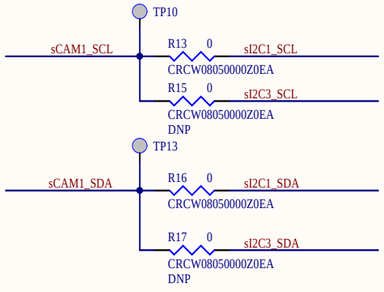






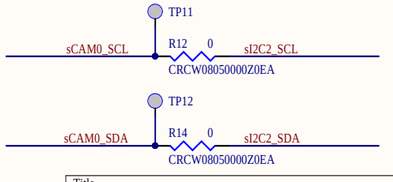






CAMERA 0 is always connected to HIGH speed header.

CAMERA 1 is default connected to the LOW speed header. To connect it to the HIGH speed header, change the resistors to disconnect it from I2C1 and connect it to I2C3.



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