



Screen shot of table in catapult






Report: General						
Solution /	Latency...	Latency...	Throug...	Throug...	Slack	Total Area
solution.v1 (new)						
EdgeDetect_Top.v1 (extract)	7	70.00	6	60.00	7.30	12198.08

Report: <a href="#">Run Time</a>														
Solution /	Total	Analyze	Compile	Libraries	Assembly	Loops	Memories	Cluster	Architect	Allocate	Schedule	Dpfsm	Instance	Extract
<a href="#">Solution.v1 (new)</a>														
<a href="#">EdgeDetect_Top.v1 (extract)</a>	52.71	7.61	7.51	0.64	0.87	2.21	1.29	0.10	1.24	1.24	9.54	4.34	6.11	10.01

Report: **Memory Usage**

Solution	Peak
 solution.v1 (new)	
 <b>EdgeDetect_Top.v1 (extract)</b>	<b>1748180.00</b>

[illegible]

Report: <span style="background-color: #ADD8E6;">Area Score</span>													
Solution /	Registers	MUX	Functional	Logic	Memory	FSM Reg	FSM Comb	FSM	Datapath	Total Reg	Total Area	Rom	
 solution.v1 (new)													
 <b>EdgeDetect_Top.v1 (extract)</b>	<b>6312.98</b>	<b>2028.46</b>	<b>1726.68</b>	<b>2077.37</b>	<b>0.00</b>	<b>48.00</b>	<b>4.60</b>	<b>52.60</b>	<b>12145.48</b>	<b>6360.98</b>	<b>12198.08</b>	<b>0.00</b>	
 allocate	5027.40	1517.89	1124.88	140.21	0.00	0.00	0.00	0.00	7810.37	5027.40	7810.37	0.00	
 dpfsm	6320.16	2444.26	1842.01	2019.53	0.00	48.00	3.00	51.00	12625.96	6368.16	12676.96	0.00	
 extract	6312.98	2028.46	1726.68	2077.37	0.00	48.00	4.60	52.60	12145.48	6360.98	12198.08	0.00	