# Università degli Studi di Firenze





# Facoltà d' Ingegneria Dipartimento di Elettronica e Telecomunicazioni

# Sintetizzatori a conversione diretta D/A (DDS)

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#### **PLL versus DDS**



#### **Sintetizzatore DDS:**

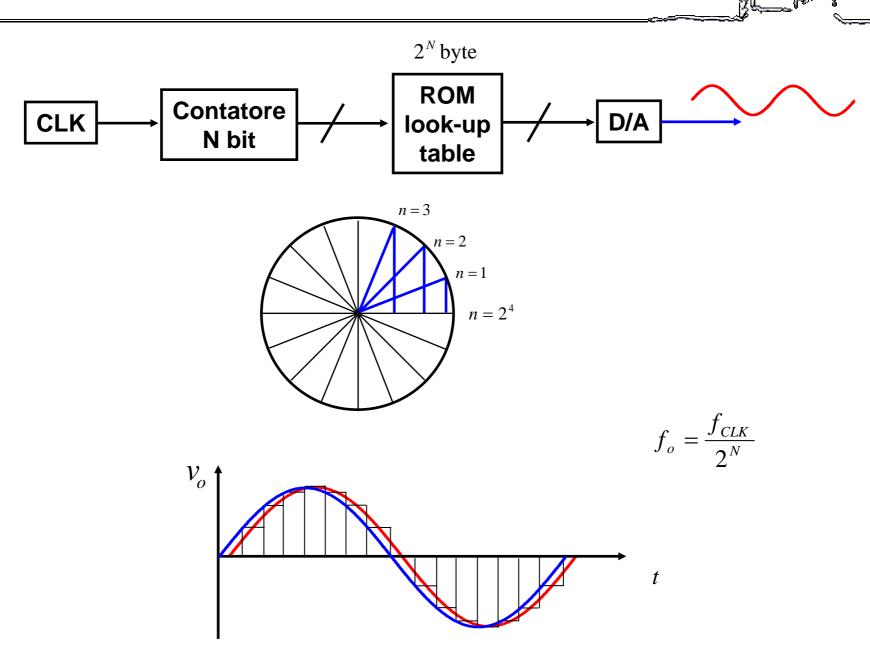
hopping time breve (ns)

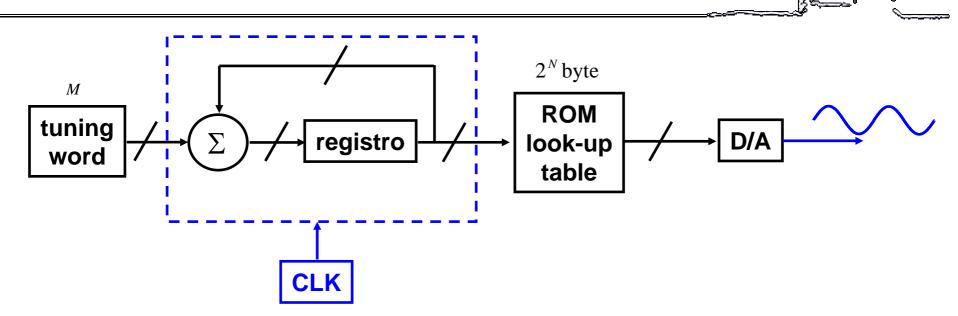
risoluzione elevata

μ**Hz** 

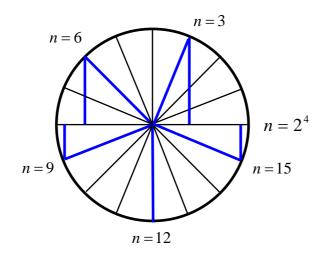
bassa frequenza <200MHz

spurie, repliche, jitter





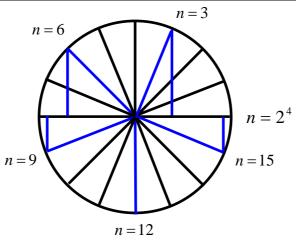
M = 3



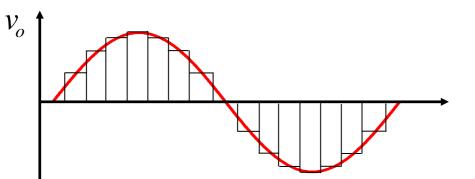
$$f_o = M \frac{f_{CLK}}{2^N}$$

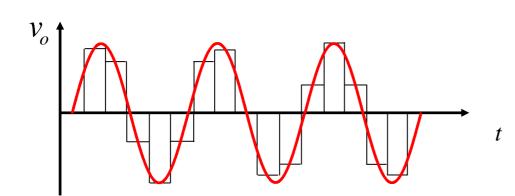


M = 3



$$f_o = M \frac{f_{CLK}}{2^N}$$





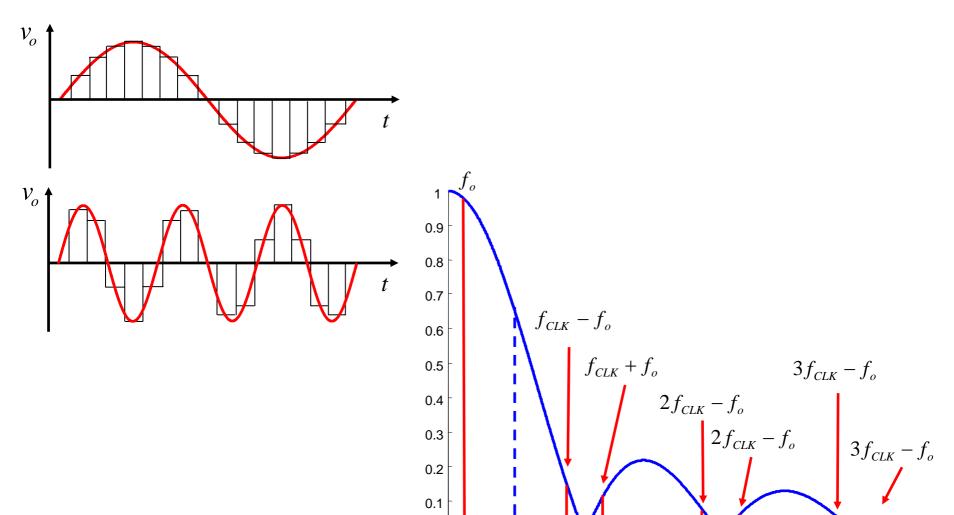
# Frequenze immagine



 $3f_{CLK}$ 

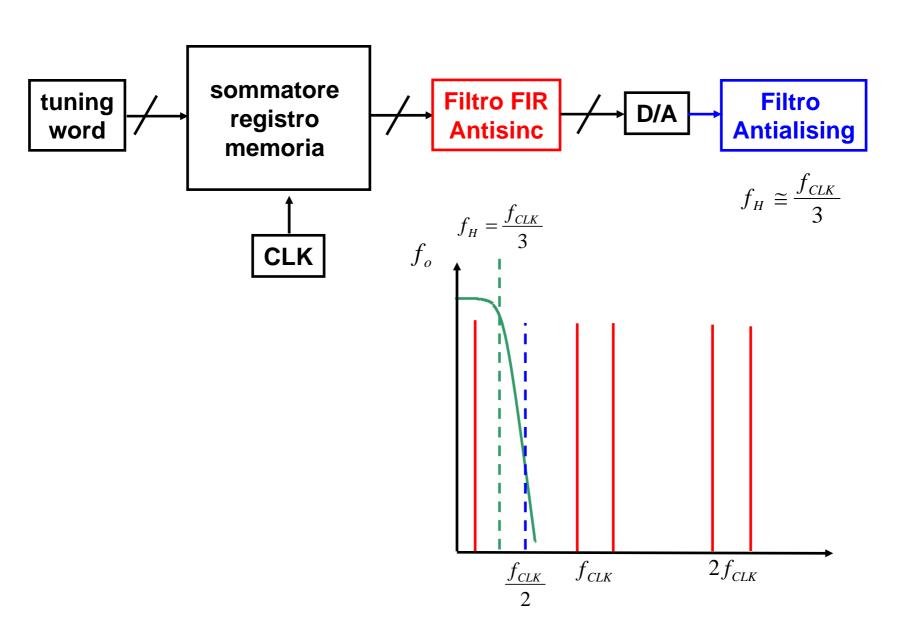
 $2f_{\mathit{CLK}}$ 

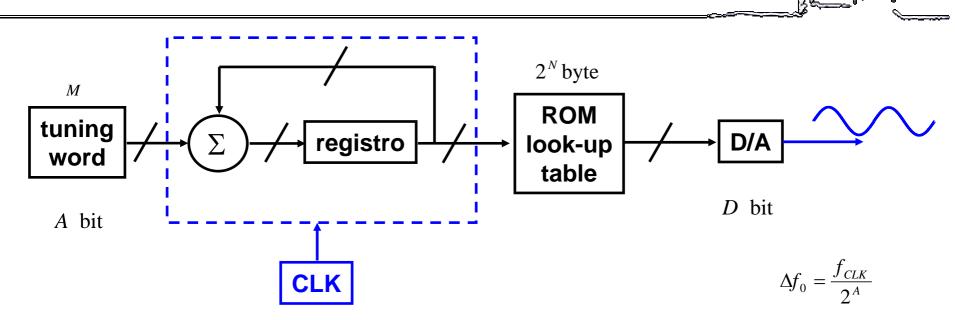
 $f_{\mathit{CLK}}$ 



0



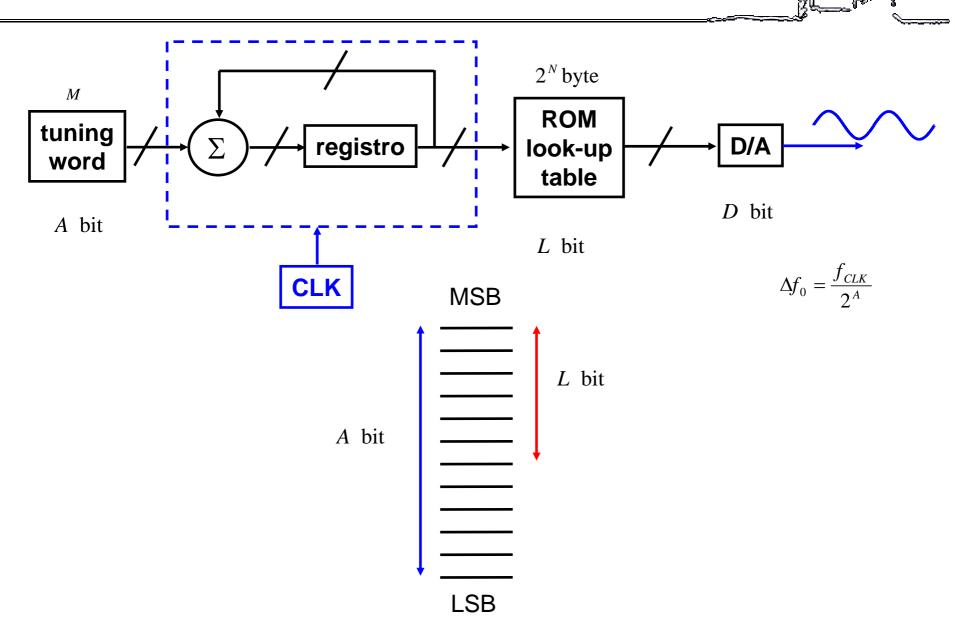


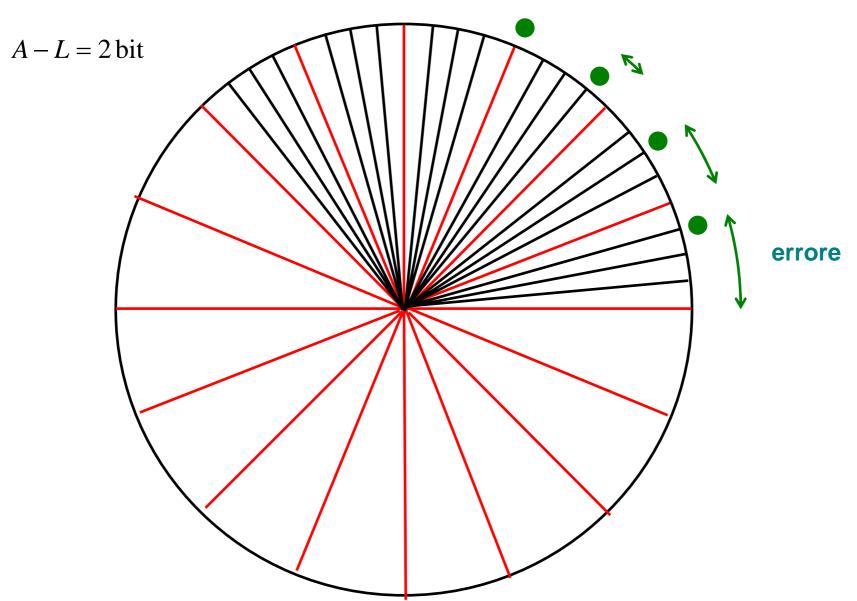


### Esempio:

$$f_{CLK} = 300 \text{MHz}$$
  
 $D = 12 \text{bit}$   
 $A = 48 \text{bit}$   
 $\Delta f_o = 1 \mu \text{Hz}$   
 $ROM = 2^{48} \times 12 = 4000 \text{ Tbit } !!!!$ 

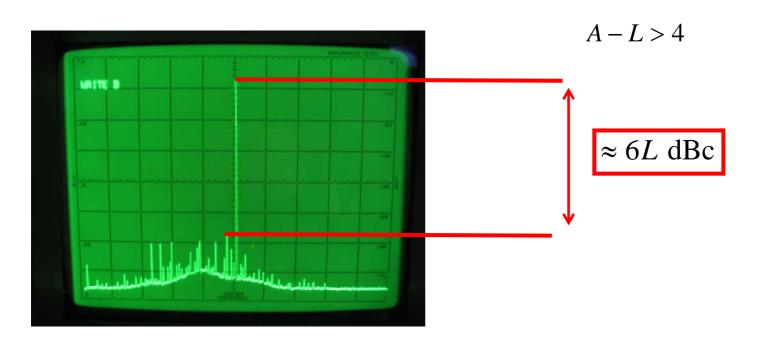
#### **Troncamento**





L'errore di quantizzazione è periodico e produce spurie

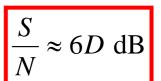


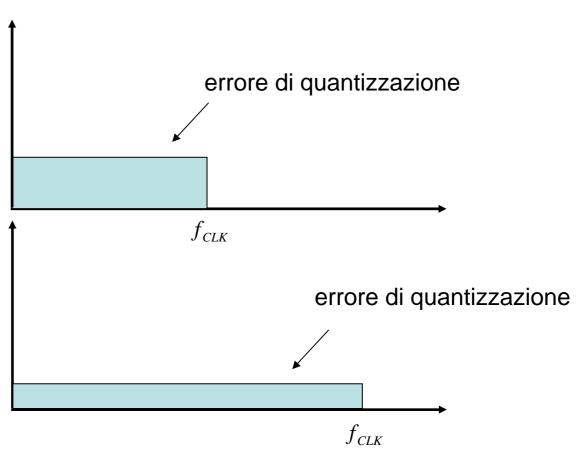


L = numero di bit della look up table

## Errore di quantizzazione







frazione di fondo scala (<1)

$$\frac{S}{N} \approx 6D \text{ dB} + 20\log_{10}(FFS)$$