



# Antonio Rapuano

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**Date of birth:** 31/10/1999 **Nationality:** Italian

## EDUCATION AND TRAINING

[ 09/2022 – Current ]

### M. Eng. equivalent, Control Engineering

**Università degli Studi di Roma "La Sapienza"** <https://www.uniroma1.it/>

**City:** Roma | **Country:** Italy | | **Final grade:** 110/110 cum laude (expected) | **Level in EQF:** EQF level 7 | **Type of credits:** CFU/ECTS | **Number of credits:** 120

- modeling and control of nonlinear systems, hybrid systems, multi-agent systems,
- estimation,
- optimal control,
- robust control,
- basic and advanced robotics

[ 09/2018 – 02/2022 ]

### B. Eng. equivalent, Electronic Engineering for Automation and Telecommunications

**Università degli Studi del Sannio** <https://www.unisannio.it/>

**City:** Benevento | **Country:** Italy | | **Final grade:** 110/110 cum laude | **Level in EQF:** EQF level 6 | **Type of credits:** CFU/ECTS | **Number of credits:** 180

- modeling and control of linear systems
- analog and digital electronics
- signal processing
- telecommunications

## LANGUAGE SKILLS

**Mother tongue(s):** Italian

**Other language(s):**

**English**

**LISTENING** C2 **READING** C2 **WRITING** C2

**SPOKEN PRODUCTION** C1 **SPOKEN INTERACTION** C1

*Levels: A1 and A2: Basic user - B1 and B2: Independent user - C1 and C2: Proficient user*

## SKILLS

### Modeling and simulation tools

Fusion 360 | LTspice | Stateflow | Simulink

### Programming and scripting languages

Arduino | C | Java | LaTeX | MATLAB | Python | Verilog HDL

### Design and productivity software

Adobe Illustrator | Adobe Photoshop | Blender | Microsoft Office

### Other technical skills

Circuit board soldering | FPGA design

### Personal skills

Attention to detail | Autonomy and initiative | Critical thinking | Curiosity and continuous learning | Problem-solving attitude | Project planning | Technical writing | Time management