VIETNAM NATIONAL UNIVERSITY HO CHI MINH CITY
HO CHI MINH CITY UNIVERSITY OF TECHNOLOGY
FACULTY OF COMPUTER SCIENCE AND ENGINEERING

REPORT
COMPUTER ENGINEERING PROJECT

Develop a RISC-V-based processor on FPGA and testing applications

Major: Computer Engineering

COMPUTER ENGINEERING PROJECT: COMMITTE 7 COMPUTER ENGINEERING
SUPERVISOR: PHẠM QUÓC CƯỜNG
MEMBER SECRETARY: HUỲNH PHÚC NGHỊ

STUDENT 1: NGUYỄN TRẦN PHƯỚC THÀNH (1912059)

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