

NGUYỄN THANH TOÀN

FRESHER ELECTRICAL ENGINEER



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- 🔗 Github: <https://github.com/nttoan-khiem>

PROFILE

A motivated electrical engineering graduate specializing in RTL design and FPGA development, with a strong interest in AI acceleration. Experienced in implementing neural networks such as MobileNet on FPGA for real-time traffic jam detection. Passionate about building efficient AI hardware systems and eager to contribute to innovative AI-on-FPGA solutions as a junior RTL/FPGA engineer.

SKILLS

- Programming language: Assembly, C/C++, Python
- Hardware description languages: Verilog/System Verilog
- FPGA Tools: Quartus
- Debug tools: Modelsim
- Problem-Solving
- English skill: Can read English document, Communication
- OS: Windows, Linux

EDUCATION

VIETNAM NATIONAL UNIVERSITY - HO CHI MINH CITY
UNIVERSITY OF TECHNOLOGY

GPA: 7.69/10 | START YEAR: 2020

PROJECT

- Implement model Mobilenet on FPGA to detect traffic jam - Graduated project - Github link: <https://github.com/nttoan-khiem/MobileNetOnFPGA>
- Smart home control device by voice in technical festival contest 2020 - Member responsible for hardware and software design - won 3rd place in the competition
- Security door in cockpit airbus A320 in subject embedded system design - Team leader.
- SRAM IC design in subject digital IC design Neural network implemented on FPGA
- Designed a 32-bit RISC-V microcontroller - Team leader
- Design Audio Equalizer using FIR filter implement on FPGA - Member responsible for designing FIR filter and implementing communication protocol with codec.

PERSONAL DETAILS

- Date of birth: 25, Oct 2002
- Gender: Male
- Nationality: Viet Nam
- Identification ID: 086202010192
- Health: No problem

INTEREST

- System design
- Playing Piano