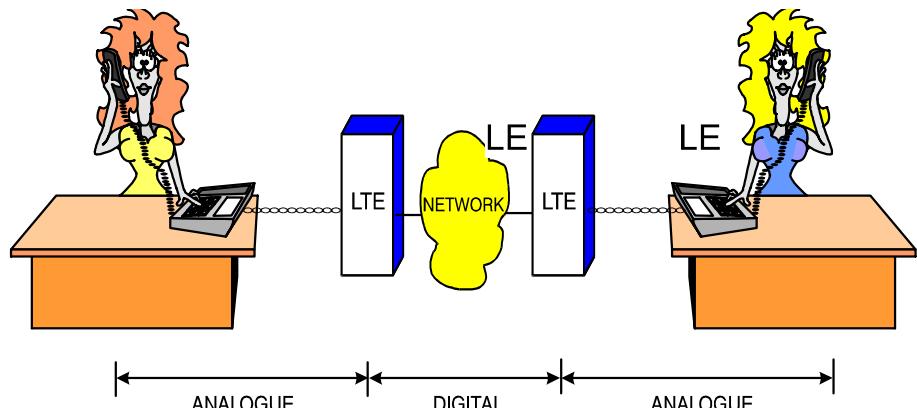




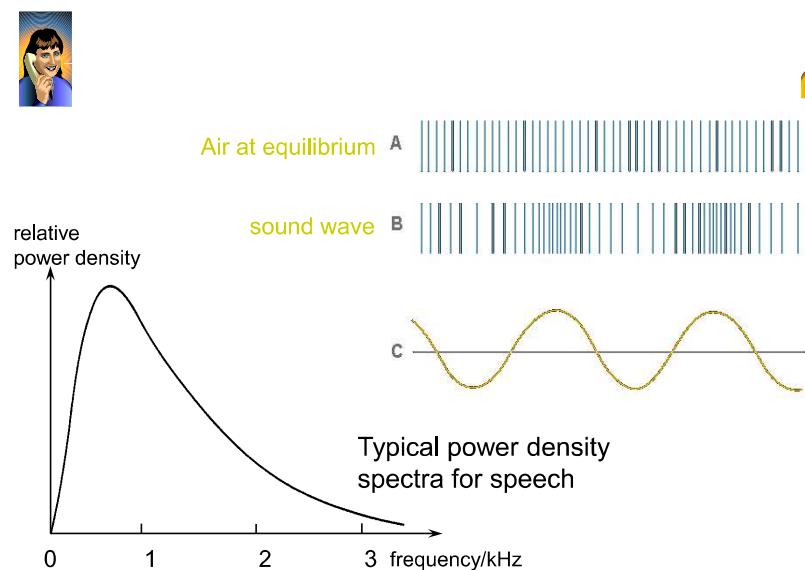
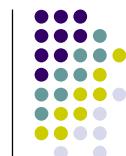
The Telephone System

COMMUNICATION SYSTEM REVIEW

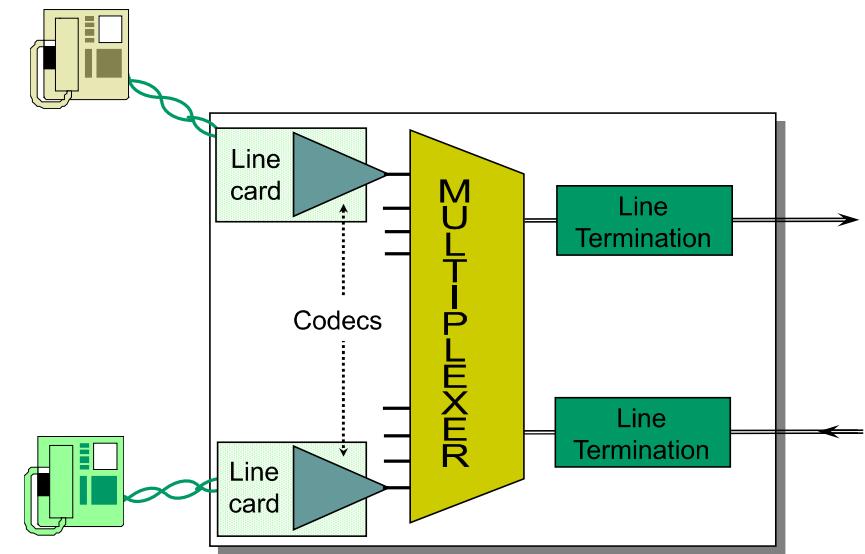
DIGITAL TELEPHONY



Power in the Human Voice

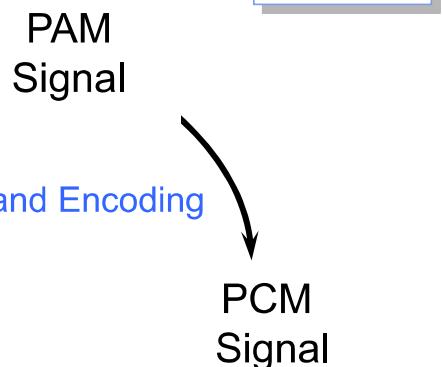


Local Exchange



The CODEC

Analogue Signal
Sampling

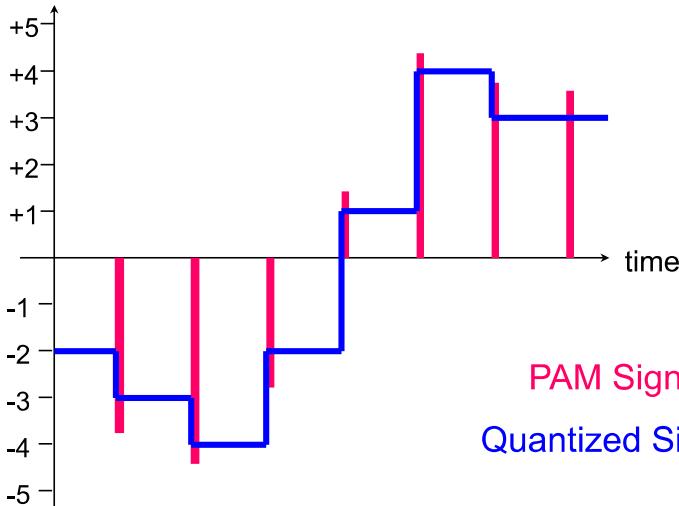


PAM Signal

Quantization and Encoding

PCM Signal

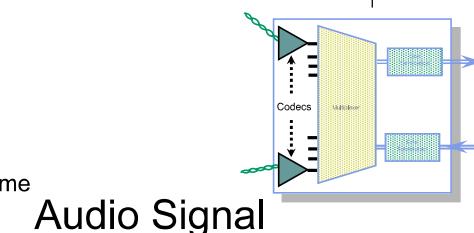
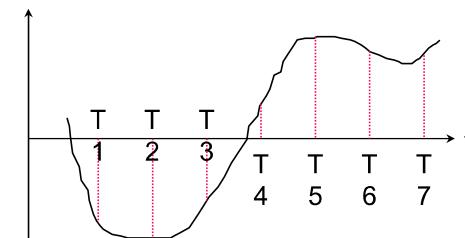
Linear Quantization



PAM Signal

Quantized Signal

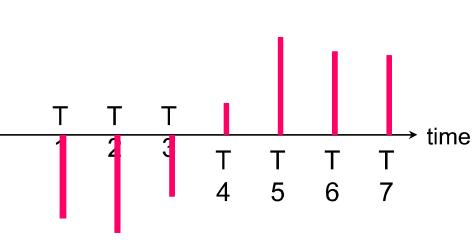
Sampling



Audio Signal

Sampler Output

Pulse Amplitude Modulated (PAM) signal



Non-Linear Quantization & Encoding

Quantization Level digital codes

112	1 1 1 1 X X X X	1/2V
96	1 1 1 0 X X X X	1/4V
80	1 1 0 1 X X X X	1/8V
64	1 1 0 0 X X X X	1/16V
48	1 0 1 1 X X X X	1/32V
32	1 0 1 0 X X X X	1/64V
16	1 0 0 1 X X X X	-V
8	1 0 0 0 X X X X	+V
4	0 0 0 0 X X X X	
2	0 0 0 1 X X X X	
1	0 0 1 0 X X X X	

In accordance with CCITT's A-law

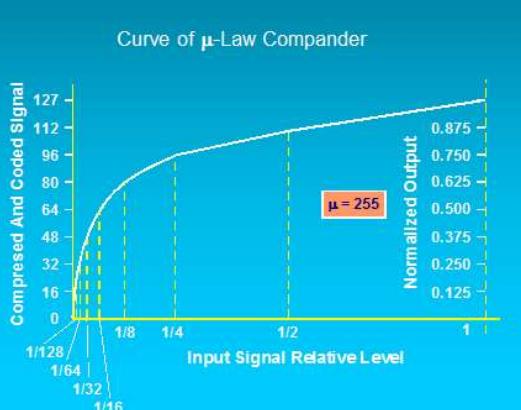
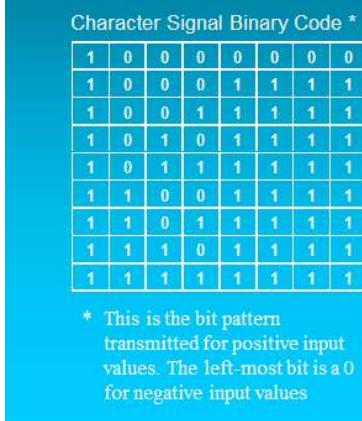
CODEC Quantizing - USA

The North American standard for assigning and decoding a signal's amplitude is μ -law 255. This law defines how many quantizing levels are used and how they are arranged

The Formula

$$F_\mu(\chi) = \text{sgn}(\chi) \frac{\ln(1+\mu|\chi|)}{\ln(1+\mu)}$$

$\mu = 255$ (compression parameter)



CODEC Quantizing - Europe

The European standard is the A-law and its formula is:

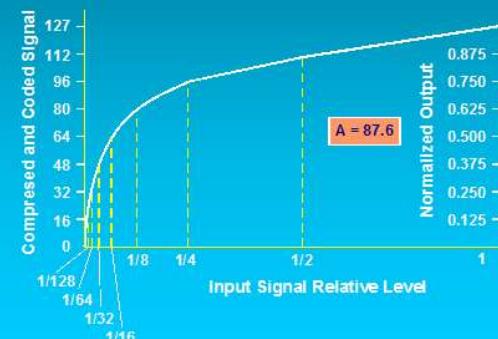
$$F(\chi) = \text{sgn}(\chi) \frac{A|\chi|}{1 + \ln(A)} \text{ when } 0 \leq |\chi| < \frac{1}{A} \text{ and } F(\chi) = \text{sgn}(\chi) \frac{1 + |nA|\chi|}{1 + \ln(A)} \text{ when } \frac{1}{A} \leq |\chi| \leq 1$$

Character Signal Binary Code *

1	1	1	1	1	1	1	1	1
1	1	1	1	0	0	0	0	0
1	1	1	0	0	0	0	0	0
1	1	0	1	0	0	0	0	0
1	1	0	0	0	0	0	0	0
1	0	1	1	0	0	0	0	0
1	0	1	0	0	0	0	0	0
1	0	0	1	0	0	0	0	0
1	0	0	0	0	0	0	0	0

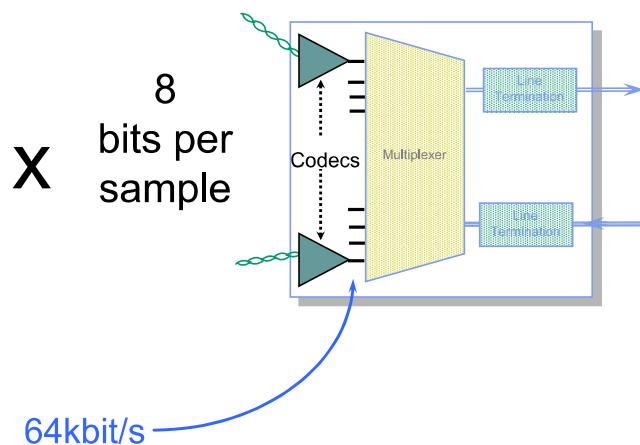
* For positive input values. The left-most bit is a 0 for negative input values. Even bits (beginning with 1 at the left) are inverted before transmission

Curve of A-Law Compressor

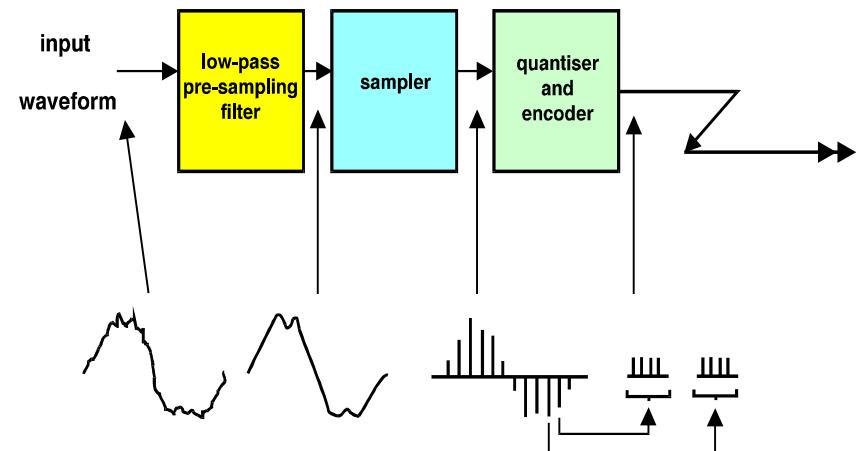


PCM Signal Data Rate

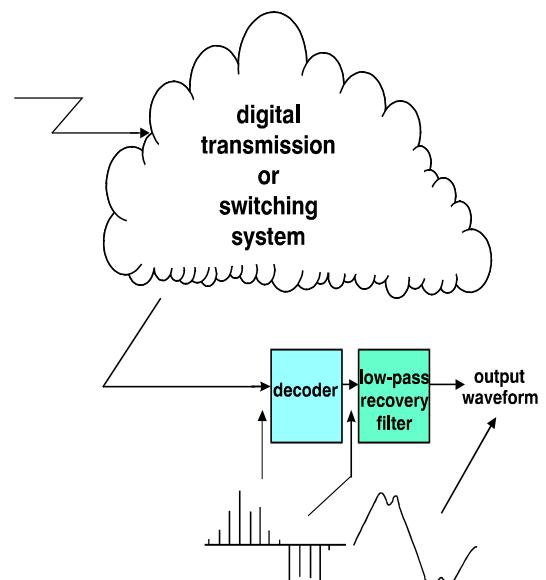
8000 samples per sec \times 8 bits per sample



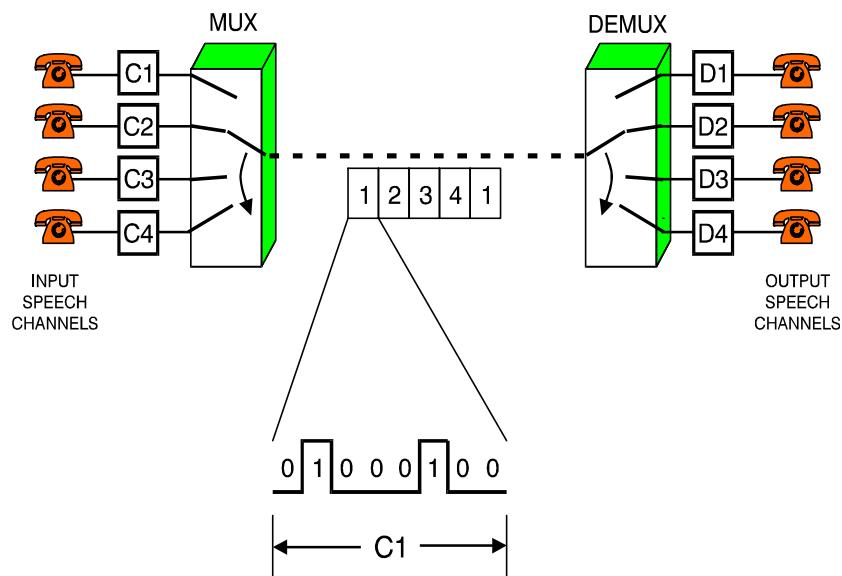
The Processes of PCM ?



The Processes of PCM ?

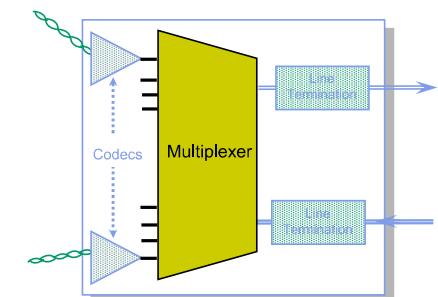


Time Division Multiplexing (TDM)

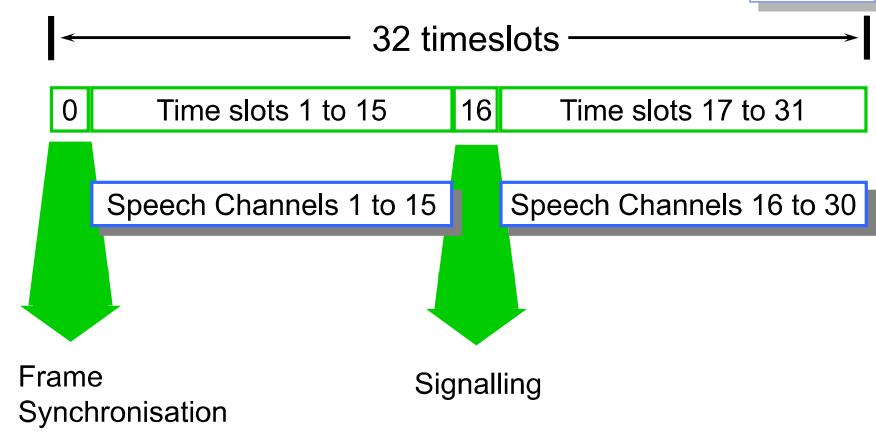


Digital Multiplexing

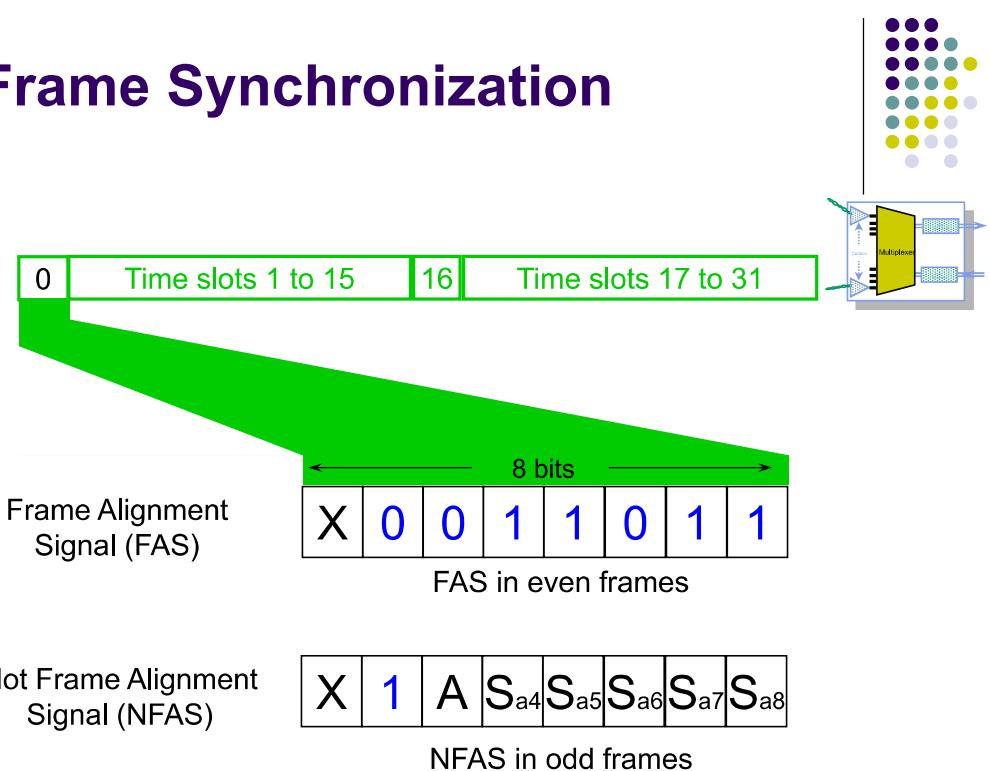
- Time Division Multiplexing
- 2Mbit/s PCM Frame
- Digital Hierarchy



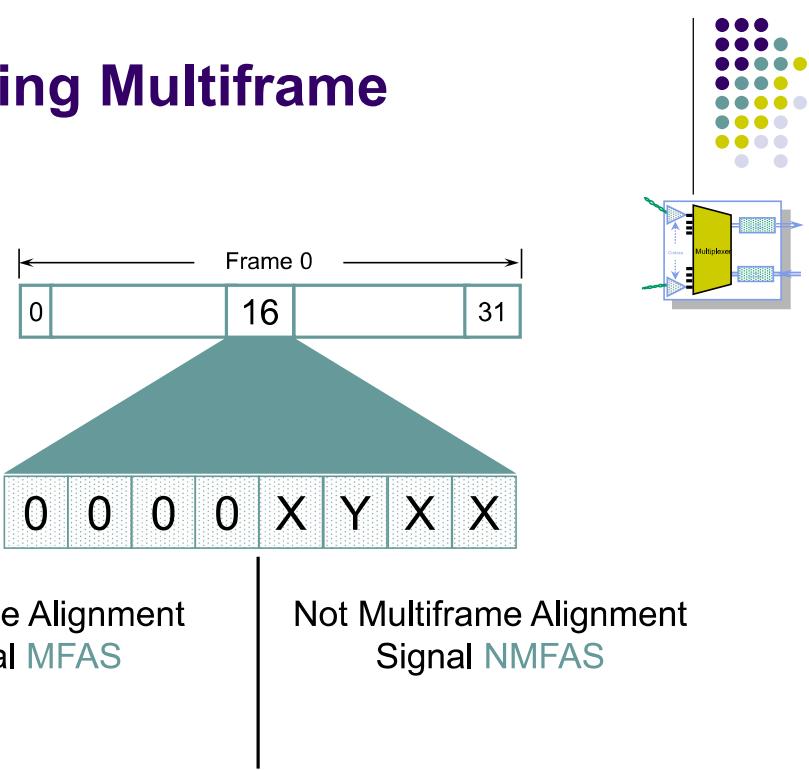
The 2Mbit/s PCM Frame



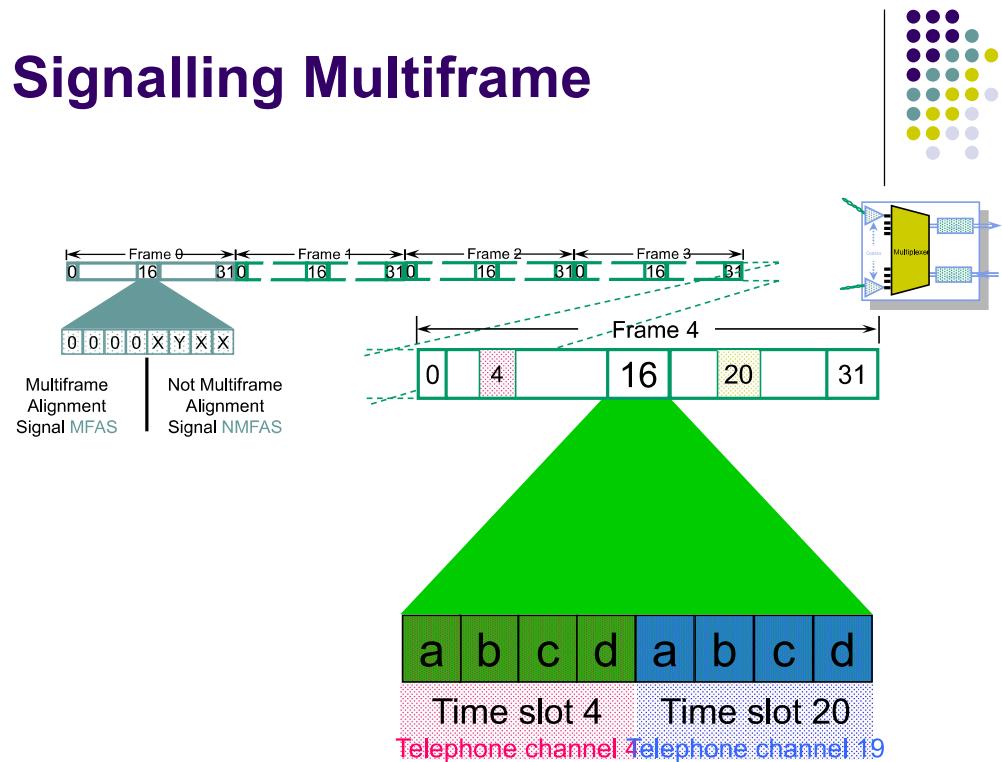
Frame Synchronization



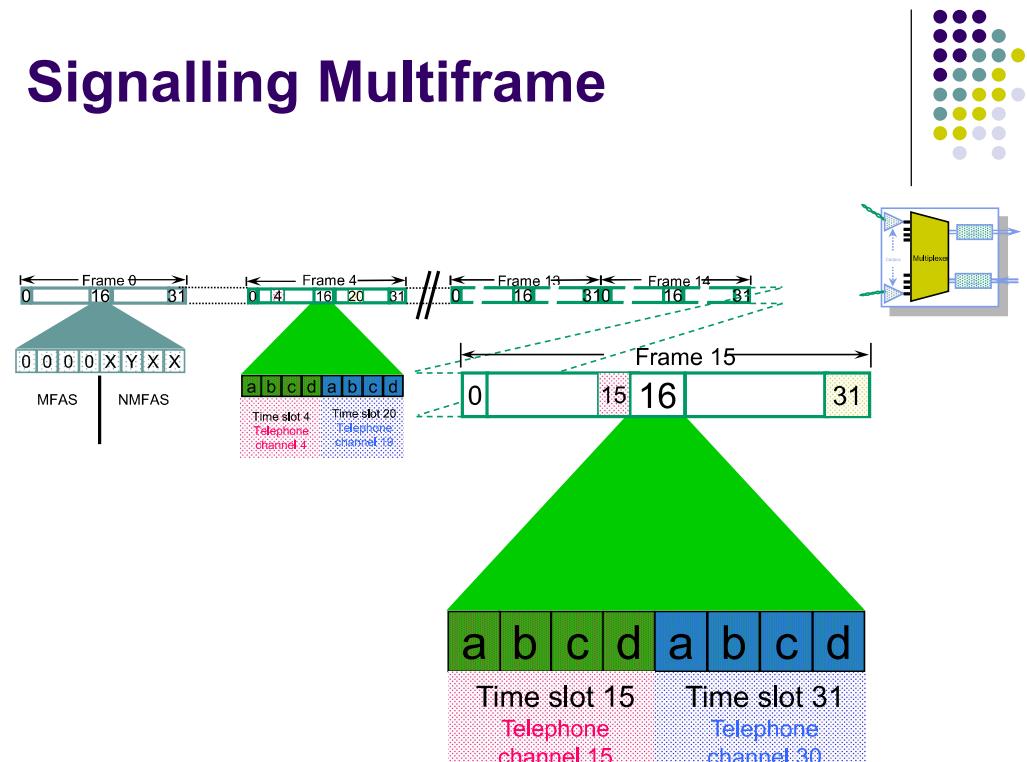
Signalling Multiframe



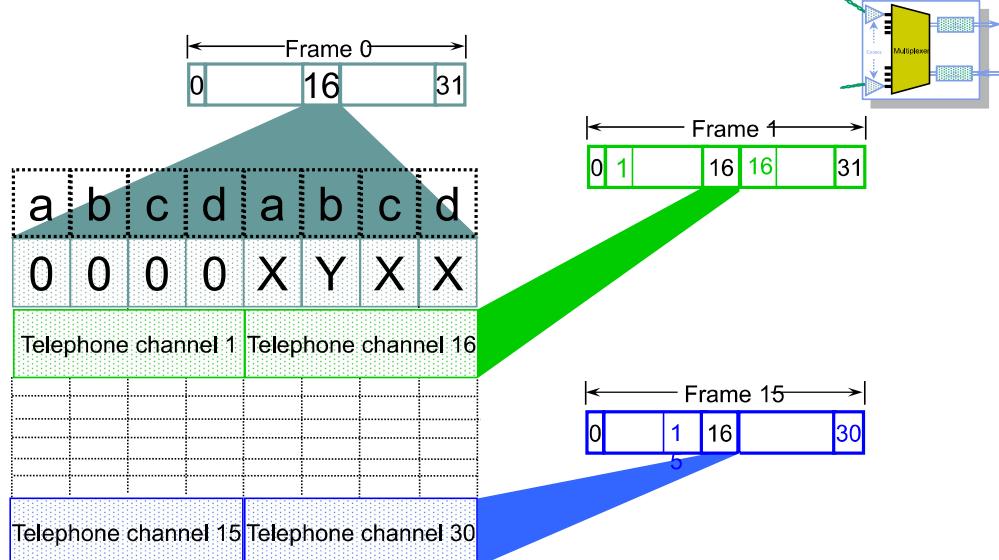
Signalling Multiframe



Signalling Multiframe



Signalling Multiframe



CAS Signalling

PCM frame number	Bits in time slot 16							
	a	b	c	d	a	b	c	d
0	0	0	0	0	X	Y	X	X
1							Telephone channel 16	
2							Telephone channel 17	
3							Telephone channel 18	
4							Telephone channel 19	
5							Telephone channel 20	
6							Telephone channel 21	
7							Telephone channel 22	
8							Telephone channel 23	
9							Telephone channel 24	
10							Telephone channel 25	
11							Telephone channel 26	
12							Telephone channel 27	
13							Telephone channel 28	
14							Telephone channel 29	
15							Telephone channel 30	

Cyclic Redundancy Check

CRC - 4 example

Data block $x^{24}:$

1 0 1 0 0 0 0 0

1 0 0 1 1

1 1 1 0 0

1 0 0 1

1 1 1 1

1 0 0 1

0

1 1 0

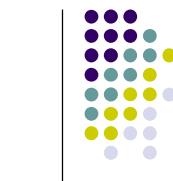
1

Generator polynomial

$$x^4 + x + 1$$

1 0 0 1 1

Remainder or
Signature



CRC-4 bits

0 Time slots 1 to 15 | 16 | Time slots 17 to 31

CRC check bits (remainder)

Frame Alignment
Signal (FAS)

8 bits

C 0 0 1 1 0 1 1

x

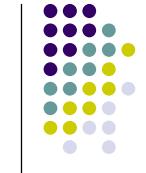
CRC Multiframe
Alignment Signal

001011

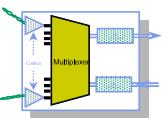
Not Frame
Alignment Signal
(NFAS)

X 1 A S_{a4} S_{a5} S_{a6} S_{a7} S_{a8}

CRC Multiframe



Sub-multiframe (SMF)	Frame number	Bits 1 to 8 of the frame							
		1	2	3	4	5	6	7	8
I	0	C ₁							
	1	0	1	A	S _n	S _n	S _n	S _n	FAS
	2	C ₂							
	3	0	1	A	S _n	S _n	S _n	S _n	FAS
	4	C ₃							
	5	1	1	A	S _n	S _n	S _n	S _n	FAS
	6	C ₄							
	7	0	1	A	S _n	S _n	S _n	S _n	FAS
II	8	C ₁							
	9	1	1	A	S _n	S _n	S _n	S _n	FAS
	10	C ₂							
	11	1	1	A	S _n	S _n	S _n	S _n	FAS
	12	C ₃							
	13	E	1	A	S _n	S _n	S _n	S _n	FAS
	14	C ₄							
	15	E	1	A	S _n	S _n	S _n	S _n	FAS



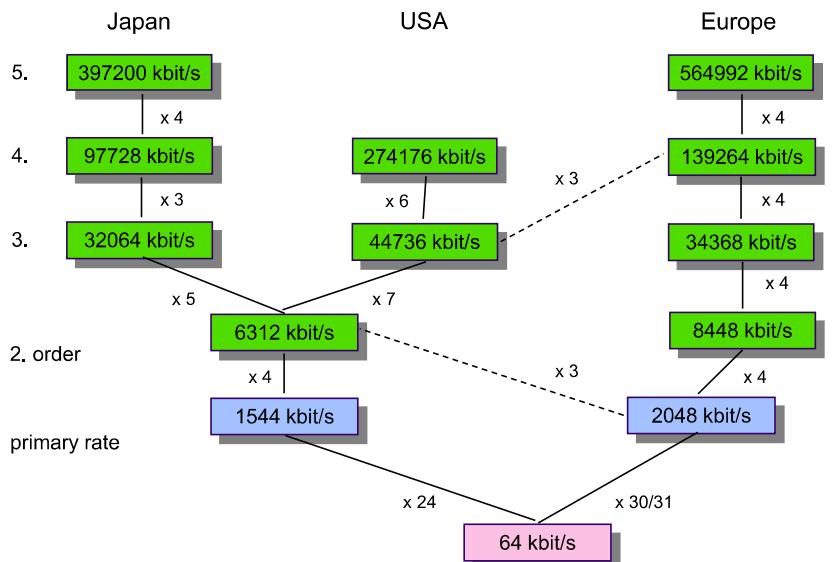
Comparison between E1 and T1



Common characteristics	PCM-30 and PCM-24
Sampling frequency	8kHz
Number of samples per telephone signal	8000
Length of PCM frame	125μs
Number of bits in each word	8
Telephone channel bit rate	64kbit/s

Differing characteristics	PCM 30	PCM 24
Encoding / decoding	A-law	μ-law
Number of segments in characteristic	13	15
Number of time slots per PCM frame	32	24
Number of bits per PCM frame	256	193
Length of an 8-bit time slot	approx. 3.9μs	approx. 5.2μs
Bit rate of time-division multiplexed signal	2048kbit/s	1544kbit/s

PDH Systems Worldwide

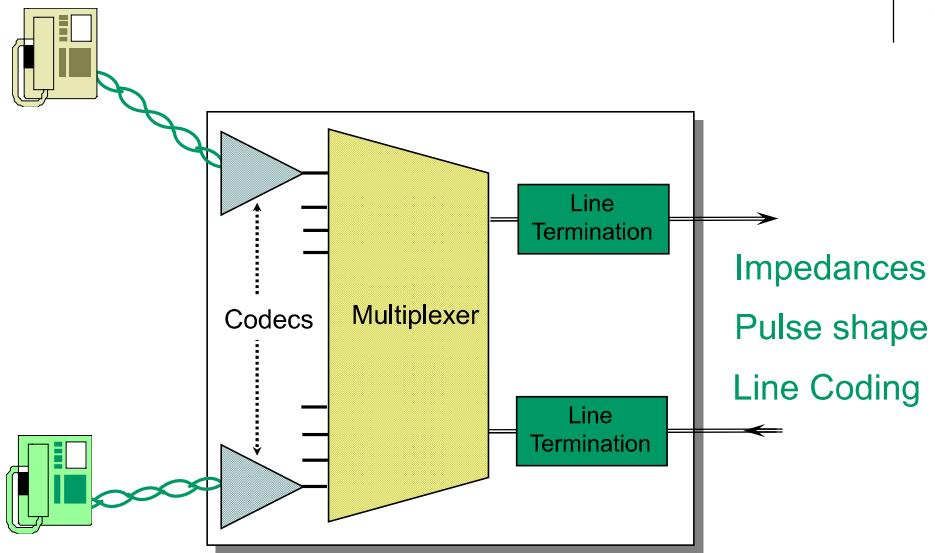


Primary Framing Structures



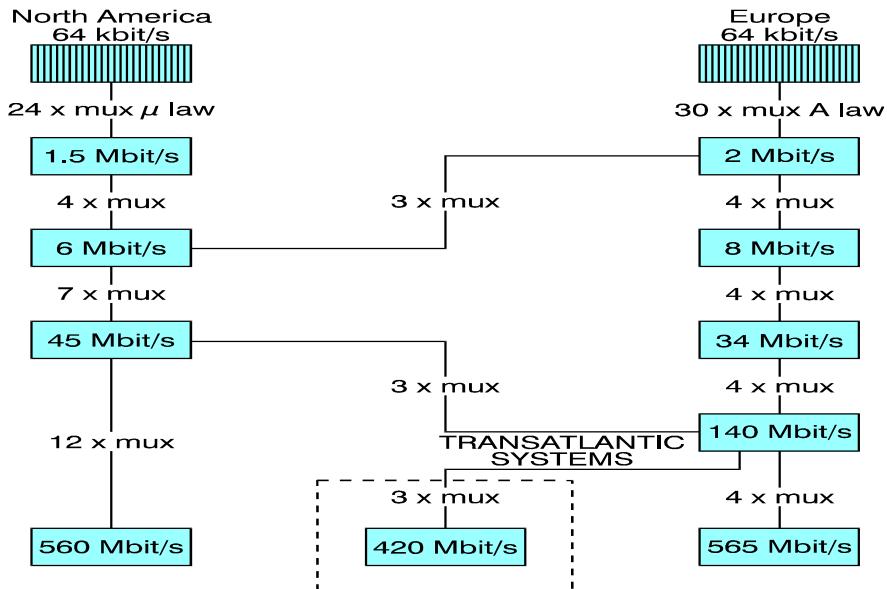
- **PCM30**
30 channels - CAS in TS16
- **PCM31**
31 channels - common channel signalling(CCS)
- **PCM30C**
30 channels - CAS in TS16 - CRC-4 error checking
- **PCM31C**
31 channels - CCS - CRC-4 error checking

Line Termination

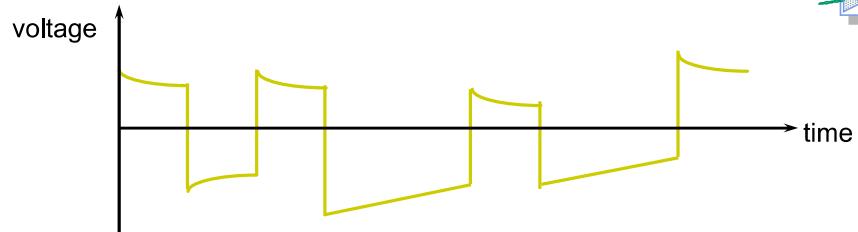


Physical/Electrical Characteristics - G.703

AMI

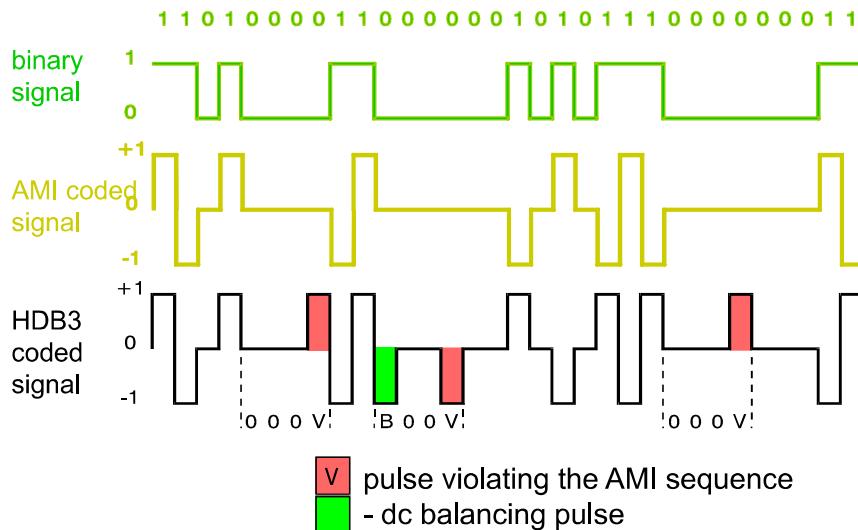


Line Codes



- Prevent “droop”
- Ensure sufficient timing information is included in the data

HDB3 Coding



What is E1?

- Serial synchronous bit stream at 2.048 Mbps
- Specifications defined in CCITT recommendations:
 - G.704/G.732 Frame definition (for framing over E1 and T1)
 - G.703 Interface physical specs (pulse mask, etc.)
 - G.823 Jitter requirements
- Interface (G.703) - two alternatives
 - 4-wire, balanced
120Ω, pulse = 3.00 volts nominal
 - 4-wire, unbalanced, 2 coaxial connectors
75Ω, pulse = 2.37 volts nominal

The Telephone System

