

# NGUYỄN THANH TOÀN

FRESHER ELECTRICAL ENGINEER



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## PROFILE

A highly motivated electrical engineering graduate with a strong foundation in RTL design, FPGA development, and digital IC design. Seeking an opportunity to apply my skills in a professional environment and grow as an RTL Design Engineer.

## SKILLS

- Programming language: Assembly, C/C++, Python
- Hardware description languages: Verilog/System Verilog
- FPGA Tools: Quartus
- Debug tools: Modelsim
- Problem-Solving
- English skill: Can read English document, Communication

## EDUCATION

VIETNAM NATIONAL UNIVERSITY – HO CHI MINH CITY  
UNIVERSITY OF TECHNOLOGY

GPA: 7.69/10 | START YEAR: 2020

## PROJECT

- Smart home control device by voice in technical festival contest 2020 - Member responsible for hardware and software design - won 3rd place in the competition
- Security door in cockpit airbus A320 in subject embedded system design - Team leader.
- SRAM IC design in subject digital IC design Neural network implemented on FPGA
- Designed a 32-bit RISC-V microcontroller – Team leader
- Design Audio Equalizer using FIR filter implement on FPGA - Member responsible for designing FIR filter and implementing communication protocol with codec.

## PERSONAL DETAILS

- Date of birth: 25, Oct 2002
- Gender: Male
- Nationality: Viet Nam
- Identification ID: 086202010192
- Health: No problem

## INTEREST

- System design
- Playing Piano