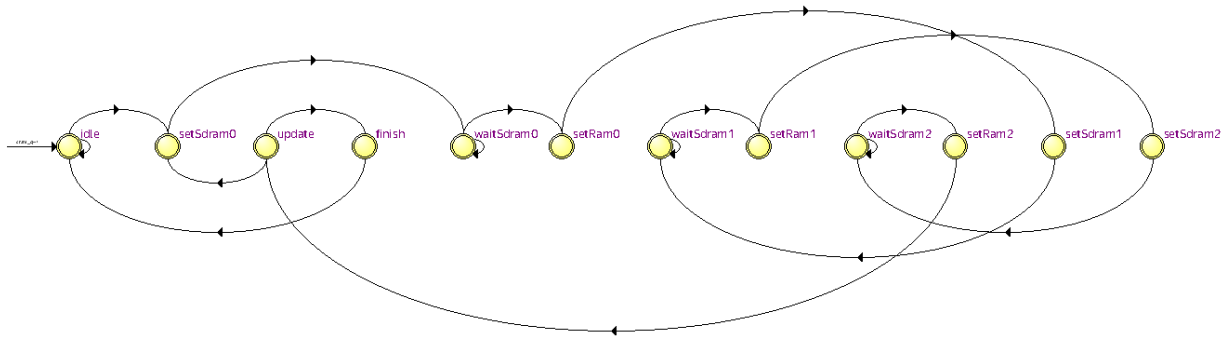


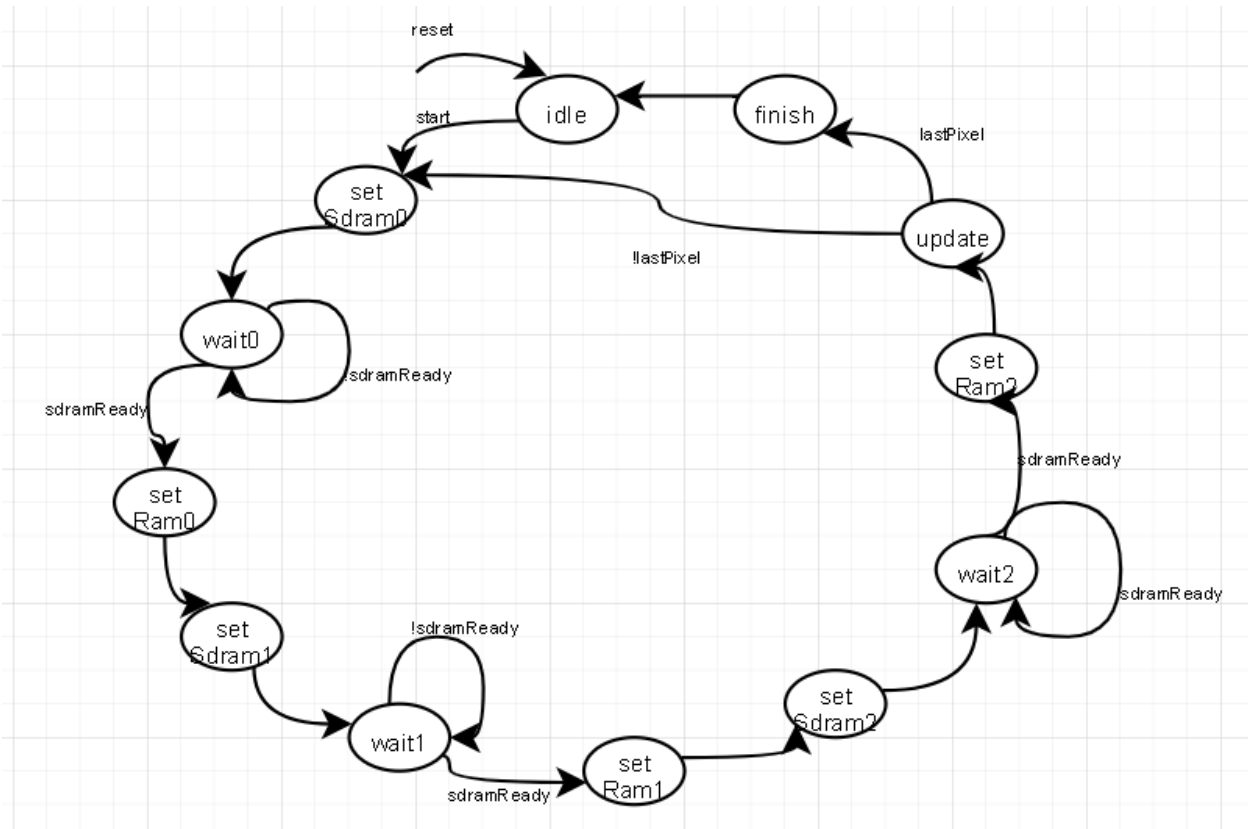
State machine

Fetching control state machine:

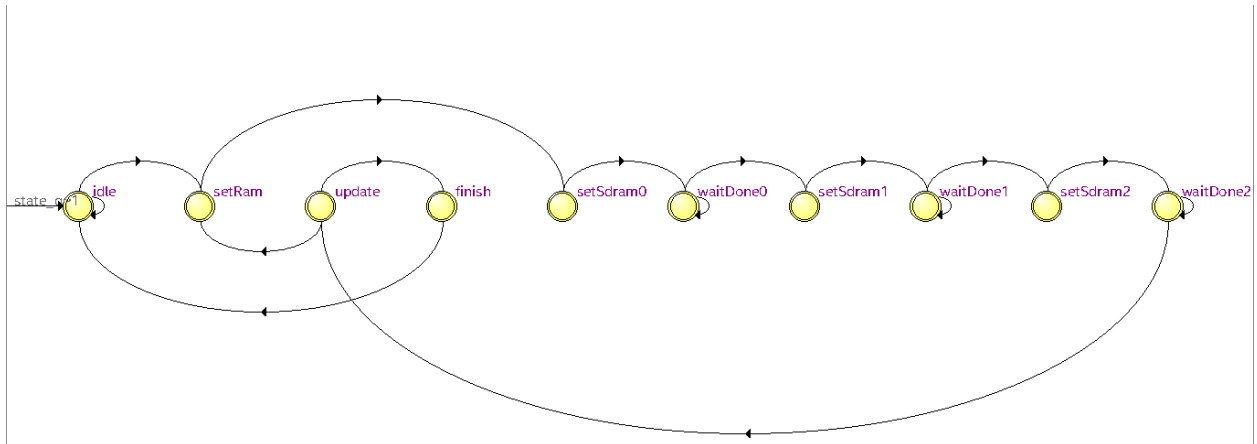
Quartus:



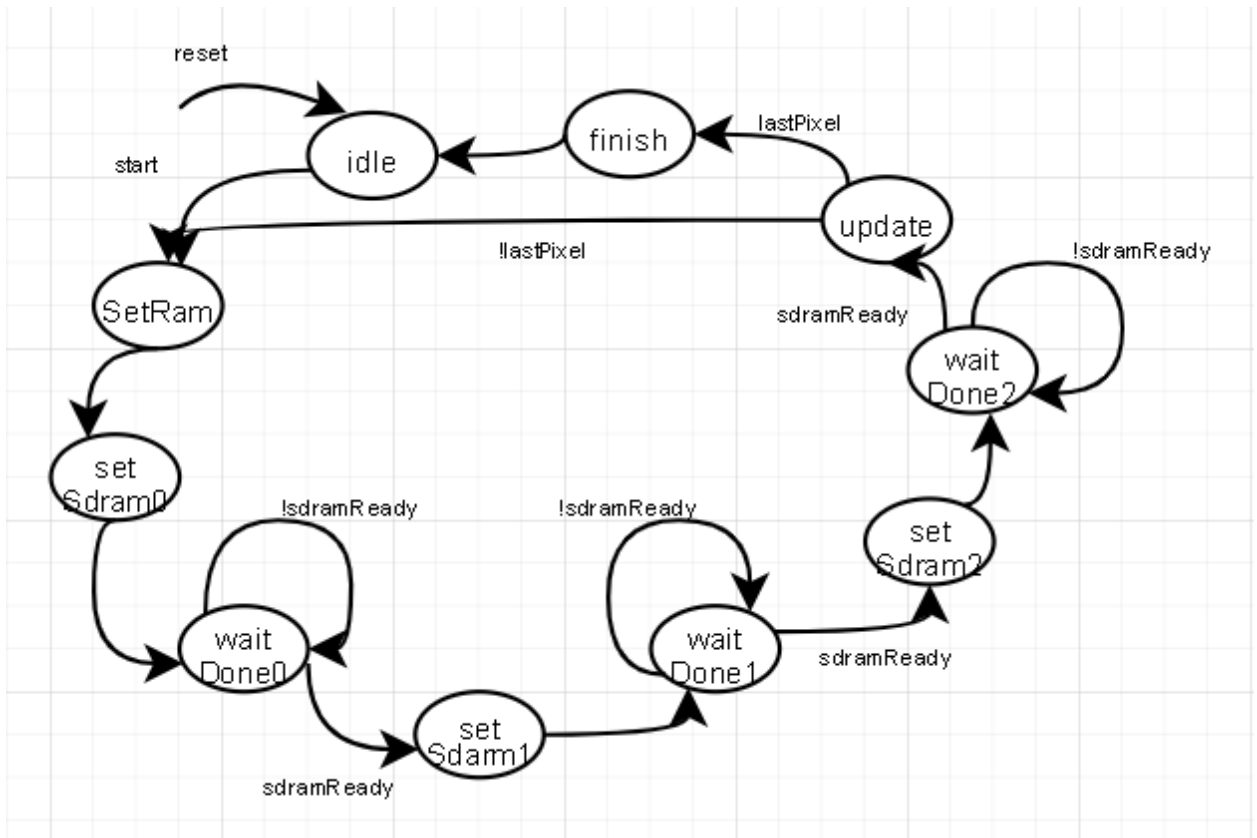
State machine:



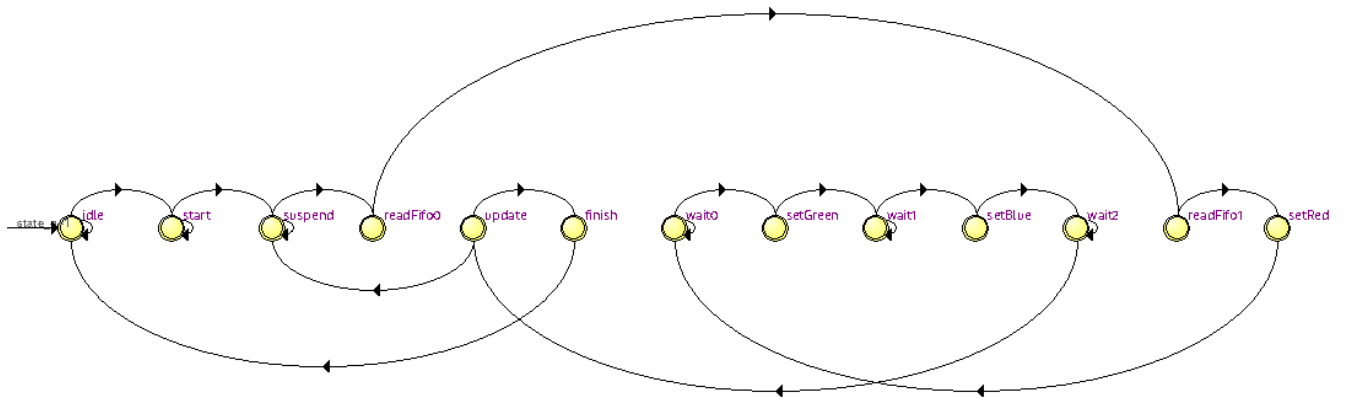
Write back control:



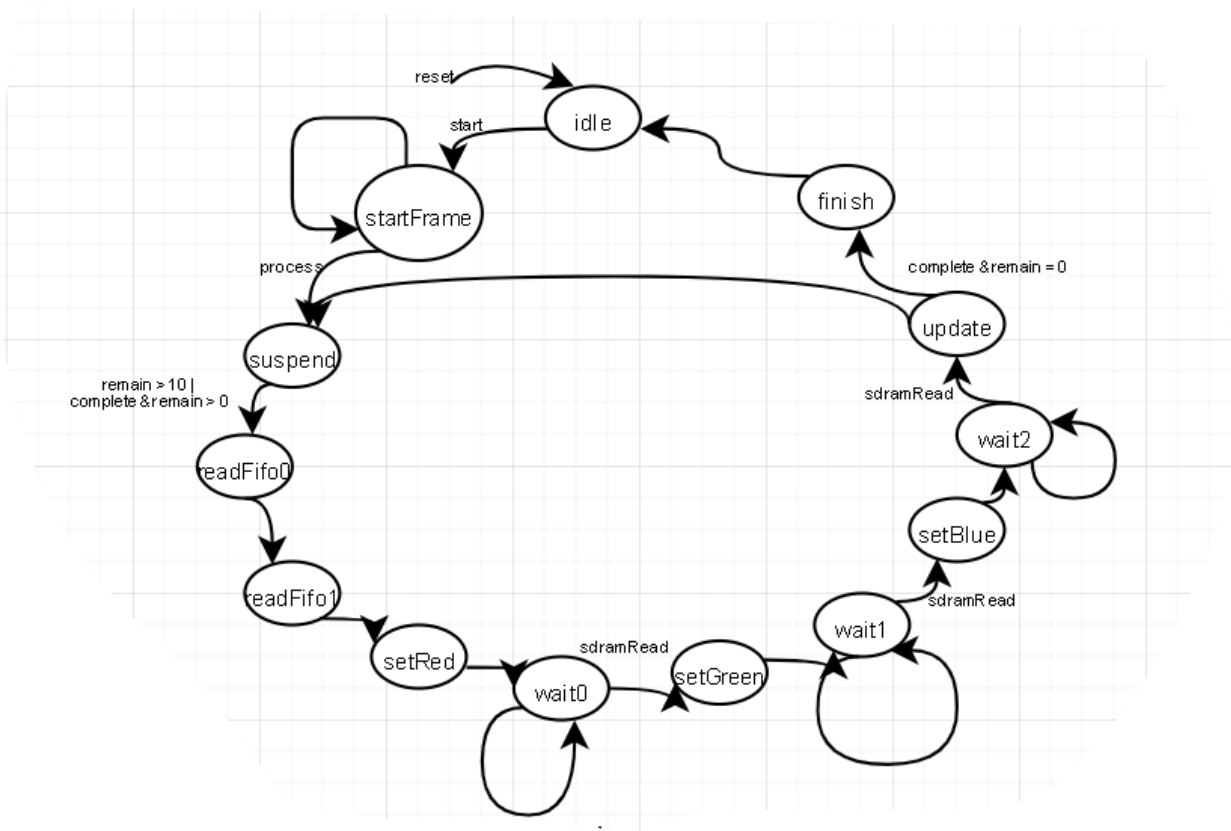
State machine:



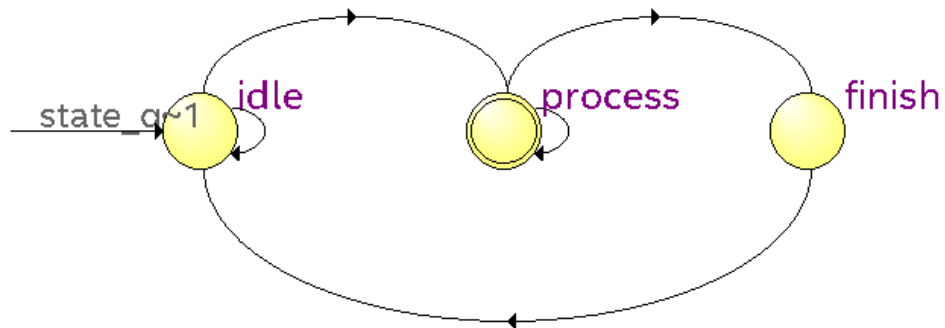
Store Cam to SDRAM control:



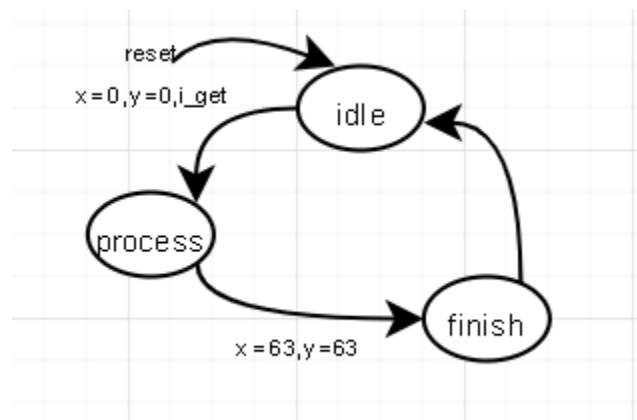
State machine:



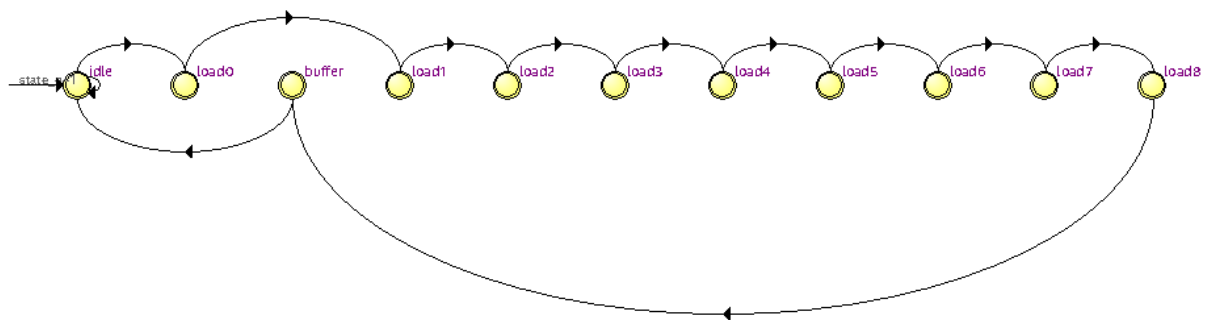
Control write to FIFO:



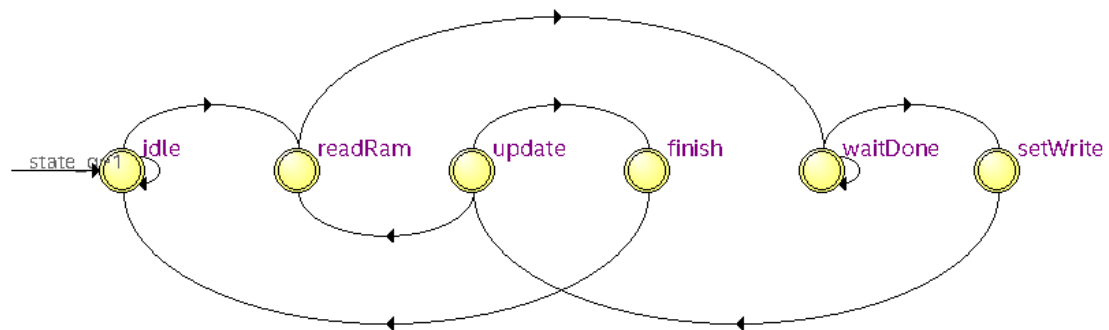
State machine:



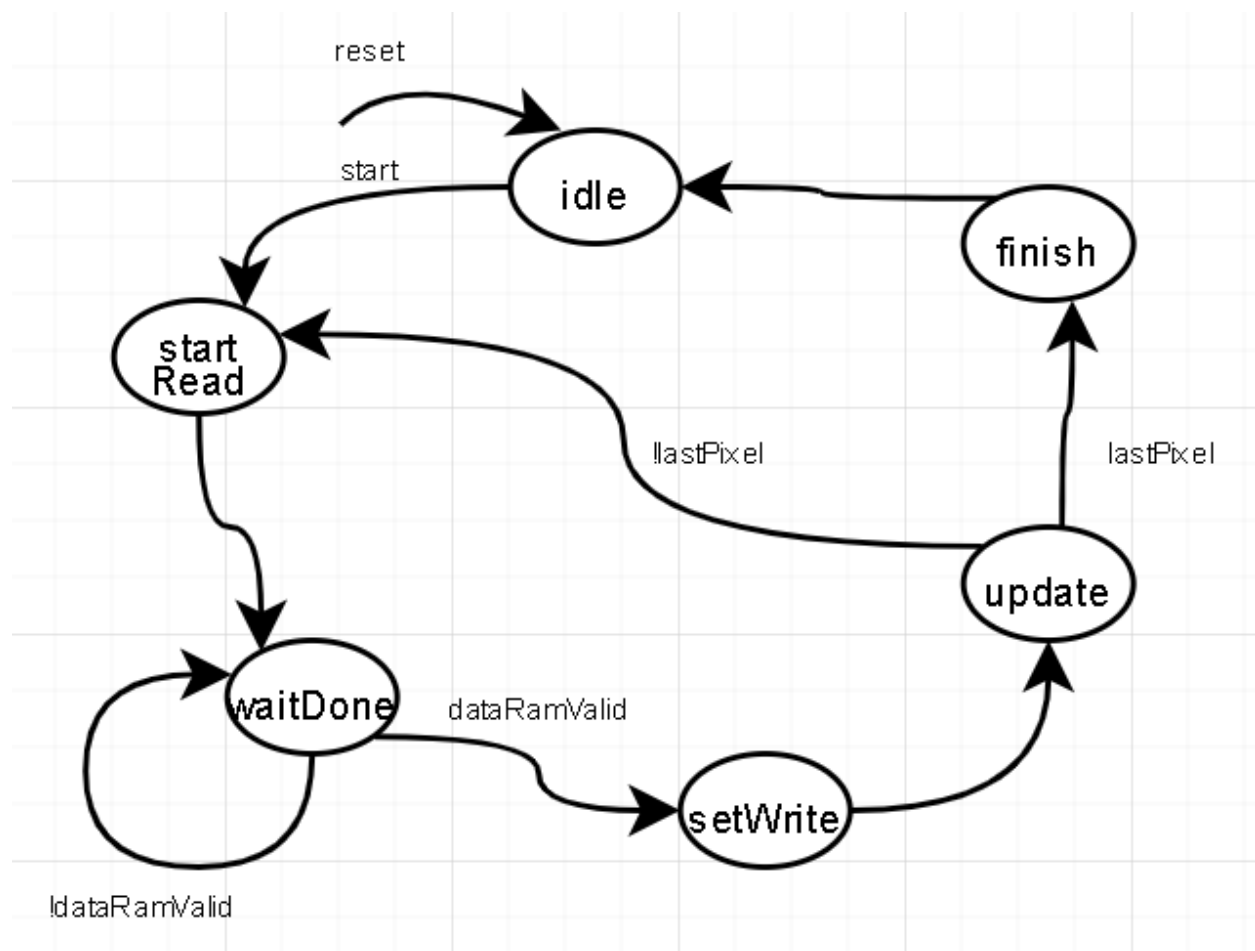
Embedded Ram control:



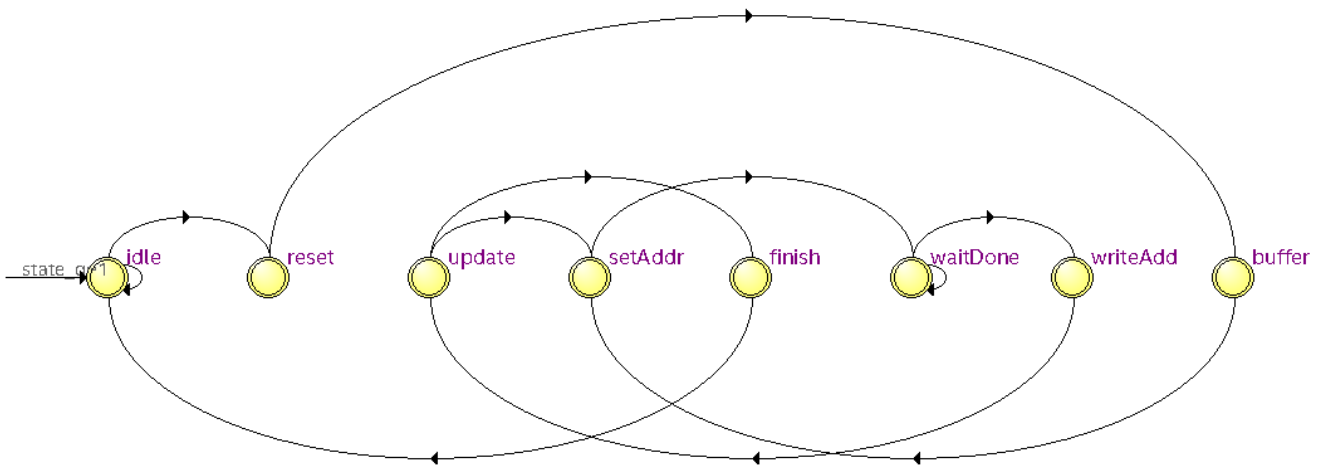
Convolution Control:



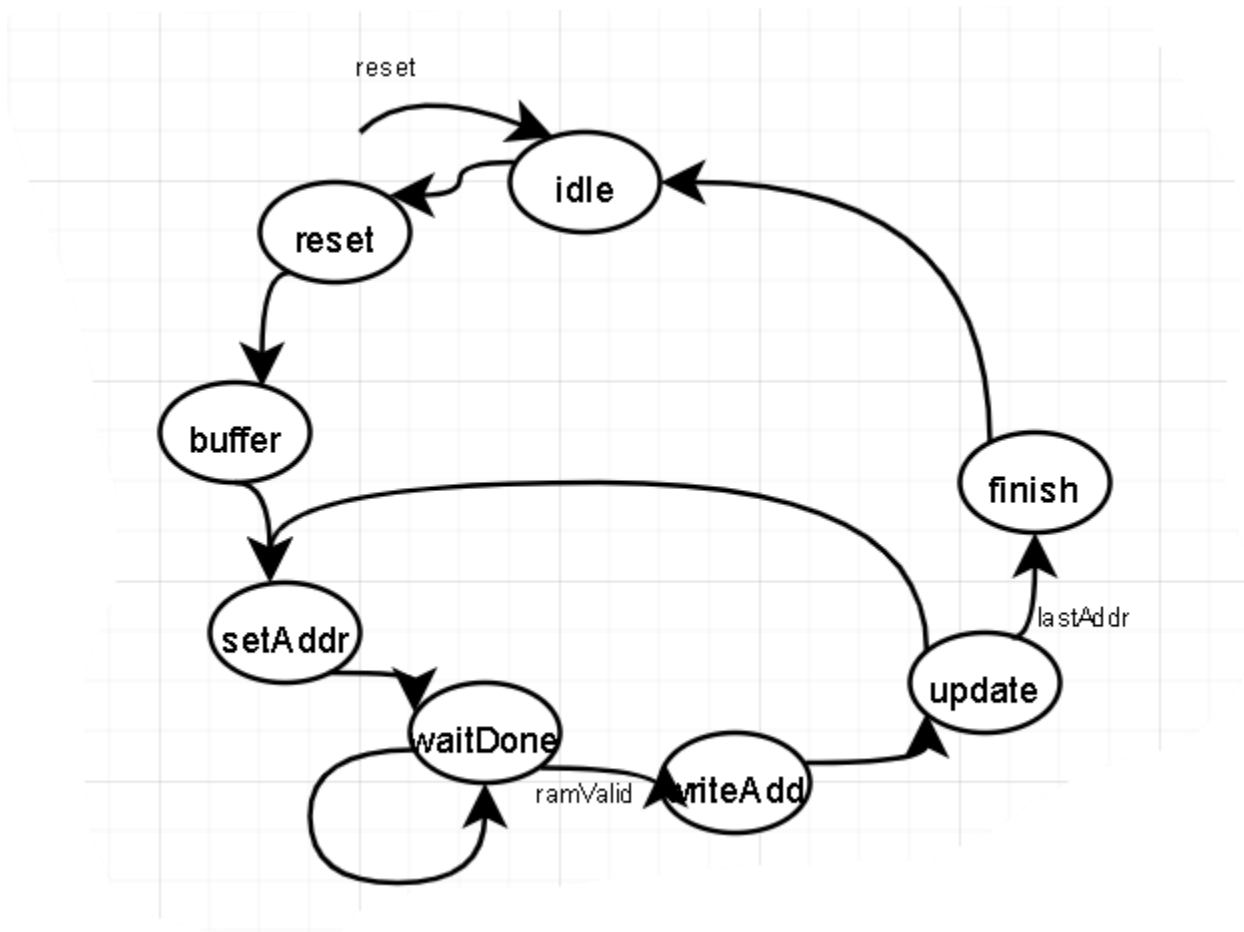
State machine:



Average Control:



State machine:



Master control:

