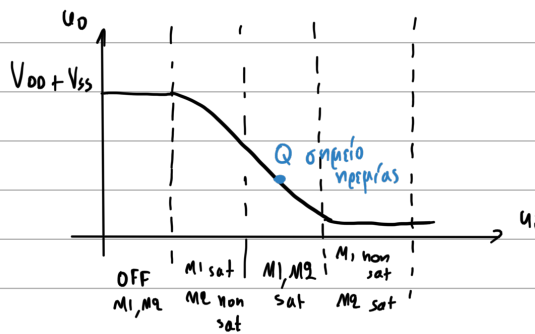
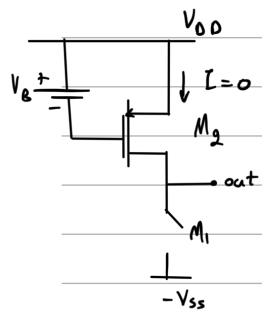
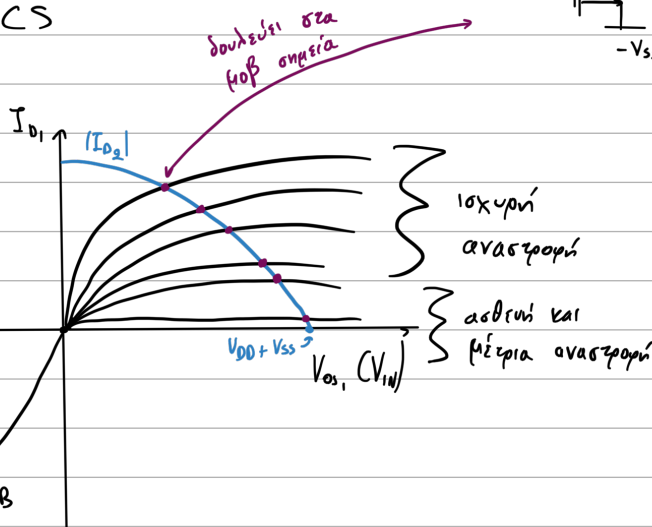
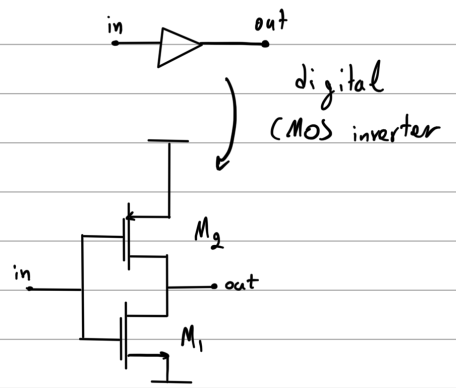
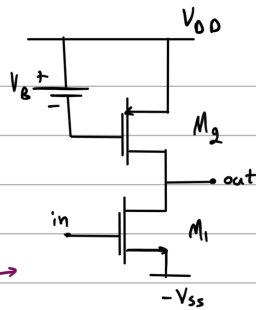
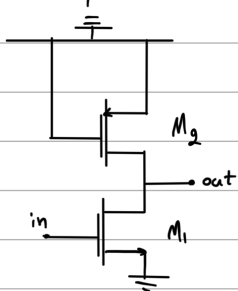


analog
CMOS
inverter

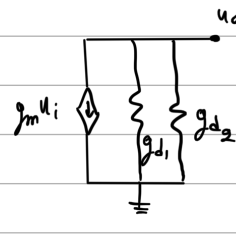
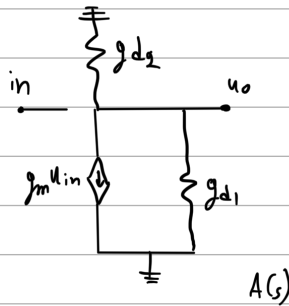


dc χαρακτηριστική

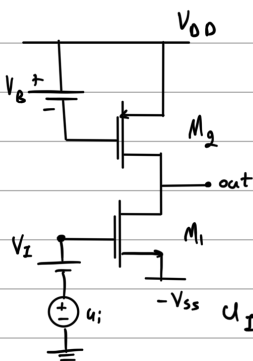
ac κύκλωμα



1KΣ

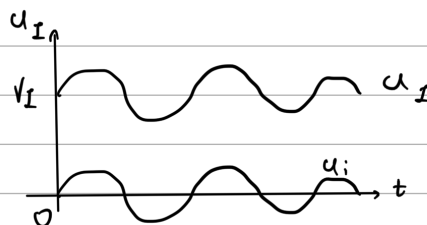
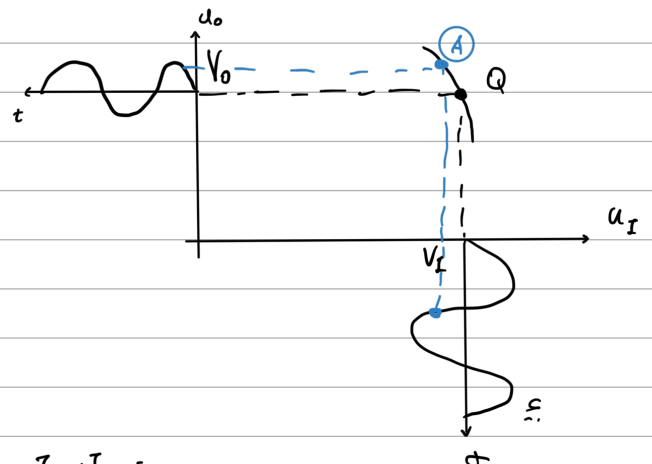


$$A_0 = -\frac{g_{m1}}{g_{d1} + g_{d2}} \rightarrow \approx 50$$

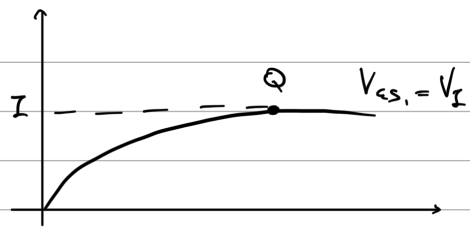
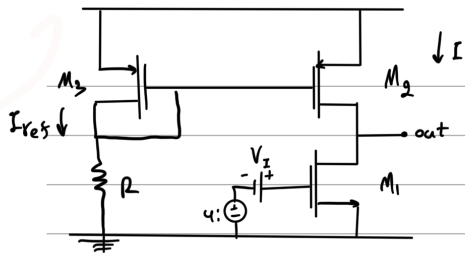


$$u_I = V_I + u_i$$

dc συνιστώσα
ac συνιστώσα



$$I \sim I_{res}$$



cascode analog inverter

