	# CV32E40P RTL Version	120ac3c
	# OneSpin Version	2021.1.0
	# build_date: Jan 7 2021 18:37:56	
	# license_mode:default	
	# flexera:11.15.1.0	
	# platform: Linux_x86_64	

RV32C

1	Property 'RV_chk.ops.RVC.Branch_a'	holds
2	Property 'RV_chk.ops.RVC.Mem_a'	holds
3	Property 'RV_chk.ops.RVC.Mem_MA_a'	holds
4	Property 'RV_chk.ops.RVC.Jump_a'	holds
5	Property 'RV_chk.ops.RVC.Arith_a'	holds
6	Property 'RV_chk.ops.RVC.Branch_taken_a'	holds

RV32I & RV32Zcsri

1	Property 'RV_chk.ops.reset_a'	holds
2	Property 'RV_chk.ops.bubble_a'	holds
3	Property 'RV_chk.ops.RV32I.CSR_flush_a'	holds
4	Property 'RV_chk.ops.RV32I.FENCE_I_a'	holds
5	Property 'RV_chk.ops.RV32I.WFI_flush_a'	holds
6	Property 'RV_chk.ops.RV32I.Mem_MA_a'	holds
7	Property 'RV_chk.ops.RV32I.Jump_a'	holds
8	Property 'RV_chk.ops.RV32I.Mem_a'	holds
9	Property 'RV_chk.ops.RV32I.CSR_a'	holds
10	Property 'RV_chk.ops.RV32I.Arith_a'	holds
11	Property 'RV_chk.ops.RV32I.Branch_a'	holds
12	Property 'RV_chk.ops.RV32I.WFI_sleep_a'	holds
13	Property 'RV_chk.ops.RV32I.WFI_nop_a'	holds
14	Property 'RV_chk.ops.RV32I.RET_a'	holds
15	Property 'RV_chk.ops.RV32I.EBREAK_BP_DM_a'	holds
16	Property 'RV_chk.ops.RV32I.EBREAK_DBG_a'	holds
17	Property 'RV_chk.ops.RV32I.CallBreak_a'	holds
18	Property 'RV_chk.ops.RV32I.Branch_taken_a'	holds

	RV32IMC Exceptions		
1	Property 'RV_chk.ops.xcpt_DBG_BP_a'	holds	
2	Property 'RV_chk.ops.xcpt_fetch_dec_a'	holds	

	RV32M		Notes
1	Property 'RV_chk.ops.RV32M.mul_a'	holds	*partial proof
2	Property 'RV_chk.ops.RV32M.mulh_a'	holds	*partial proof
3	Property 'RV_chk.ops.RV32M.mulhsu_a'	holds	*partial proof
4	Property 'RV_chk.ops.RV32M.mulhu_a'	holds	*partial proof
5	Property 'RV_chk.ops.RV32M.genblk1[10].div_a'	holds	*partial proof for div, divu, rem, remu (latency 10)
6	Property 'RV_chk.ops.RV32M.genblk1[11].div_a'	holds	*partial proof for div, divu, rem, remu (latency 11)
7	Property 'RV_chk.ops.RV32M.genblk1[12].div_a'	holds	*partial proof for div, divu, rem, remu (latency 12)
8	Property 'RV_chk.ops.RV32M.genblk1[13].div_a'	holds	*partial proof for div, divu, rem, remu (latency 13)
9	Property 'RV_chk.ops.RV32M.genblk1[14].div_a'	holds	*partial proof for div, divu, rem, remu (latency 14)
10	Property 'RV_chk.ops.RV32M.genblk1[15].div_a'	holds	*partial proof for div, divu, rem, remu (latency 15)
11	Property 'RV_chk.ops.RV32M.genblk1[16].div_a'	holds	*partial proof for div, divu, rem, remu (latency 16)
12	Property 'RV_chk.ops.RV32M.genblk1[17].div_a'	holds	*partial proof for div, divu, rem, remu (latency 17)
13	Property 'RV_chk.ops.RV32M.genblk1[18].div_a'	holds	*partial proof for div, divu, rem, remu (latency 18)
14	Property 'RV_chk.ops.RV32M.genblk1[19].div_a'	holds	*partial proof for div, divu, rem, remu (latency 19)
15	Property 'RV_chk.ops.RV32M.genblk1[2].div_a'	holds	*partial proof for div, divu, rem, remu (latency 2)
16	Property 'RV_chk.ops.RV32M.genblk1[20].div_a'	holds	*partial proof for div, divu, rem, remu (latency 20)
17	Property 'RV_chk.ops.RV32M.genblk1[21].div_a'	holds	*partial proof for div, divu, rem, remu (latency 21)
18	Property 'RV_chk.ops.RV32M.genblk1[22].div_a'	holds	*partial proof for div, divu, rem, remu (latency 22)
19	Property 'RV_chk.ops.RV32M.genblk1[23].div_a'	holds	*partial proof for div, divu, rem, remu (latency 23)
20	Property 'RV_chk.ops.RV32M.genblk1[24].div_a'	holds	*partial proof for div, divu, rem, remu (latency 24)
21	Property 'RV_chk.ops.RV32M.genblk1[25].div_a'	holds	*partial proof for div, divu, rem, remu (latency 25)
22	Property 'RV_chk.ops.RV32M.genblk1[26].div_a'	holds	*partial proof for div, divu, rem, remu (latency 26)
23	Property 'RV_chk.ops.RV32M.genblk1[27].div_a'	holds	*partial proof for div, divu, rem, remu (latency 27)
24	Property 'RV_chk.ops.RV32M.genblk1[28].div_a'	holds	*partial proof for div, divu, rem, remu (latency 28)
25	Property 'RV_chk.ops.RV32M.genblk1[29].div_a'	holds	*partial proof for div, divu, rem, remu (latency 29)
26	Property 'RV_chk.ops.RV32M.genblk1[3].div_a'	holds	*partial proof for div, divu, rem, remu (latency 3)
27	Property 'RV_chk.ops.RV32M.genblk1[30].div_a'	holds	*partial proof for div, divu, rem, remu (latency 30)

28	Property 'RV_chk.ops.RV32M.genblk1[31].div_a'	holds	*partial proof for div, divu, rem, remu (latency 31)
29	Property 'RV_chk.ops.RV32M.genblk1[32].div_a'	holds	*partial proof for div, divu, rem, remu (latency 32)
30	Property 'RV_chk.ops.RV32M.genblk1[33].div_a'	holds	*partial proof for div, divu, rem, remu (latency 33)
31	Property 'RV_chk.ops.RV32M.genblk1[34].div_a'	holds	*partial proof for div, divu, rem, remu (latency 34)
32	Property 'RV_chk.ops.RV32M.genblk1[4].div_a'	holds	*partial proof for div, divu, rem, remu (latency 4)
33	Property 'RV_chk.ops.RV32M.genblk1[5].div_a'	holds	*partial proof for div, divu, rem, remu (latency 5)
34	Property 'RV_chk.ops.RV32M.genblk1[6].div_a'	holds	*partial proof for div, divu, rem, remu (latency 6)
35	Property 'RV_chk.ops.RV32M.genblk1[7].div_a'	holds	*partial proof for div, divu, rem, remu (latency 7)
36	Property 'RV_chk.ops.RV32M.genblk1[8].div_a'	holds	*partial proof for div, divu, rem, remu (latency 8)
37	Property 'RV_chk.ops.RV32M.genblk1[9].div_a'	holds	*partial proof for div, divu, rem, remu (latency 9)

*partial proof - completely unrestricted and unbounded proof that "just" does not check the value of X[rd], but performs all other checks required for instructions. Regression run with 4 most significant bits and 4 least siginificant bits of either operands free and the remaining bits all set to the same value - all 0 or all 1