

Parameter set

Matrix equation sizes 2×4 and 4×1 ($n = 2$, $m = 4$, $k = 1$); bound $B = 4$; plaintext modulus $q = 8191$; Z_p prime p is the one used for scalar operations in SECP256k1 ($2^{256} - 2^{32} - 977$); modulus size 1024.

Measurements, SECP256k1

Network transfer

Transfer	size
Shared randomness (to verifier)	6.13 Mb
Random challenge (to prover)	3.05 Mb
Folding info (to verifier)	64 b \times 17
Folding info (to prover)	32 b \times 17
Inner product info (to verifier)	64 b
Inner product info (to prover)	32 b
Inner product info (to verifier)	96 b

Single thread, 6-Core Intel Core i7 @ 2.6 GHz

main	inner product	folding	time	main	inner product	folding	time
P1			1.27s				
				V1			46.3ms
P2			30.3s	V2			30.1s
					V1		136 μ s
	P1		9.11ms		V2		7.67ms
		P1	21.7s				
						V1	96.2 μ s
		P2	16.8s			V2	16.7s
	P2		765 μ s				
					V3		2.68 μ s
	P3		127 μ s				
					V4 (check happens here)		993 μ s
Prover time			~ 70 s	Verifier time			~ 47 s

6 threads, 6-Core Intel Core i7 @ 2.6 GHz

main	inner product	folding	time	main	inner product	folding	time
P1			790ms				
				V1			35.9ms
P2			6.01s	V2			6.58s
					V1		171 μ s
	P1		23.0ms		V2		17.5ms
		P1	5.23s				
						V1	98.6 μ s
		P2	4.42s			V2	4.28s
	P2		931 μ s				
					V3		4.88 μ s
	P3		145 μ s				
					V4 (check happens here)		1.23ms
Prover time			~ 15.7 s	Verifier time			~ 10.9 s

Totals

	i7 @ 2.6 GHz, 1 thread	i7 @ 2.6 GHz, 6 threads
Prover time	70s	15.7s
Verifier time	47s	10.9s
Encryption time	16ms	
Initial proof generation		
Prover transfers	6 Mb	
Verifier transfers	3 Mb	

Measurements, Curve25519

Note: not all optimized batch multiplications algorithms are in use here. The numbers can be improved.

Network transfer

Transfer	size
Shared randomness (to verifier)	30.6 Mb
Random challenge (to prover)	3.05 Mb
Folding info (to verifier)	$320 \text{ b} \times 17$
Folding info (to prover)	$32 \text{ b} \times 17$
Inner product info (to verifier)	320 b
Inner product info (to prover)	32 b
Inner product info (to verifier)	96 b

Single thread, 6-Core Intel Core i7 @ 2.6 GHz

main	inner product	folding	time	main	inner product	folding	time
P1			4.43s				
				V1			685ms
P2			23.8s	V2			22.5s
					V1		60.3 μ s
	P1		23.3ms		V2		24.5ms
		P1	12.1s				
						V1	239 μ s
		P2	20.8s			V2	18.4s
	P2		351 μ s				
					V3		6.46 μ s
	P3		134 μ s				
					V4 (check happens here)		506 μ s
Prover time			$\sim 61 \text{ s}$	Verifier time			$\sim 41 \text{ s}$

6 threads, 6-Core Intel Core i7 @ 2.6 GHz

main	inner product	folding	time	main	inner product	folding	time
P1			4.65ms				
				V1			603ms
P2			8.91s	V2			7.84s
					V1		62.6 μ s
	P1		39.2ms		V2		30.8ms
		P1	4.09s				
						V1	250 μ s
		P2	5.76s			V2	4.29s
	P2		405 μ s				
					V3		9.47 μ s
	P3		151 μ s				
					V4 (check happens here)		577 μ s
Prover time			$\sim 18.8 \text{ s}$	Verifier time			$\sim 12.1 \text{ s}$

Totals

	i7 @ 2.6 GHz, 1 thread	i7 @ 2.6 GHz, 6 threads
Prover time	61s	18.8s
Verifier time	41s	12.1s
Encryption time	16 ms	
Initial proof generation	16.7s	
Prover transfers	30.6 Mb	
Verifier transfers	3 Mb	

Notes

- The initial transfers consist mostly of random numbers. Their size can be reduced significantly (to the order of several bytes) if one can just transfer a random seed and trust the other party to generate the randoms.
- There are many consecutive transfers during folding stages. It may be possible to pack them into a single transfer from each size (the verifier prepares an array of randoms c , the prover calculates t_1, t_{-1} for each stage and sends them to the verifier), if it that does not compromise security.
- There is a large amount of identical calculations that both prover and verifier perform on the same data. If only one party can be trusted to perform them, performance can be significantly improved.

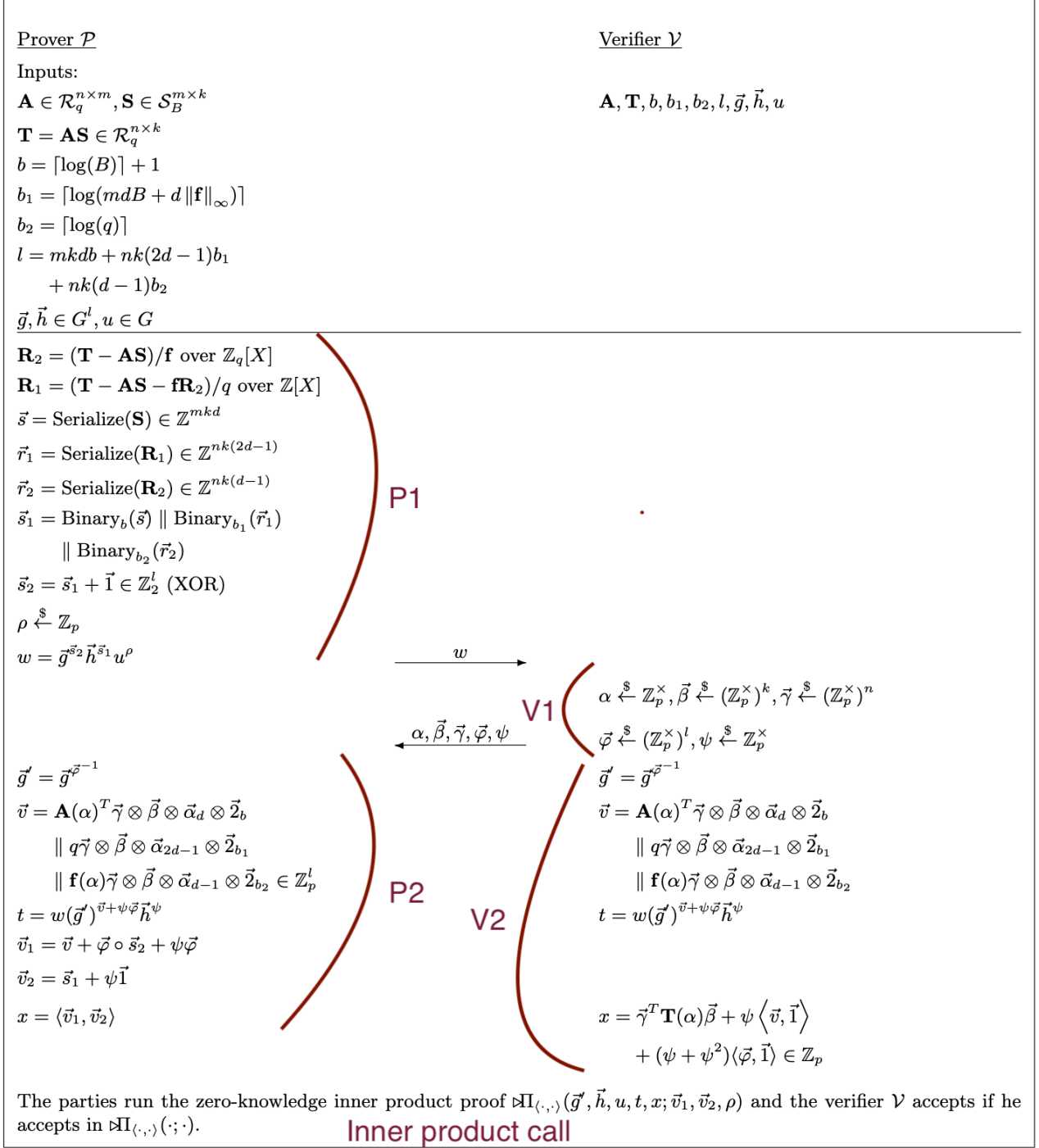


Figure 1: Main protocol

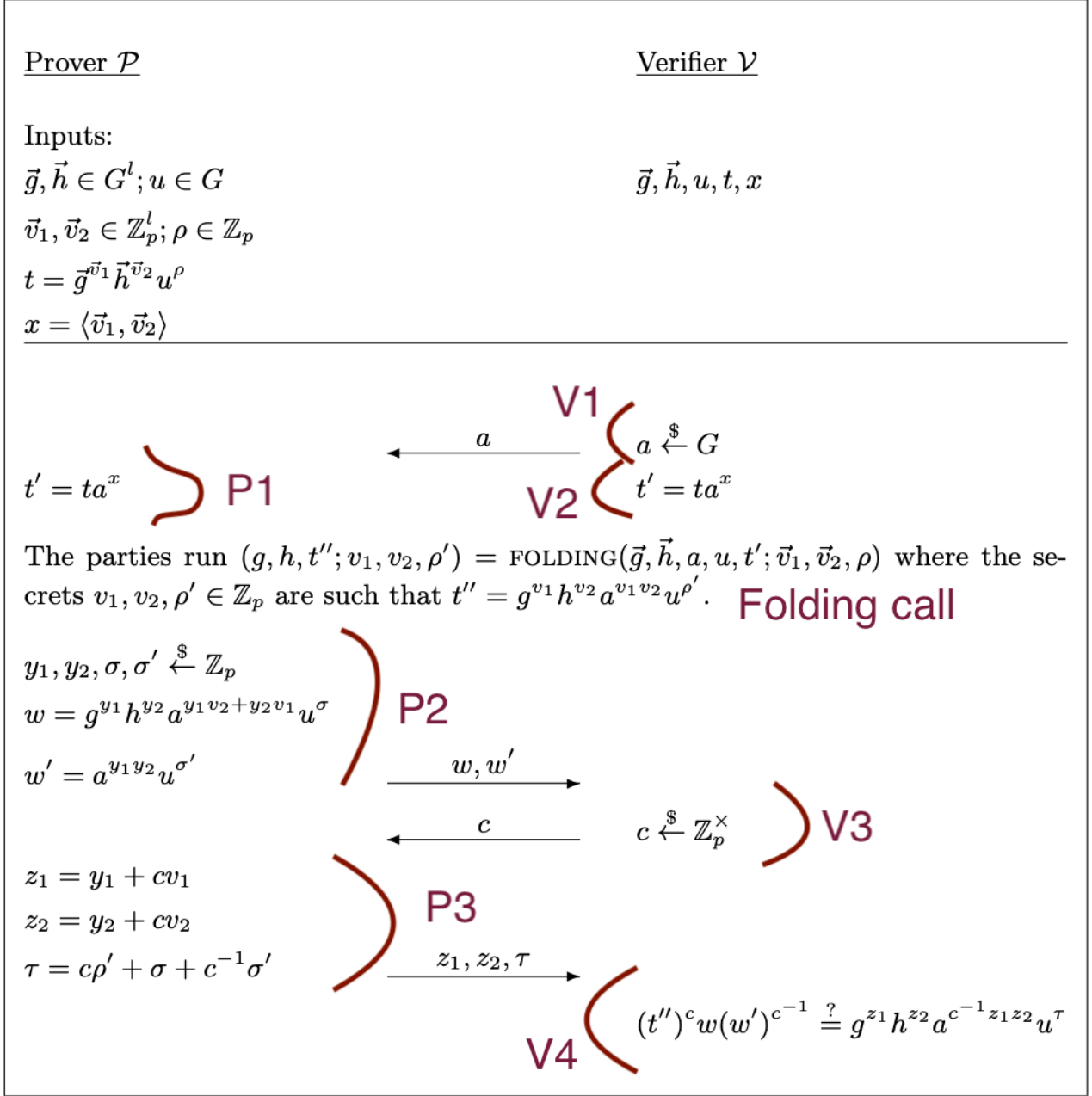


Figure 2: Inner product protocol

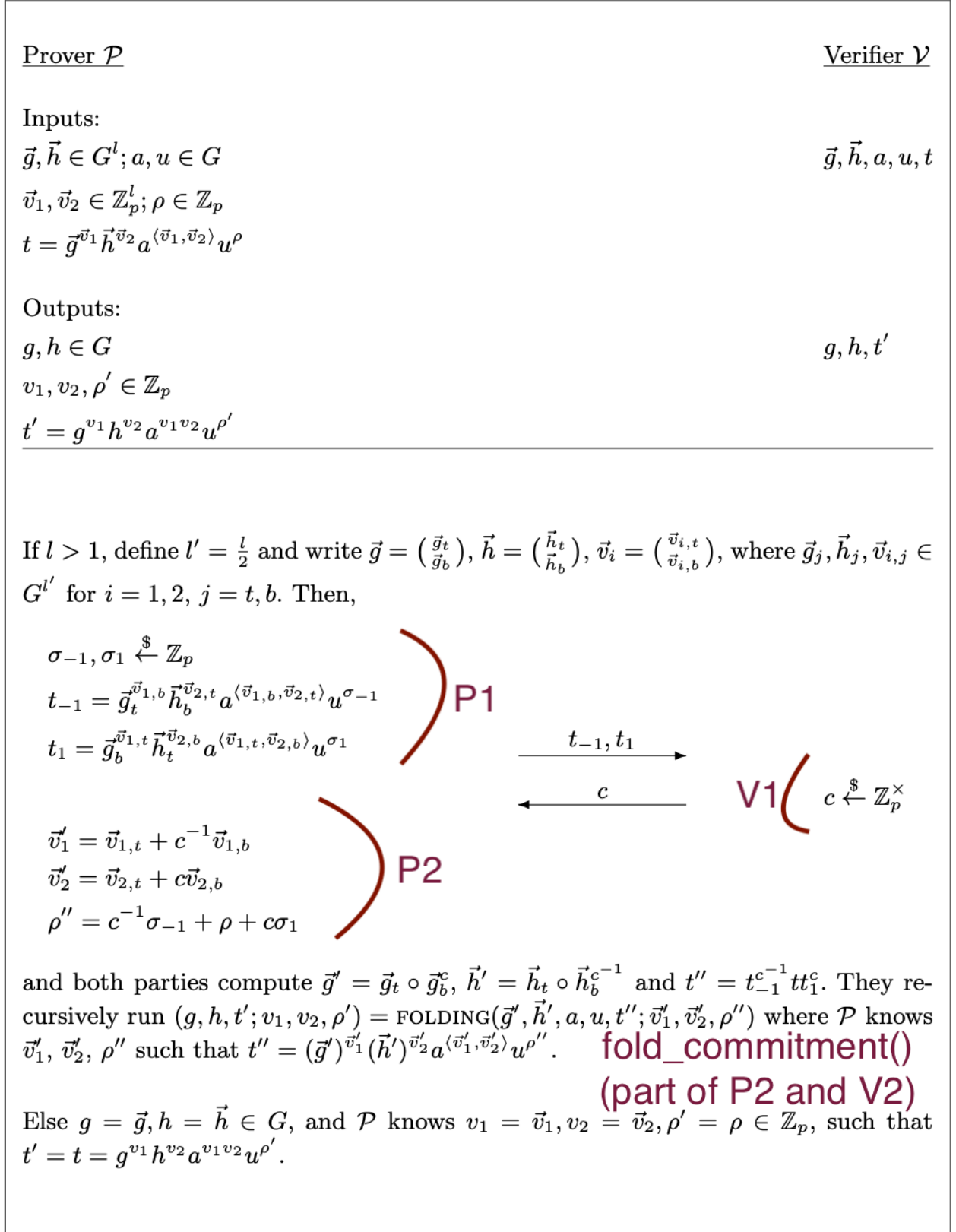


Figure 3: Folding protocol