#### Parameter set

Matrix equation sizes  $2 \times 4$  and  $4 \times 1$  (n=2, m=4, k=1); bound B=4; plaintext modulus q=8191;  $Z_p$  prime p is the one used for scalar operations in SECP256k1 ( $2^{256}-2^{32}-977$ ); modulus size 1024.

# Measurements, SECP256k1

# Network transfer

Transfer	size
Shared randomness (to verifier)	6.13 Mb
Random challenge (to prover)	3.05 Mb
Folding info (to verifier)	$64 \text{ b} \times 17$
Folding info (to prover)	$32 \text{ b} \times 17$
Inner product info (to verifier)	64 b
Inner product info (to prover)	32 b
Inner product info (to verifier)	96 b

# Single thread, 6-Core Intel Core i7 @ 2.6 GHz

main	inner product	folding	time	main	inner product	folding	time
P1			1.27s				
				V1			$46.3 \mathrm{ms}$
P2			30.3s	V2			30.1s
					V1		136µs
	P1		9.11ms		V2		$7.67 \mathrm{ms}$
		P1	21.7s				
						V1	$96.2 \mu s$
		P2	16.8s			V2	16.7s
	P2		765µs				
					V3		$2.68 \mu \mathrm{s}$
	P3		127µs				
					V4 (check happens here)		$993 \mu s$
	Prover time	•	$\sim 70\mathrm{s}$		Verifier time		$\sim 47\mathrm{s}$

#### 6 threads, 6-Core Intel Core i7 @ 2.6 GHz

main	inner product	folding	time	main	inner product	folding	time
P1			790ms				
				V1			$35.9 \mathrm{ms}$
P2			6.01s	V2			6.58s
					V1		171µs
	P1		$23.0 \mathrm{ms}$		V2		17.5ms
		P1	5.23s				
						V1	98.6µs
		P2	4.42s			V2	4.28s
	P2		931µs				
			-		V3		4.88µs
	P3		145µs				
					V4 (check happens here)		1.23ms
	Prover time		$\sim 15.7\mathrm{s}$	S Verifier time		$\sim 10.9\mathrm{s}$	

#### Totals

	i7 @ 2.6 GHz, 1 thread	i7 @ 2.6 GHz, 6 threads			
Prover time	70s	$15.7\mathrm{s}$			
Verifier time	47s	10.9s			
Encryption time	$16 \mathrm{ms}$				
Initial proof generation					
Prover transfers	6 Mb				
Verifier transfers	3 Mb				

# Measurements, Curve25519

Note: not all optimized batch multiplications algorithms are in use here. The numbers can be improved.

#### Network transfer

Transfer	size
Shared randomness (to verifier)	30.6 Mb
Random challenge (to prover)	3.05 Mb
Folding info (to verifier)	$320 \text{ b} \times 17$
Folding info (to prover)	$32 \text{ b} \times 17$
Inner product info (to verifier)	320 b
Inner product info (to prover)	32 b
Inner product info (to verifier)	96 b

# Single thread, 6-Core Intel Core i7 @ 2.6 GHz

main	inner product	folding	time	main	inner product	folding	time
P1			4.43s				
				V1			$685 \mathrm{ms}$
P2			23.8s	V2			22.5s
					V1		60.3µs
	P1		23.3ms		V2		24.5ms
		P1	12.1s				
						V1	239µs
		P2	20.8s			V2	18.4s
	P2		351µs				
					V3		6.46µs
	P3		134µs				
					V4 (check happens here)		506µs
	Prover time		$\sim 61\mathrm{s}$		Verifier time		$\sim 41\mathrm{s}$

#### 6 threads, 6-Core Intel Core i7 @ 2.6 GHz

main	inner product	folding	time	main	inner product	folding	time
P1			$4.65 \mathrm{ms}$				
				V1			$603 \mathrm{ms}$
P2			8.91s	V2			7.84s
					V1		$62.6 \mu s$
	P1		$39.2 \mathrm{ms}$		V2		$30.8 \mathrm{ms}$
		P1	4.09s				
						V1	$250 \mu s$
		P2	5.76s			V2	4.29s
	P2		405µs				
					V3		$9.47 \mu s$
	P3		151µs				
					V4 (check happens here)		577µs
	Prover time		$\sim 18.8\mathrm{s}$	Verifier time		$\sim 12.1\mathrm{s}$	

#### Totals

	i7 @ 2.6 GHz, 1 thread	i7 @ 2.6 GHz, 6 threads		
Prover time	61s	18.8s		
Verifier time	41s	12.1s		
Encryption time	16 ms			
Initial proof generation	16.7s			
Prover transfers	30.6 Mb			
Verifier transfers	3 Mb			

#### Notes

- The initial transfers consist mostly of random numbers. Their size can be reduced significantly (to the order of several bytes) if one can just transfer a random seed and trust the other party to generate the randoms.
- There are many consecutive transfers during folding stages. It may be possible to pack them into a single transfer from each size (the verifier prepares an array of randoms c, the prover calculates  $t_1$ ,  $t_{-1}$  for each stage and sends them to the verifier), if it that does not compromise security.
- There is a large amount of identical calculations that both prover and verifier perform on the same data. If only one party can be trusted to perform them, performance can be significantly improved.

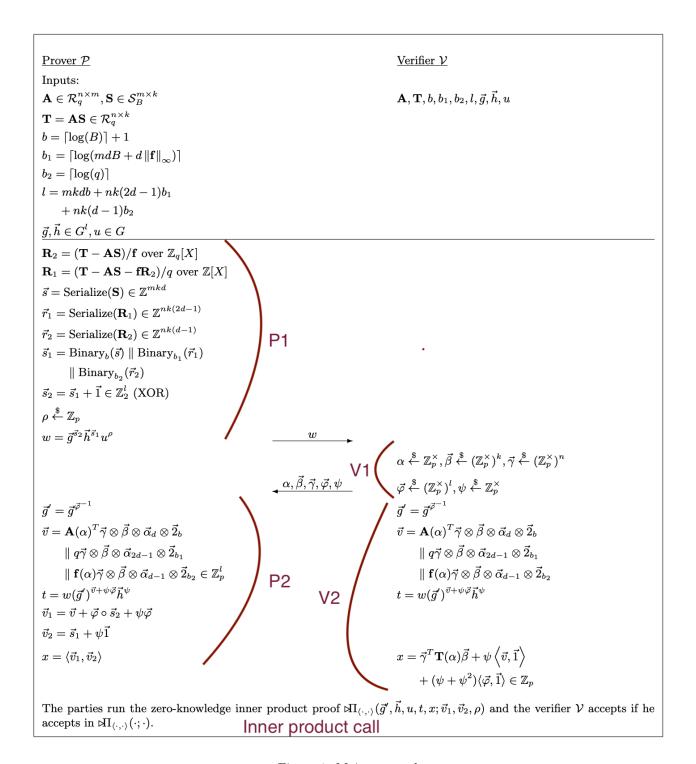


Figure 1: Main protocol

# $\begin{array}{ll} \underline{\text{Prover }\mathcal{P}} & \underline{\text{Verifier }\mathcal{V}} \\ \\ \text{Inputs:} \\ \vec{g}, \vec{h} \in G^l; u \in G & \vec{g}, \vec{h}, u, t, x \\ \vec{v}_1, \vec{v}_2 \in \mathbb{Z}_p^l; \rho \in \mathbb{Z}_p \\ \\ t = \vec{g}^{\vec{v}_1} \vec{h}^{\vec{v}_2} u^{\rho} \\ \\ x = \langle \vec{v}_1, \vec{v}_2 \rangle \end{array}$

$$t' = ta^x$$
 P1 
$$\frac{a}{\sqrt{2}} \begin{cases} a \stackrel{\$}{\leftarrow} G \\ t' = ta^x \end{cases}$$

The parties run  $(g,h,t'';v_1,v_2,\rho')=\text{FOLDING}(\vec{g},\vec{h},a,u,t';\vec{v}_1,\vec{v}_2,\rho)$  where the secrets  $v_1,v_2,\rho'\in\mathbb{Z}_p$  are such that  $t''=g^{v_1}h^{v_2}a^{v_1v_2}u^{\rho'}$ . Folding call

$$\begin{array}{c} y_{1},y_{2},\sigma,\sigma' \overset{\$}{\leftarrow} \mathbb{Z}_{p} \\ w = g^{y_{1}}h^{y_{2}}a^{y_{1}v_{2}+y_{2}v_{1}}u^{\sigma} \\ w' = a^{y_{1}y_{2}}u^{\sigma'} \\ z_{1} = y_{1} + cv_{1} \\ z_{2} = y_{2} + cv_{2} \\ \tau = c\rho' + \sigma + c^{-1}\sigma' \end{array} \qquad \begin{array}{c} w,w' \\ \hline \\ P3 \\ \hline \\ z_{1},z_{2},\tau \\ \hline \\ V4 \end{array} \qquad \begin{array}{c} (t'')^{c}w(w')^{c^{-1}} \overset{?}{=} g^{z_{1}}h^{z_{2}}a^{c^{-1}z_{1}z_{2}}u^{\tau} \end{array}$$

Figure 2: Inner product protocol

 $\underline{\text{Prover }\mathcal{P}}$ 

Inputs:

$$ec{g}, ec{h} \in G^l; a, u \in G$$
 
$$ec{v}_1, ec{v}_2 \in \mathbb{Z}_p^l; 
ho \in \mathbb{Z}_p$$

$$t = \vec{g}^{\vec{v}_1} \vec{h}^{\vec{v}_2} a^{\langle \vec{v}_1, \vec{v}_2 \rangle} u^{\rho}$$

Outputs:

$$g,h \in G$$
 
$$y_1,v_2,
ho' \in \mathbb{Z}_p$$
 
$$t'=g^{v_1}h^{v_2}a^{v_1v_2}u^{
ho'}$$

If l > 1, define  $l' = \frac{l}{2}$  and write  $\vec{g} = \begin{pmatrix} \vec{g}_t \\ \vec{g}_b \end{pmatrix}$ ,  $\vec{h} = \begin{pmatrix} \vec{h}_t \\ \vec{h}_b \end{pmatrix}$ ,  $\vec{v}_i = \begin{pmatrix} \vec{v}_{i,t} \\ \vec{v}_{i,b} \end{pmatrix}$ , where  $\vec{g}_j$ ,  $\vec{h}_j$ ,  $\vec{v}_{i,j} \in G^{l'}$  for i = 1, 2, j = t, b. Then,

and both parties compute  $\vec{g}' = \vec{g}_t \circ \vec{g}_b^c$ ,  $\vec{h}' = \vec{h}_t \circ \vec{h}_b^{c^{-1}}$  and  $t'' = t_{-1}^{c^{-1}} t t_1^c$ . They recursively run  $(g, h, t'; v_1, v_2, \rho') = \text{FOLDING}(\vec{g}', \vec{h}', a, u, t''; \vec{v}_1', \vec{v}_2', \rho'')$  where  $\mathcal{P}$  knows  $\vec{v}_1'$ ,  $\vec{v}_2'$ ,  $\rho''$  such that  $t'' = (\vec{g}')^{\vec{v}_1'} (\vec{h}')^{\vec{v}_2'} a^{\langle \vec{v}_1', \vec{v}_2' \rangle} u^{\rho''}$ . fold\_commitment()

Else  $g=\vec{g}, h=\vec{h}\in G$ , and  $\mathcal{P}$  knows  $v_1=\vec{v}_1, v_2=\vec{v}_2, \rho'=\rho\in\mathbb{Z}_p$ , such that  $t'=t=g^{v_1}h^{v_2}a^{v_1v_2}u^{\rho'}$ .

Figure 3: Folding protocol