

Parameter set

Matrix equation sizes 2×4 and 4×1 ($n = 2$, $m = 4$, $k = 1$); bound $B = 4$; plaintext modulus $q = 8191$; Z_p prime p is the one used for scalar operations in SECP256k1 ($2^{256} - 2^{32} - 977$); modulus size 1024.

Measurements, SECP256k1

Network transfer

Transfer	size	size (compressed points)
Shared randomness (to verifier)	19.9 Mb	6.13 Mb
Random challenge (to prover)	3.05 Mb	3.05 Mb
Folding info (to verifier)	$208 \text{ b} \times 17$	$64 \text{ b} \times 17$
Folding info (to prover)	$32 \text{ b} \times 17$	$32 \text{ b} \times 17$
Inner product info (to verifier)	208 b	64 b
Inner product info (to prover)	32 b	32 b
Inner product info (to verifier)	96 b	96 b

Single thread, 6-Core Intel Core i7 @ 2.6 GHz

main	inner product	folding	time	main	inner product	folding	time
P1			702ms				
				V1			2.09ms
P2			25.9s	V2			25.6s
					V1		86.0 μ s
	P1		7.17ms		V2		7.99ms
		P1	21.5s				
						V1	31.5 μ s
		P2	16.6s			V2	16.6s
	P2		519 μ s				
					V3		2.29 μ s
	P3		118 μ s				
					V4 (check happens here)		759 μ s
Prover time			$\sim 64 \text{ s}$	Verifier time			$\sim 47 \text{ s}$

6 threads, 6-Core Intel Core i7 @ 2.6 GHz

main	inner product	folding	time	main	inner product	folding	time
P1			807ms				
				V1			2.09ms
P2			5.36s	V2			5.38s
					V1		109 μ s
	P1		32.7ms		V2		20.1ms
		P1	5.15s				
						V1	31.8 μ s
		P2	4.36s			V2	4.30s
	P2		611 μ s				
					V3		2.95 μ s
	P3		133 μ s				
					V4 (check happens here)		906 μ s
Prover time			$\sim 14.9 \text{ s}$	Verifier time			$\sim 9.7 \text{ s}$

Totals

	i7 @ 2.6 GHz, 1 thread	i7 @ 2.6 GHz, 6 threads
Prover time	70s	14.9s
Verifier time	47s	9.7s
Encryption time	1.18ms	=
Decryption time	607 μ s	=
Initial proof generation	16.0s	3.23s
Prover transfers	20 Mb (non-compressed) / 6 Mb (compressed)	
Verifier transfers	3 Mb	

Measurements, Curve25519

Note: not all optimized batch multiplications algorithms are in use here. The numbers can be improved.

Network transfer

Transfer	size
Shared randomness (to verifier)	30.6 Mb
Random challenge (to prover)	3.05 Mb
Folding info (to verifier)	320 b \times 17
Folding info (to prover)	32 b \times 17
Inner product info (to verifier)	320 b
Inner product info (to prover)	32 b
Inner product info (to verifier)	96 b

Single thread, 6-Core Intel Core i7 @ 2.6 GHz

main	inner product	folding	time	main	inner product	folding	time
P1			535ms				
				V1			5.25ms
P2			9.14s	V2			8.90s
					V1		20.0 μ s
	P1		50.9ms		V2		76.1ms
		P1	9.70s				
						V1	29.0 μ s
		P2	15.2s			V2	14.8s
	P2		234 μ s				
					V3		2.42 μ s
	P3		31.2 μ s				
					V4 (check happens here)		309 μ s
Prover time			\sim 34.6 s	Verifier time			\sim 23.7 s

6 threads, 6-Core Intel Core i7 @ 2.6 GHz

main	inner product	folding	time	main	inner product	folding	time
P1			564ms				
				V1			5.34ms
P2			2.09s	V2			2.10s
					V1		20.6 μ s
	P1		25.7ms		V2		33.4ms
		P1	2.37s				
						V1	30.0 μ s
		P2	3.15s			V2	3.08s
	P2		273 μ s				
					V3		3.70 μ s
	P3		37.2 μ s				
					V4 (check happens here)		329 μ s
Prover time			\sim 8.2 s	Verifier time			\sim 5.2 s

Totals

	i7 @ 2.6 GHz, 1 thread	i7 @ 2.6 GHz, 6 threads
Prover time	34.6s	8.2s
Verifier time	23.7s	5.2s
Encryption time	1.36ms	=
Initial proof generation	2.15s	434ms
Prover transfers	30.6 Mb	
Verifier transfers	3 Mb	

Notes

- The initial transfers consist mostly of random numbers. Their size can be reduced significantly (to the order of several bytes) if one can just transfer a random seed and trust the other party to generate the randoms.
- There are many consecutive transfers during folding stages. It may be possible to pack them into a single transfer from each size (the verifier prepares an array of randoms c , the prover calculates t_1, t_{-1} for each stage and sends them to the verifier), if it that does not compromise security.
- There is a large amount of identical calculations that both prover and verifier perform on the same data. If only one party can be trusted to perform them, performance can be significantly improved.

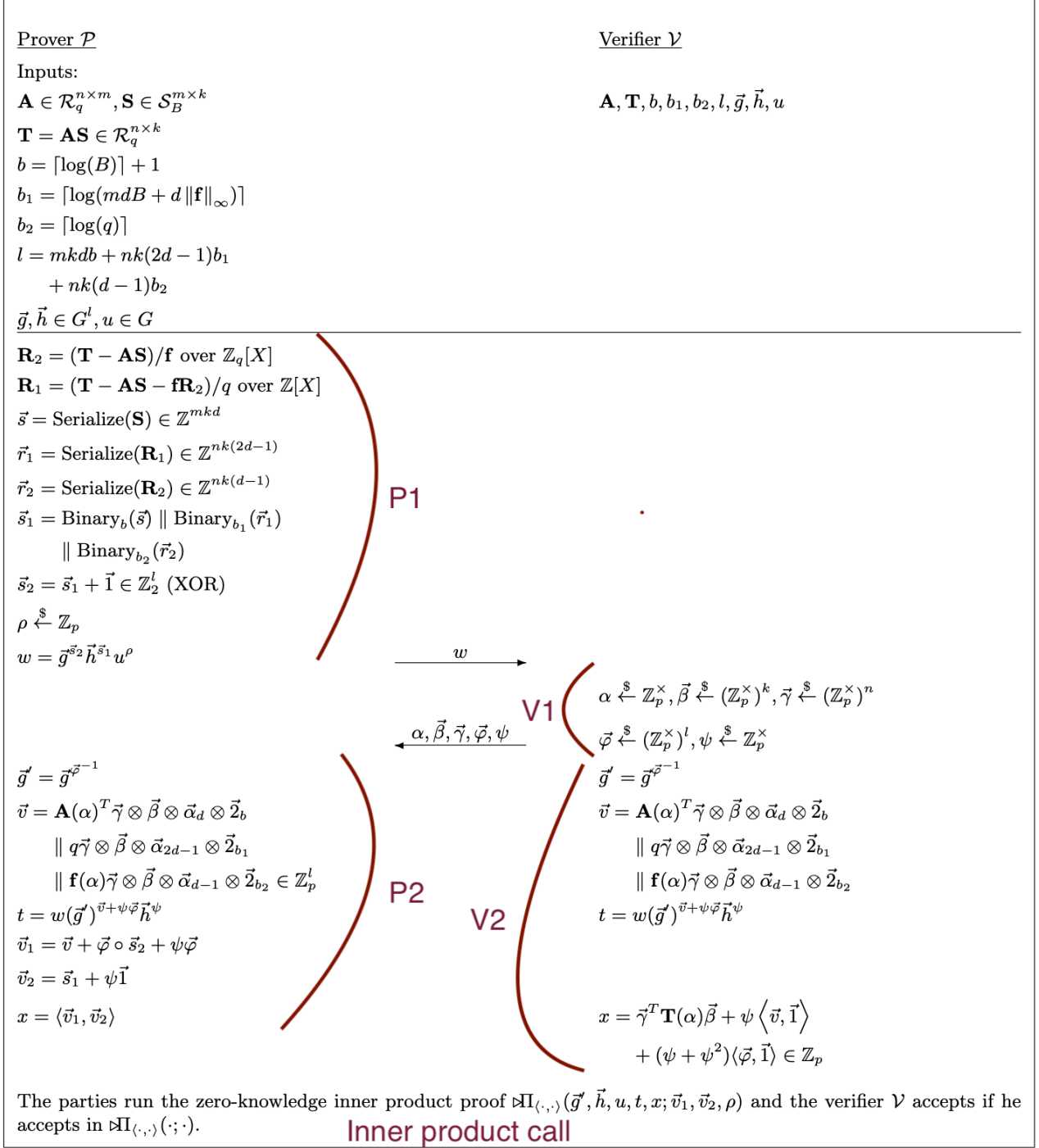


Figure 1: Main protocol

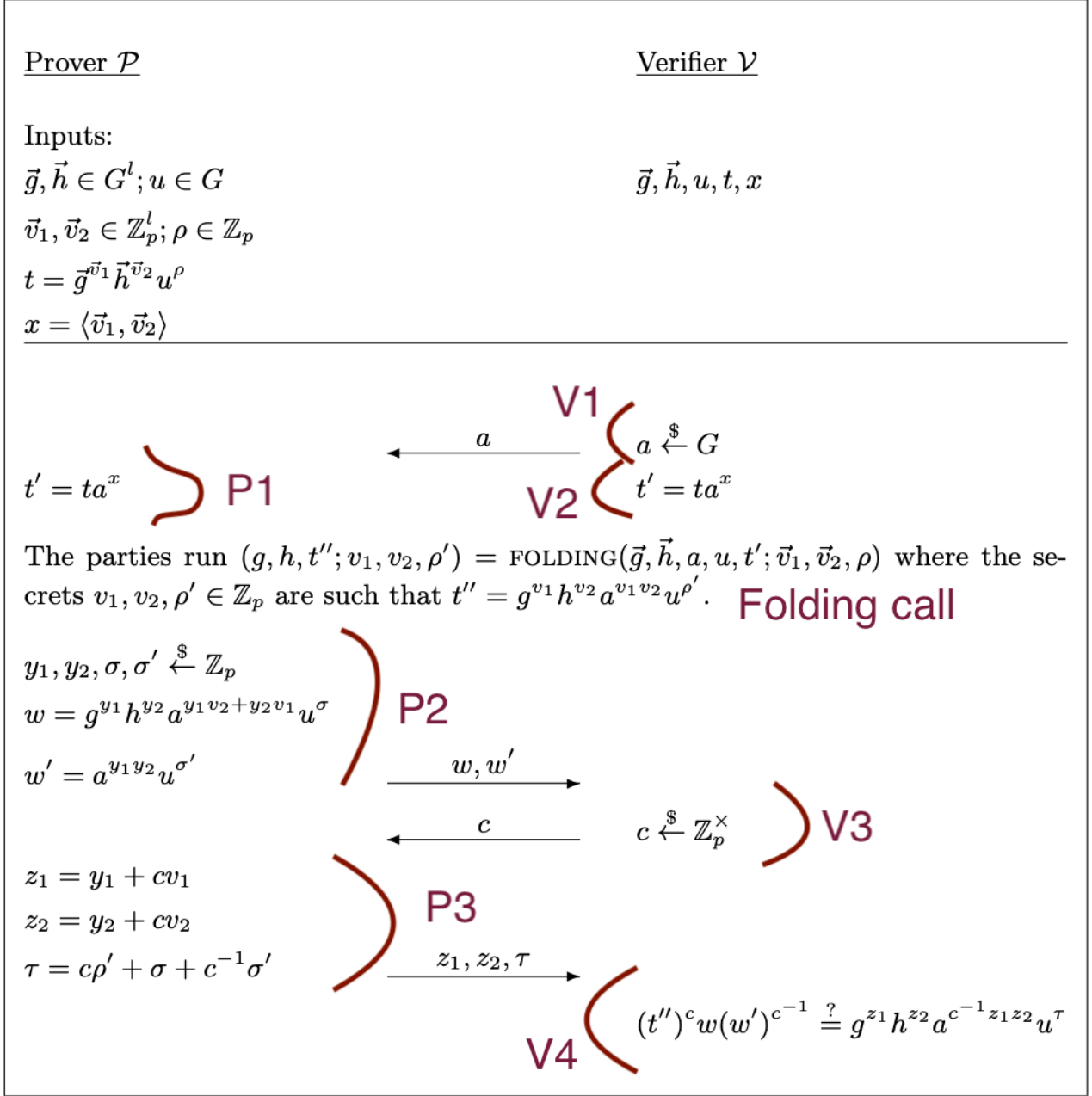


Figure 2: Inner product protocol

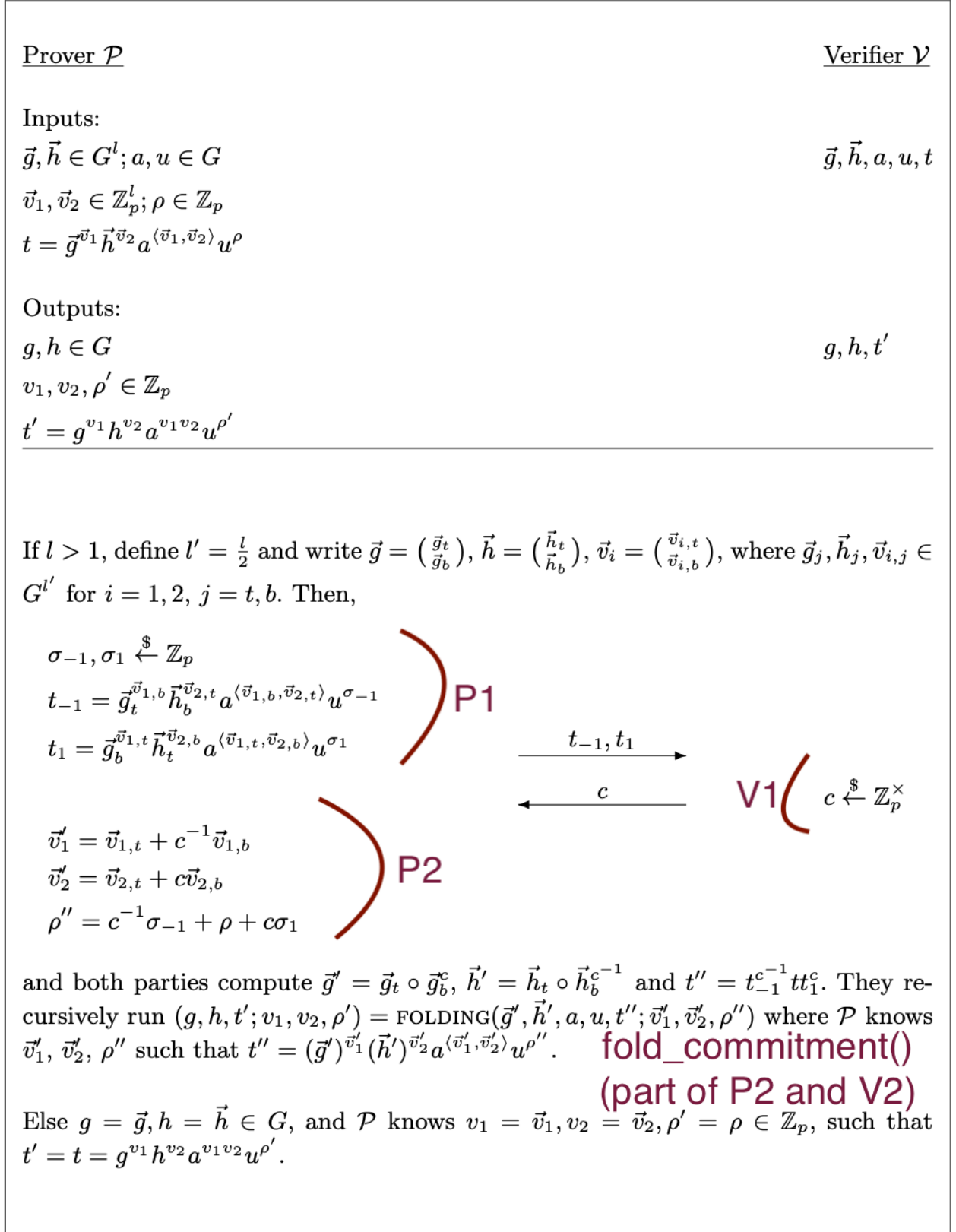


Figure 3: Folding protocol