Parameter set

Matrix equation sizes 2×4 and 4×1 ($n = 2$, $m = 4$, $k = 1$); bound $B = 4$; plaintext modulus $q = 8191$; Z_p prime p is the one used for scalar operations in SECP256k1 ($2^{256} - 2^{32} - 977$); modulus size 1024.

Measurements, SECP256k1

Network transfer

Transfer	size	size (compressed points)
Shared randomness (to verifier)	19.9 Mb	6.13 Mb
Random challenge (to prover)	3.05 Mb	3.05 Mb
Folding info (to verifier)	208 b × 17	64 b × 17
Folding info (to prover)	32 b × 17	32 b × 17
Inner product info (to verifier)	208 b	64 b
Inner product info (to prover)	32 b	32 b
Inner product info (to verifier)	96 b	96 b

Single thread, 6-Core Intel Core i7 @ 2.6 GHz

main	inner product	folding	time	main	inner product	folding	time
P1			702ms				
				V1			2.09ms
P2			25.9s	V2			25.6s
					V1		86.0µs
	P1		7.17ms		V2		7.99ms
		P1	21.5s				
						V1	31.5µs
		P2	16.6s			V2	16.6s
	P2		519µs				
					V3		2.29µs
	P3		118µs				
					V4 (check happens here)		759µs
Prover time			∼ 64 s	Verifier time			∼ 47 s

6 threads, 6-Core Intel Core i7 @ 2.6 GHz

main	inner product	folding	time	main	inner product	folding	time
P1			807ms				
				V1			2.09ms
P2			5.36s	V2			5.38s
					V1		109µs
	P1		32.7ms		V2		20.1ms
		P1	5.15s				
						V1	31.8µs
		P2	4.36s			V2	4.30s
	P2		611µs				
					V3		2.95µs
	P3		133µs				
					V4 (check happens here)		906µs
Prover time			∼ 14.9 s	Verifier time			∼ 9.7 s

Totals

	i7 @ 2.6 GHz, 1 thread	i7 @ 2.6 GHz, 6 threads
Prover time	70s	14.9s
Verifier time	47s	9.7s
Encryption time	1.18ms	=
Initial proof generation	16.0s	3.23s
Prover transfers	20 Mb (non-compressed) / 6 Mb (compressed)	
Verifier transfers	3 Mb	

Measurements, Curve25519

Note: not all optimized batch multiplications algorithms are in use here. The numbers can be improved.

Network transfer

Transfer	size
Shared randomness (to verifier)	30.6 Mb
Random challenge (to prover)	3.05 Mb
Folding info (to verifier)	320 b × 17
Folding info (to prover)	32 b × 17
Inner product info (to verifier)	320 b
Inner product info (to prover)	32 b
Inner product info (to verifier)	96 b

Single thread, 6-Core Intel Core i7 @ 2.6 GHz

main	inner product	folding	time	main	inner product	folding	time
P1			535ms				
				V1			5.25ms
P2			9.14s	V2			8.90s
					V1		20.0µs
	P1		50.9ms		V2		76.1ms
		P1	9.70s				
						V1	29.0µs
		P2	15.2s			V2	14.8s
	P2		234µs				
					V3		2.42µs
	P3		31.2µs				
					V4 (check happens here)		309µs
Prover time			∼ 34.6 s	Verifier time			∼ 23.7 s

6 threads, 6-Core Intel Core i7 @ 2.6 GHz

main	inner product	folding	time	main	inner product	folding	time
P1			564ms				
				V1			5.34ms
P2			2.09s	V2			2.10s
					V1		20.6µs
	P1		25.7ms		V2		33.4ms
		P1	2.37s				
						V1	30.0µs
		P2	3.15s			V2	3.08s
	P2		273µs				
					V3		3.70µs
	P3		37.2µs				
					V4 (check happens here)		329µs
Prover time			∼ 8.2 s	Verifier time			∼ 5.2 s

Totals

	i7 @ 2.6 GHz, 1 thread	i7 @ 2.6 GHz, 6 threads
Prover time	34.6s	8.2s
Verifier time	23.7s	5.2s
Encryption time	1.36ms	=
Initial proof generation	2.15s	434ms
Prover transfers	30.6 Mb	
Verifier transfers	3 Mb	

Notes

- The initial transfers consist mostly of random numbers. Their size can be reduced significantly (to the order of several bytes) if one can just transfer a random seed and trust the other party to generate the randoms.
- There are many consecutive transfers during folding stages. It may be possible to pack them into a single transfer from each size (the verifier prepares an array of randoms c, the prover calculates t_1, t_{-1} for each stage and sends them to the verifier), if it that does not compromise security.
- There is a large amount of identical calculations that both prover and verifier perform on the same data. If only one party can be trusted to perform them, performance can be significantly improved.

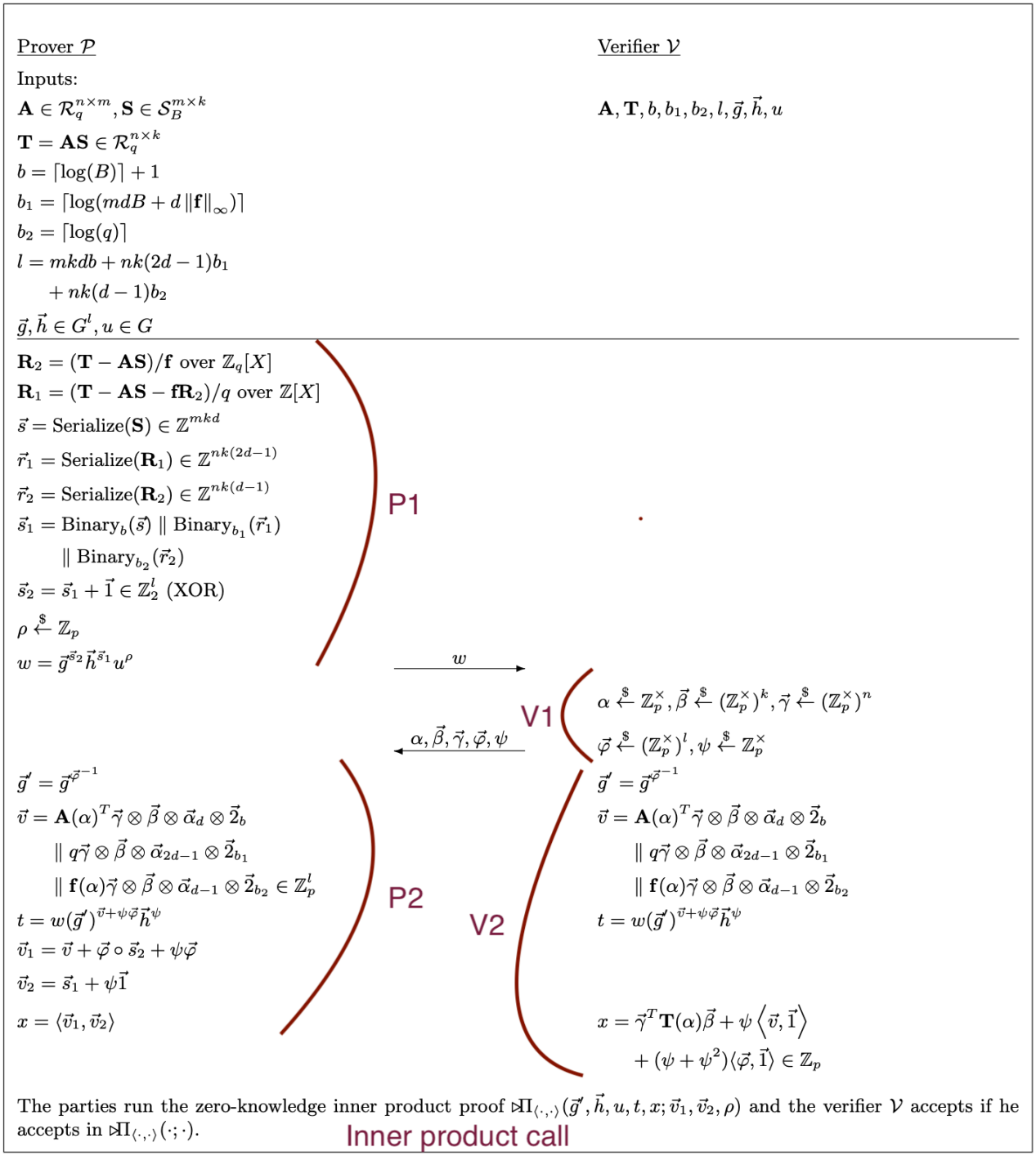

Figure 1: Main protocol

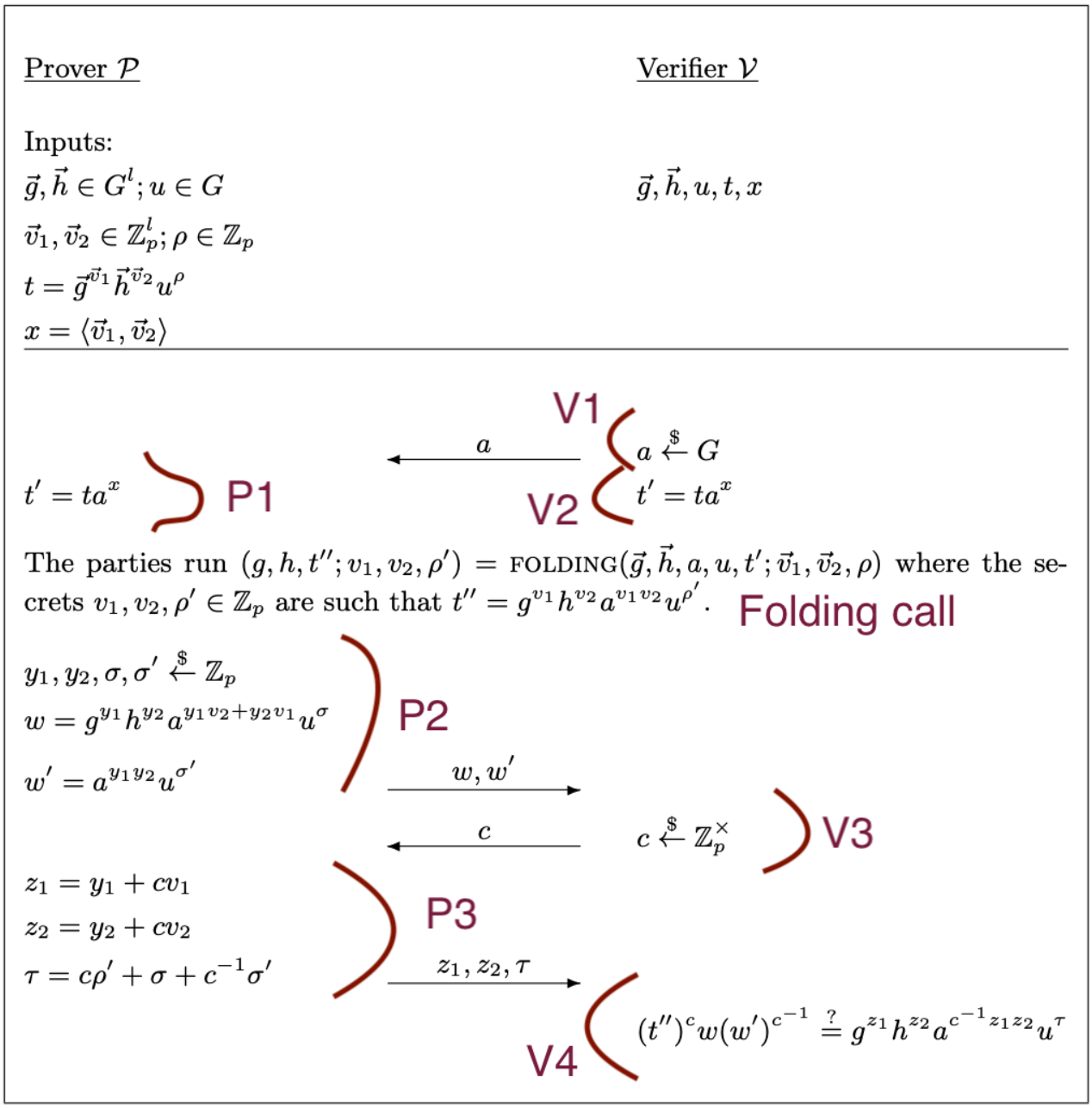

Figure 2: Inner product protocol

Prover $\mathcal{P}$		Verifier $\mathcal{V}$
Inputs:		
$\vec{g}, \vec{h} \in G^l; a, u \in G$		$\vec{g}, \vec{h}, a, u, t$
$\vec{v}_1, \vec{v}_2 \in \mathbb{Z}_p^l; \rho \in \mathbb{Z}_p$		
$t = \vec{g}^{\vec{v}_1} \vec{h}^{\vec{v}_2} a^{\langle \vec{v}_1, \vec{v}_2 \rangle} u^{\rho}$		
Outputs:		
$g, h \in G$		g, h, t'
$v_1, v_2, \rho' \in \mathbb{Z}_p$		
$t' = g^{v_1} h^{v_2} a^{v_1 v_2} u^{\rho'}$		

If $l > 1$, define $l' = \frac{l}{2}$ and write $\vec{g} = \begin{pmatrix} \vec{g}_t \\ \vec{g}_b \end{pmatrix}$, $\vec{h} = \begin{pmatrix} \vec{h}_t \\ \vec{h}_b \end{pmatrix}$, $\vec{v}_i = \begin{pmatrix} \vec{v}_{i,t} \\ \vec{v}_{i,b} \end{pmatrix}$, where $\vec{g}_j, \vec{h}_j, \vec{v}_{i,j} \in G^{l'}$ for $i = 1, 2$, $j = t, b$. Then,

$\sigma_{-1}, \sigma_1 \xleftarrow{\$} \mathbb{Z}_p$

$t_{-1} = \vec{g}_t^{\vec{v}_{1,b}} \vec{h}_b^{\vec{v}_{2,t}} a^{\langle \vec{v}_{1,b}, \vec{v}_{2,t} \rangle} u^{\sigma_{-1}}$

$t_1 = \vec{g}_b^{\vec{v}_{1,t}} \vec{h}_t^{\vec{v}_{2,b}} a^{\langle \vec{v}_{1,t}, \vec{v}_{2,b} \rangle} u^{\sigma_1}$

P1

$t_{-1}, t_1 \longrightarrow$

$\longleftarrow c$

V1 $c \xleftarrow{\$} \mathbb{Z}_p^\times$

$\vec{v}_1' = \vec{v}_{1,t} + c^{-1} \vec{v}_{1,b}$

$\vec{v}_2' = \vec{v}_{2,t} + c \vec{v}_{2,b}$

$\rho'' = c^{-1} \sigma_{-1} + \rho + c \sigma_1$

P2

and both parties compute $\vec{g}' = \vec{g}_t \circ \vec{g}_b^c$, $\vec{h}' = \vec{h}_t \circ \vec{h}_b^{c^{-1}}$ and $t'' = t_{-1}^{c^{-1}} t t_1^c$. They recursively run $(g, h, t'; v_1, v_2, \rho') = \text{FOLDING}(\vec{g}', \vec{h}', a, u, t''; \vec{v}_1', \vec{v}_2', \rho'')$ where $\mathcal{P}$ knows $\vec{v}_1'$, $\vec{v}_2'$, ρ'' such that $t'' = (\vec{g}')^{\vec{v}_1'} (\vec{h}')^{\vec{v}_2'} a^{\langle \vec{v}_1', \vec{v}_2' \rangle} u^{\rho''}$.

fold_commitment()
(part of P2 and V2)

Else $g = \vec{g}, h = \vec{h} \in G$, and $\mathcal{P}$ knows $v_1 = \vec{v}_1, v_2 = \vec{v}_2, \rho' = \rho \in \mathbb{Z}_p$, such that $t' = t = g^{v_1} h^{v_2} a^{v_1 v_2} u^{\rho'}$.

Figure 3: Folding protocol