

J9 USB-B

SW1
BOOT

J7 NET

J9
AUDIO OUT

J11 MIDI IN

AGND_DAC

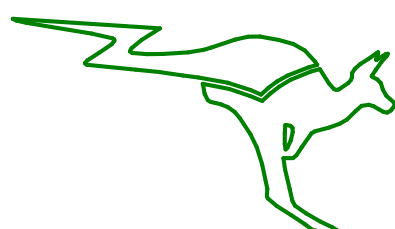
3V3A_DAC

GND

GND

3V3_DAC

3V3



Cardinia Pro

David Ng 2024

Rev A2

J15 GPIO

3V3
GPIO1
GPIO2
GPIO3
GPIO4
GND

J12 MIDI OUT

SW2
CONFIG

CFG4
CFG3
MENU
P7/8

R51
R50
R49
R44

BCLK
DIN
LRCLK

C22
R6
R7
U3

U10
C33

R36
C59
GND

C8
C7
C15
R37
R38
R39
R40
R41
R42
R43

R18 R20 R22 R23
R17 R16
C25 C30 C29
C28 C26 C27
R1 R2 R4 R5
LD6 LD5 LD4
LD1 3.3V
R3
GND
RXD TXD
Debug UART

C12 C14 C6 C40 C39
C38
C20 Y1
C19 C13 C16

LD3 LD2

R28
FB4

U5
VBUS

5V
3.3V

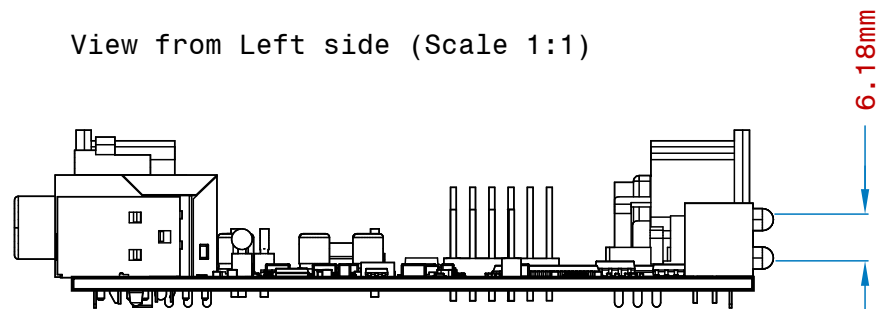
LD1 3.3V

3V3
TXD
RXD
GND

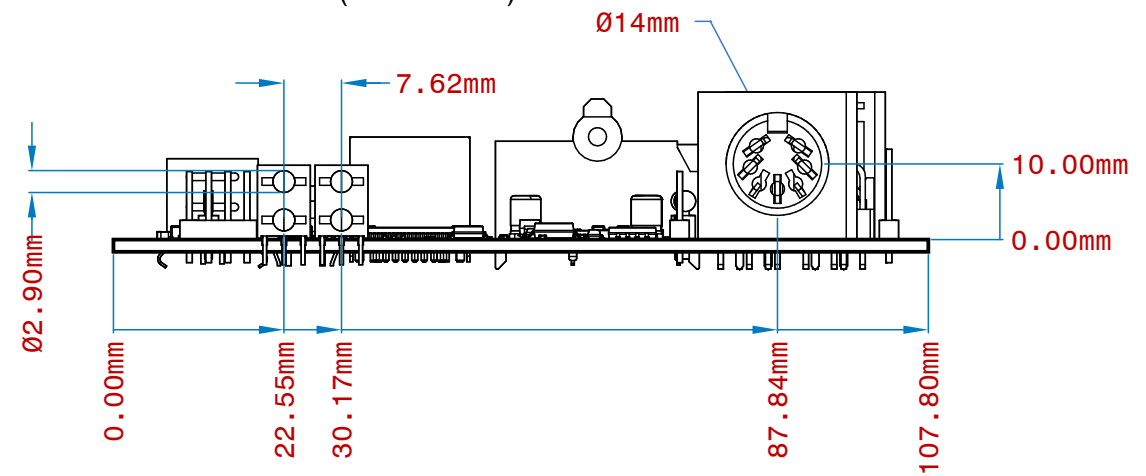
J5 SWD

GND

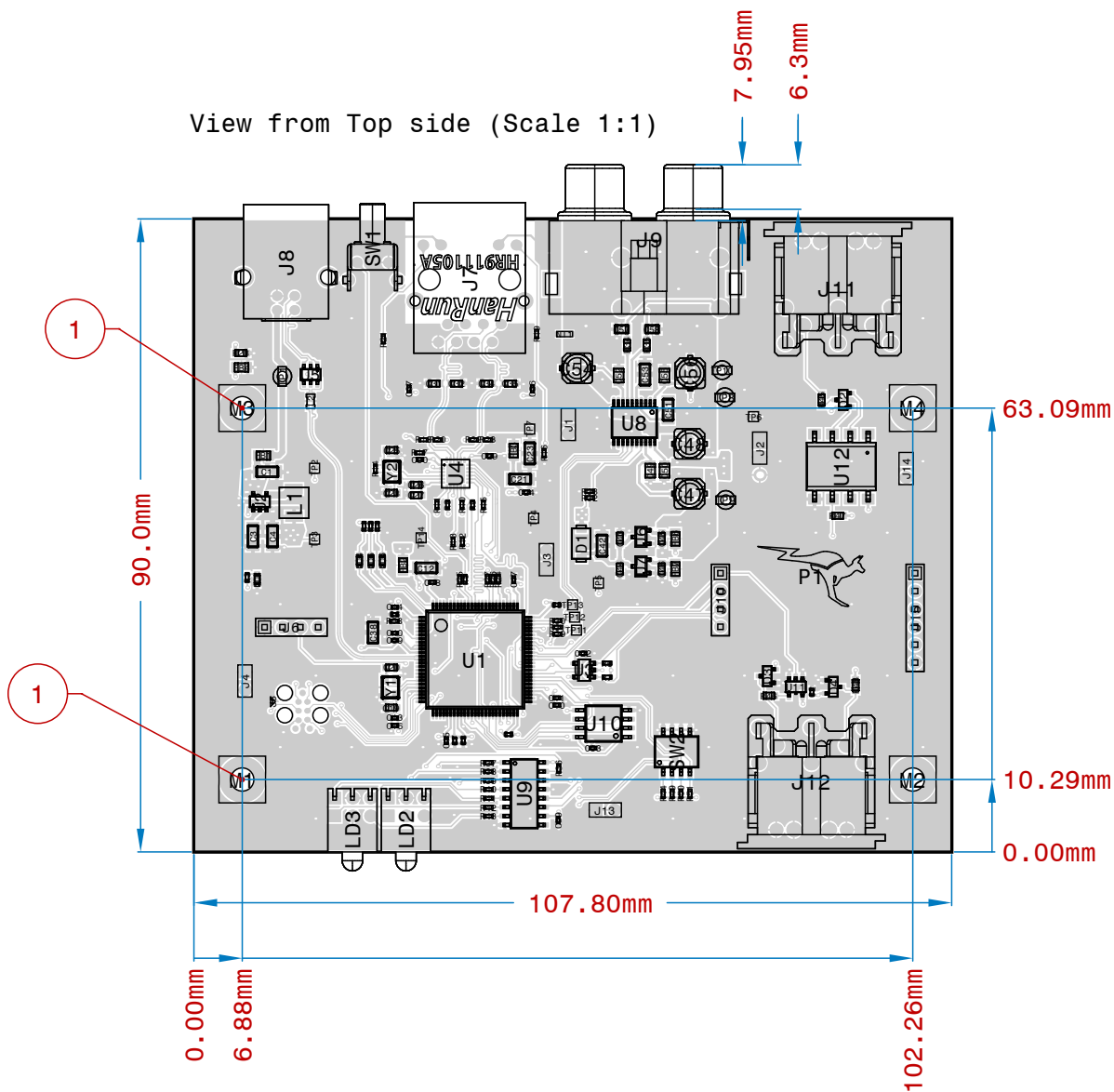
View from Left side (Scale 1:1)



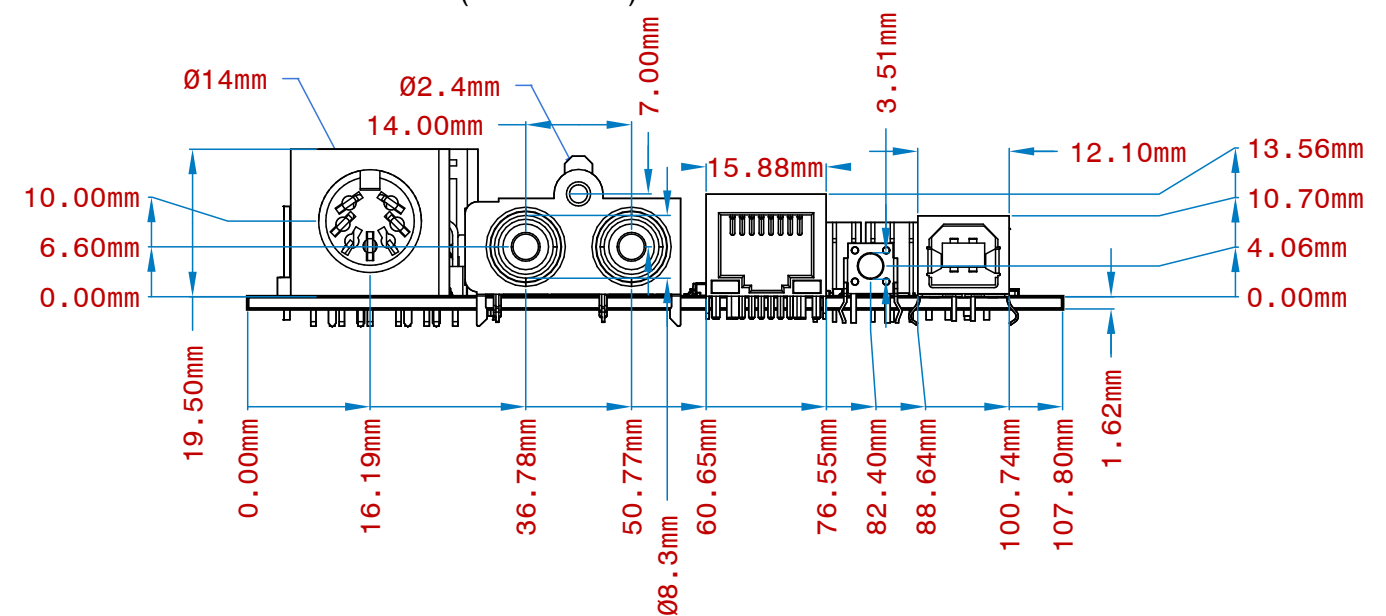
View from Front side (Scale 1:1)



View from Top side (Scale 1:1)



View from Back side (Scale 1:1)

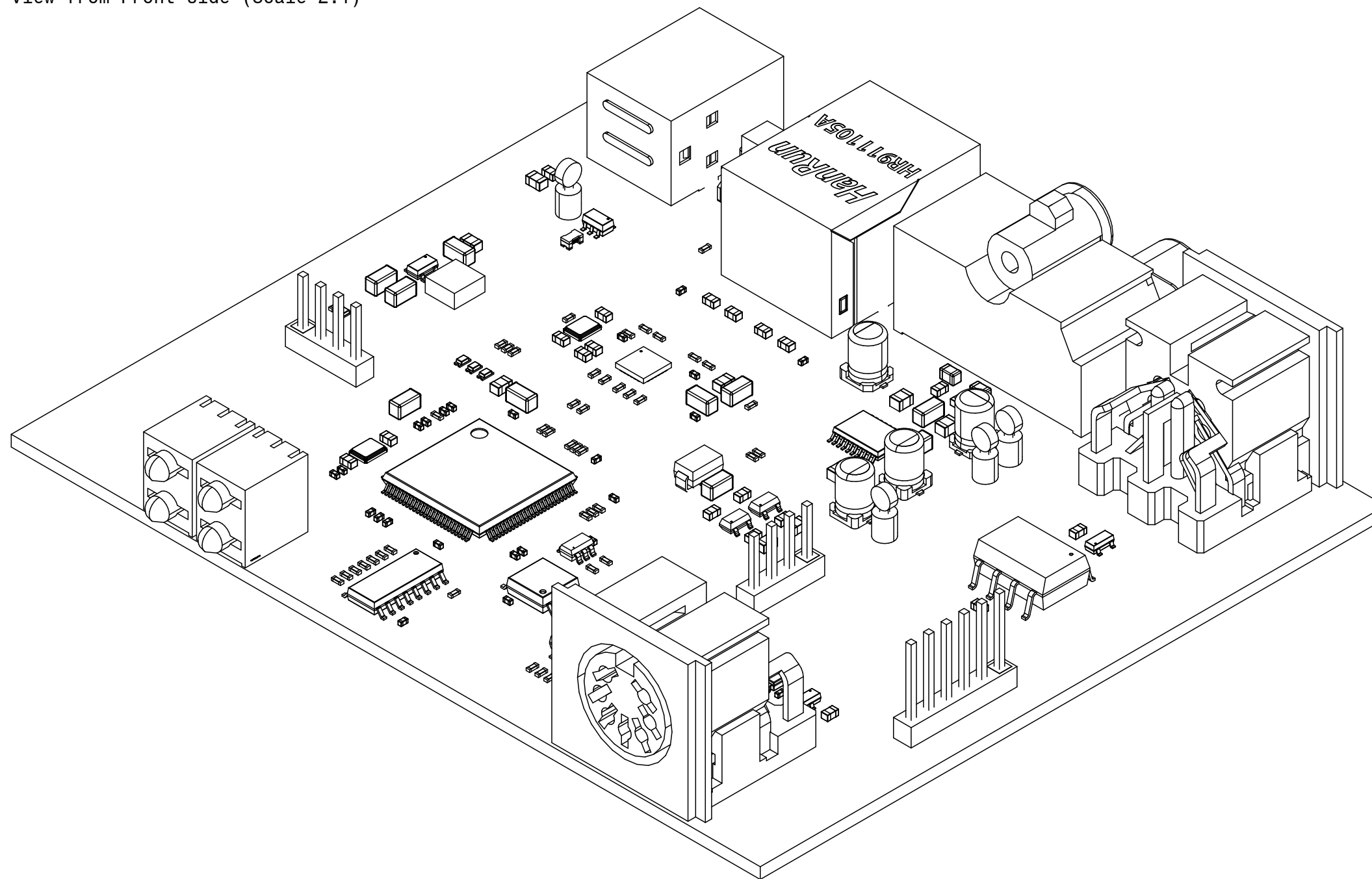


Notes:

- ① M3 Mounting Hole

APPROVALS		DATE	PROJECT		
ENG:		=ENG_APP_DATE	=Project		
DSN:		=DSN_APP_DATE	PROJECT REVISION:		
CHK:		=CHK_APP_DATE	DOCUMENT REVISION:		
			DESIGN ITEM:		
REFERENCE DOCUMENTS		=title			
BOM:		=DOC_NO_BOM			
ASSY DWG:		=DOC_NO_ASSY_DWG	CAGE CODE	DWG NO.	REV
FAB DWG:		=DOC_NO_FAB_DWG	=DOC_NO_SCH_DWG		
PCB DWG:		=PCB_DWG_NO	SCALE: =SCH_SCALE	FILE NAME	Cardinia Pro.PCBDwf
			SHEET 1 OF 2		

View from Front side (Scale 2:1)



APPROVALS	DATE	PROJECT			
ENG:	=ENG_APP_DATE	=Project			
DSN:	=DSN_APP_DATE	PROJECT REVISION:	DOCUMENT REVISION:	DESIGN ITEM:	
CHK:	=CHK_APP_DATE				
REFERENCE DOCUMENTS		=title			
BOM:	=DOC_NO_BOM				
ASSY DWG:	=DOC_NO_ASSY_DWG	CAGE CODE	DWG NO.	REV	
FAB DWG:	=DOC_NO_FAB_DWG	A3=CAGE	CODE=DOC_NO_SCH_DWG		
PCB DWG:	=PCB_DWG_NO	SCALE=	SCH SCALE	Cardinia Pro.PCB	Dwf
				SHEET 2	OF 2