Experiment: (11)

Title: Sequence Detectors (Johnson, Ring Counters)

Theory: A Ring counter in a type of digital sequential circuit built from a shift-register whose serial output is fet back to its serial input, forming a closed ring. The output of the last-flip-flop is nowted back to the output apput of the first, so each clock pluse shifts the single "1" one position around the ring.

Johnson counters is a switch-tail ring counters where the complement output of the last flip-flop connected to the input of the first flip-flop.

Required Instruments: i) D-flip-flop (4013)
ii) Logic Probe
iii) Logic State
iv) Clock V) breadboard V) LED Vi) Wres

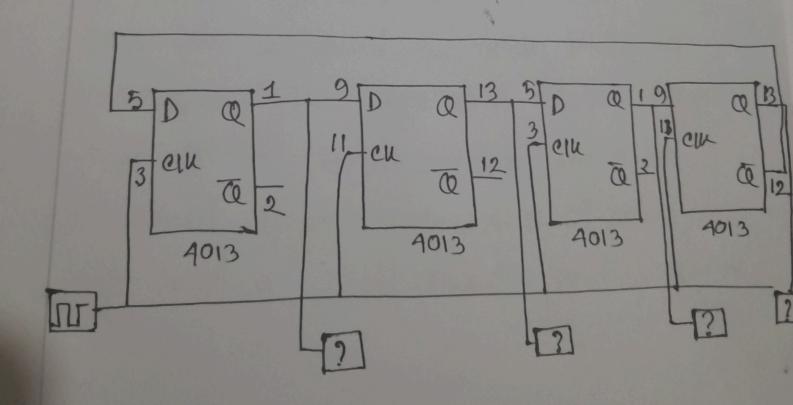
Vii) power supply

nuth	-table:	Ring (counter	
CILL sear	A	B	C	E
	1	0	0	0
2	0	1	0	0
3	0	O	1	0
14	0	0	0	1
5	1	0	0	O
6	0	1	O	0
7	0	0	1	0
8	0	0	0	1
9	4	0	0	0

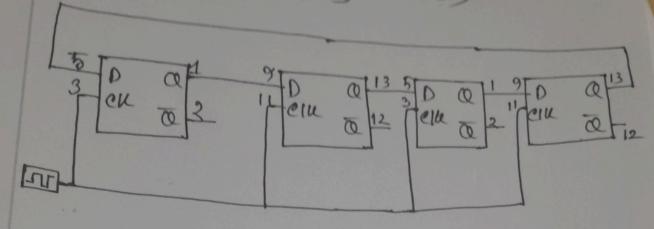
trouth table: (Johnson counters)

Sea. 1		- Courters		
1	A	В	C	E
2	10	0	0	0
3	1	10	0	0
4	1	1	0	0
5	1	1	1	0
6	1	1	1	1
Z	0	1	1	1
1	0	0	1	1
8	0	0	0	1

logic Diagnam: (Johnson Counters)



logie Diagnam: (Ring counter)



Conclusion: This expriment nucentully demonstrated the working of requerice detectors. Johnson counters and ring counters we used the truth table to examines the operation of the basic logic gates. Both counters give a self contained, repeating sequence of states with just a shift register and feedback.