CSE 565 - Homework 1

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1 Question

Consider a desktop publishing system used to produce documents for various organizations.

a Give an example of a type of publication for which confidentiality of the stored data is the most important requirement.

For publishing student performance report, confidentiality has prime importance. By law, only student, their parents and the employers given access by the students should have access to the reports.

b Give an example of a type of publication in which data integrity is the most important requirement.

Data integrity is the most important requirement for corporate financial reports. If the data is somehow tempered with, it not only affects company but all the stakeholders involved which in turn destroys the reputation of the company.

c Give an example in which system availability is the most important requirement.

In this case system availability is most important for news paper companies that are getting served. Newspaper companies work within a time constraint and the service must be available without fail to fulfill that requirement.

2 Question

Consider the following general code for allowing access to a resource:

```
DWORD dwRet = IsAccessAllowed(...);
if (dwRet == ERROR_ACCESS_DENIED) {
    // Security check failed.
    // Inform user that access is denied.
} else {
    // Security check OK.
}
```

a Explain the security flaw in this program.

The code is expecting a value which denotes if the permission is denied to the user. If it is denied the system is informing the user that they do not have sufficient permission to perform the task. The programmer failed to recognize it will give access to the user in other cases. Besides proper authentication, if network fails or exception occurs, this snippet of code will allow the user past the security check.

b Rewrite the code to avoid the flaw. Hint: Consider the design principle of fail-safe defaults.

```
DWORD dwRet = IsAccessAllowed(...);
if (dwRet != SUCCESS_ACCESS_ALLOWED) {
    // Security check failed.
    // Inform user that access is denied.
} else {
    // Security check OK.
}
```

3 Question

Consider the block encryption algorithm TEA described in problem 2.4 in the textbook. In the description, \oplus denotes bitwise exclusive OR (XOR), \boxplus denotes addition modulo 2^{32} , $x \ll y$ denotes circular left shift of value x by y bits, and $x \gg y$ denotes circular right shift of x by y bits.

a Suppose we are given this algorithm that consists only of two rounds and produces ciphertext $C = (L_2, R_2)$. Express the ciphertext as a function of the input, i.e., the message and key blocks as well as the constants δ_i .

Let,
$$G(L_0, R_0)$$
 is the function where $G(L_0, R_0) = C = (L_2, R_2)$
As $L_i = R_{i-1}$ and $R_i = L_{i-1} \boxplus F(R_{i-1}, K_i, K_k, \delta_i)$

where F is defined as

$$F(M, K_j, K_k, \delta_i) = ((M \ll 4) \boxplus K_j) \oplus ((M \gg 5) \boxplus K_k) \oplus (M + \delta_i)$$

Replacing L_2 and R_2 accordingly we get:

$$G(L_0, R_0) = (R_1, L_1 \boxplus (((R_1 \ll 4) \boxplus K_2) \oplus ((R_1 \gg 5) \boxplus K_3) \oplus (R_1 + \delta_2))$$

Following the same principle, replacing L_1 and R_1 we get:

$$G(L_0, R_0) = ((L_0 \boxplus (((R_0 \ll 4) \boxplus K_0) \oplus ((R_0 \gg 5) \boxplus K_1) \oplus (R_0 + \delta_1))) , R_0 \boxplus ((((L_0 \boxplus (((R_0 \ll 4) \boxplus K_0) \oplus ((R_0 \gg 5) \boxplus K_1) \oplus (R_0 + \delta_1))) \ll 4) \boxplus K_2) \oplus (((L_0 \boxplus (((R_0 \ll 4) \boxplus K_0) \oplus ((R_0 \gg 5) \boxplus K_1) \oplus (R_0 + \delta_1))) \gg 5) \boxplus K_3) \oplus ((L_0 \boxplus (((R_0 \ll 4) \boxplus K_0) \oplus ((R_0 \gg 5) \boxplus K_1) \oplus (R_0 + \delta_1))) + \delta_2))$$

b Assume that the constants δ_i are publicly known. You don't have the knowledge of the key, but can mount the chosen plaintext attack (i.e., request ciphertexts on messages of your choice). Given this ability, what information can you learn about the key using the 2-round version of TEA? Justify your answer.

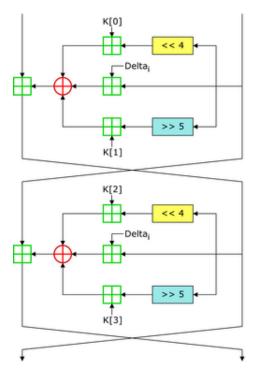


Figure 1: Two Feistel rounds (one cycle) of TEA [1]

We know from the answer 3a that,

$$L_2 = (L_0 \boxplus (((R_0 \ll 4) \boxplus K_0) \oplus ((R_0 \gg 5) \boxplus K_1) \oplus (R_0 + \delta_1)))$$

As we have access to cipher-texts of the messages of our choice, we know the values of L_2 , L_0 and R_0 , replacing those values by constants we get the equation of

$$C_2 = (C_0 \boxplus ((C_1 \boxplus K_0) \oplus (C_2 \boxplus K_1) \oplus C_3))$$

or,
$$C_4 = (C_1 \boxplus K_0) \oplus (C_2 \boxplus K_1)$$

From the above equation we can guess the values of K_0 , K_1 and try those values in another equation derived from a new plaintext and ciphertext pair. Since each key is 32bit, we may need 2^{32} guesses to solve K_0 , K_1 .

Following the above technique, we will need another 2^{32} guesses to solve K_2 , K_3 .

In conclusion, if a user is allowed to mount a plain-text attack in a 2-round version of TEA, they can derive the value of keys in $2.2^{32} = 2^{33}$ iterations.

c Would the answer described above change if the 4-round version of TEA is used instead? Justify your answer.

With the 4-rounds version, the complexity will change exponentially. With 2 Feistel rounds, we have 2 unknown K variables in the equations (i.e. $C_4 = (C_1 \boxplus K_0) \oplus (C_2 \boxplus K_1)$). But 4-round equations will involve 4 unknown variables: K_0 , K_1 , K_2 and K_3 , which means the number of guesses required will be significantly higher. If we visualize this, we have to plot the data point obtained from multiple plaintext-ciphertext pairs in 4-Dimensions space where axes are K_0 , K_1 , K_2 and K_3 . Comparatively, for 2-rounds equations, we will create 2 graphs in 2-dimension space consisting of K_0 , K_1 and K_2 , K_3 .

4 Question

Read about AES-NI and research its use in programs.

a Determine a way to use hardware accelerated AES in one programming language. Provide a segment of code to encrypt one block (16 bytes) of plaintext using AES hardware instructions such as aesenc, aesenclast, etc. Assume that the plaintext is initially stored in a binary buffer (array) and a 128-bit key is also stored in a binary buffer. Executing your code segment should result in a 16-byte cipher block.

```
#include <stdio.h>
1
     #include <stdint.h>
2
     #include <wmmintrin.h> //AES-NI intrinsics library
3
     static __m128i key_expansion(__m128i key,__m128i generated_key){
10
         generated_key = _mm_shuffle_epi32(generated_key, _MM_SHUFFLE(3,3,3,3));
         key = _mm_xor_si128(key, _mm_slli_si128(key, 4));
         key = _mm_xor_si128(key, _mm_slli_si128(key, 4));
         key = _mm_xor_si128(key, _mm_slli_si128(key, 4));
13
         return _mm_xor_si128(key, generated_key);
14
15
16
     static void printOutput(uint8_t *val){
17
         for (int i = 0; i < 16; ++i)
18
19
             printf("0x%x ", val[i]);
20
21
     }
22
23
24
     int main(void){
25
26
         uint8_t plainText[] = {0x32, 0x43, 0xf6, 0xa8, 0x88, 0x5a, 0x30, 0x8d, 0x31, 0x31, 0x98, 0xa2,
27
                                  0xe0, 0x37, 0x07, 0x34;
28
         uint8_t enc_key[] = {0x2b, 0x7e, 0x15, 0x16, 0x28, 0xae, 0xd2, 0xa6, 0xab, 0xf7, 0x15, 0x88, 0x09,
29
                                  0xcf, 0x4f, 0x3c};
         uint8_t computed_cipher[16];
31
33
34
35
```

```
Key Scheduling is being performed here
37
                  Expansion of given cipher key into 11 128 bit partial keys
38
                   used in one initial round, 9 main rounds and one final round
39
40
41
         __m128i key_schedule[11];
42
43
         key_schedule[0] = _mm_loadu_si128((const __m128i*) enc_key);
44
         key_schedule[1] = key_expansion(key_schedule[0], _mm_aeskeygenassist_si128(key_schedule[0], 0x01));
45
         key_schedule[2] = key_expansion(key_schedule[1], _mm_aeskeygenassist_si128(key_schedule[1], 0x02));
46
         key_schedule[3] = key_expansion(key_schedule[2], _mm_aeskeygenassist_si128(key_schedule[2], 0x04));
47
         key_schedule[4] = key_expansion(key_schedule[3], _mm_aeskeygenassist_si128(key_schedule[3], 0x08));
48
         key_schedule[5] = key_expansion(key_schedule[4], _mm_aeskeygenassist_si128(key_schedule[4], 0x10));
49
         key_schedule[6] = key_expansion(key_schedule[5], _mm_aeskeygenassist_si128(key_schedule[5], 0x20));
50
         key_schedule[7] = key_expansion(key_schedule[6], _mm_aeskeygenassist_si128(key_schedule[6], 0x40));
51
         key_schedule[8] = key_expansion(key_schedule[7], _mm_aeskeygenassist_si128(key_schedule[7], 0x80));
52
         key_schedule[9] = key_expansion(key_schedule[8], _mm_aeskeygenassist_si128(key_schedule[8], 0x1B));
53
         key_schedule[10] = key_expansion(key_schedule[9], _mm_aeskeygenassist_si128(key_schedule[9], 0x36));
         /* Encyption process starts here */
60
61
         __m128i state_array = _mm_loadu_si128((__m128i *) plainText);
62
63
64
         /* Initial round */
65
66
         state_array = _mm_xor_si128(state_array, key_schedule[ 0]);
67
68
69
                  9 main rounds
70
                  each round consists of 4 operations: SubBytes, ShiftRows, MixColumns, AddRoundKeys
71
                  using \_mm\_aesenc\_si128 we are performing these 4 operations in one instruction
72
         */
73
74
         for (int i = 1; i < 10; ++i){
75
                 state_array = _mm_aesenc_si128(state_array, key_schedule[i]);
         /*
80
                  Final round consists of 3 operations: SubBytes, ShiftRows, AddRoundKeys
81
                  using _mm_aesenclast_si128 we are performing these 3 operations in one instruction
82
83
84
         state_array = _mm_aesenclast_si128(state_array, key_schedule[10]);
85
86
         _mm_storeu_si128((__m128i *) computed_cipher, state_array);
87
88
89
         printOutput(plainText);
90
91
         printf("\n");
92
         printOutput(computed_cipher);
93
94
         return 0;
95
```

Figure 2: AES-NI program output: Given plain text and computed cipher text

b Is hardware accelerated AES available in all programming languages? Explain.

Currently, only languages which support hardware accelerated AES are C/C++. Most of the high-level programming languages restrict users from using SIMD directly. C# provides partial SIMD support with Mono framework and languages like Java, Python, Ruby does have libraries which are just an abstraction layer on top of C implementations. Some of them have plans to implement SIMD in the future whereas most of them stand against implementing this to provide security and stability. So at the moment, the only way to take full advantage of AES hardware acceleration is using C/C++.

c List resources that were useful in working on this problem.

Below resources helped me to work on the problem:

- Computer Security: Principles and Practice (3rd Edition) [2]
- Intel® Advanced Encryption Standard Instructions (AES-NI) [3]
- Network security- aes (advanced encryption standard) algorithm [4]
- AES Rijndael Cipher Visualization [5]
- Playing with AES intrinsics [6]
- Using Intel AES-NI and c++ threads to search an AES128 key [7]
- High-Level Programming Languages Should Improve Support for SIMD Instructions [8]

5 Question

a Find an alternative implementation of AES in the form of a cryptographic library, preferably one that doesn't use AES-NI. Write a very small program using that library for accomplishing the task of the previous problem using that library, i.e., for encrypting a 1-block message with a 128-bit key in the ECB mode. Submit the name of the library, the programming language that you used (it can be different from the language in problem 4), and the code.

For implementing AES I have used Java as the programming language. It has in-build extensive support for Cryptography. Crypto library from Javax package proved useful to implement this.

```
import javax.crypto.Cipher;
import javax.crypto.spec.SecretKeySpec;

public class AES {
```

```
private static SecretKeySpec secretKey;
         private static final byte[] plainText = { (byte) 0x32, (byte) 0x43, (byte) 0xf6, (byte) 0xa8,
                      (byte) 0x88, (byte) 0x5a, (byte) 0x30, (byte) 0x8d, (byte) 0x31, (byte) 0x31,
                      (byte) 0x98, (byte) 0xa2, (byte) 0xe0, (byte) 0x37, (byte) 0x07, (byte) 0x34 };
         private static final byte[] encKey = { (byte) 0x2b, (byte) 0x7e, (byte) 0x15, (byte) 0x16,
12
                      (byte) 0x28, (byte) 0xae, (byte) 0xd2, (byte) 0xa6, (byte) 0xab, (byte) 0xf7,
13
                      (byte) 0x15, (byte) 0x88, (byte) 0x09, (byte) 0xcf, (byte) 0x4f, (byte) 0x3c };
14
15
16
17
         public static void printOutput(byte[] bytes) {
18
             StringBuilder sb = new StringBuilder();
19
             sb.append("[ ");
20
             for (byte b : bytes) {
21
                  sb.append(String.format("0x%02X ", b));
22
             }
23
             sb.append("]");
24
             System.out.println(sb.toString());
25
         }
26
27
28
         public static void main(String[] args)
29
         {
31
             byte[] computedCipher = null;
32
33
34
             try
             {
35
                  secretKey = new SecretKeySpec(encKey, 0,16,"AES");
36
37
                  Cipher cipher = Cipher.getInstance("AES/ECB/noPadding");
38
                  cipher.init(Cipher.ENCRYPT_MODE, secretKey);
39
                  computedCipher = cipher.doFinal(plainText);
40
41
             catch (Exception e)
42
             {
43
                  System.out.println("Error: " + e.toString());
44
             }
45
46
             printOutput(plainText);
47
             printOutput(computedCipher);
48
49
50
         }
51
```

```
Run: AES ×

//Library/Java/JavaVirtualMachines/jdk-10.0.2.jdk/Contents/Home/bin/java "-javaagent:/Applications/Intelli:
[ 0x32 0x43 0xF6 0xA8 0x88 0x5A 0x30 0x8D 0x31 0x98 0xA2 0xE0 0x37 0x07 0x34 ]
[ 0x39 0x25 0x84 0x1D 0x02 0xDC 0x09 0xFB 0xDC 0x11 0x85 0x97 0x19 0x6A 0x0B 0x32 ]

Process finished with exit code 0
```

Figure 3: AES program output: Given plain text and computed cipher text

b Compare the speed of the two implementations. For improved accuracy, run each program to encrypt 1000 1-block messages and report the total time for each of the two programs. If key expansion uses separate code, execute key expansion once followed by 1000 1-block encryptions. Report the times for both implementations and comment on the differences or similarities and any performance conclusions that you can provide.

```
Homework_1 — -bash — 80×24

[Amlans-Air:Homework_1 amlan$ gcc -o aesni aesni.c -march=native;
[Amlans-Air:Homework_1 amlan$ time ./aesni

real @m@.007s
ay user @m@.002s
sys @m@.003s
Amlans-Air:Homework_1 amlan$

i =
_ar

ay
```

Figure 4: AES-NI C program execution time for 1000 1-block encryptions



Figure 5: AES Java program execution time for 1000 1-block encryptions

From the above-mentioned reports, we can understand that doing the encryption using AESNI is faster than AES. The difference becomes more evident with a bigger dataset (i.e. For 10000 1-block encryptions, the duration of runtime is 14 and 62 milliseconds respectively). Though there is some overhead due to the choice of programming languages and environments, the difference in performance is quite evident.

References

- [1] Matthew D. Russell. Tinyness: An overview of tea and related ciphers, 27 Feb 2004. [Online; accessed April 27, 2013].
- [2] William Stallings; Lawrie Brown. Computer Security: Principles and Practice (3rd Edition). Abe Books, 2014.
- [3] Jeffrey Rott (Intel). Intel® advanced encryption standard instructions (aes-ni). 2012.
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- [5] Aes rijndael cipher visualization, 2011. https://www.youtube.com/watch?v=mlzxpkdXP58.
- [6] Vincent Hanquez. Playing with aes intrinsics. 2012.

- [7] Sebastien Riou. Using intel aes-ni and c++ threads to search an aes128 key, 2017.
- [8] Gastón Hillar. High-level programming languages should improve support for simd instructions. 2011.