

Fault Localization & Relevance Analysis

(For the latest version: <http://numairmansur.github.io/Error-Localization/project.pdf>)

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1 Introduction

The most tedious task in a programmer's routine is to spend time on debugging and to determine the cause of the error. Debugging can be made much simpler if we can determine automatically what program segments are actually responsible for the error. This process of automatically determining error locations is called "**Fault Localization**".

Fault Localization encompasses the task of identification of the program statements that are **relevant** for the error trace and determining the variables whose values should be tracked in order to understand the cause of the error.

There can be many notions of error relevancy. During my master praktikum, I worked with and developed algorithm for two new relevancy criterion, which we called Flow-Sensitive and non Flow-Sensitive error relevancy. A part of this work will be to formalize these two new types of error relevancy. But first we need a basic formal definition to "relevancy", that is, what does it mean that a statement is relevant for an error. Surprisingly enough, I was unable to find a formal and a generally accepted definition of error relevancy in the literature. But this also makes sense because relevancy is not a well established concept and is a hard thing to define, let alone formally. Therefore, another aim of this project is to try to give a formal definition to error relevancy.

1.1 Relevant Work

In the paper Flow-Sensitive Fault Localization [1], the authors take into account the flow of the program while determining the statements that are relevant for the error. They do it by a new flow-sensitive encoding of error traces into trace formulas. After which, by identifying the irrelevant portions of the new trace formulas, we can isolate the possible cause of the error. The result of a flow-sensitive fault localization not only explains why the error occurred, but also justifies why the statements leading to the error were executed [1].

In the Error invariants paper [2], the authors introduce a new concept in which for each position in an error trace they find a formula over program variables that over-approximates the reachable states at the given position while only

capturing the states that will still produce the error, if execution of the trace is continued from that position. We can find the relevant statements in the trace with the help of inductive error invariants. Inductive invariants are those error invariants that hold for consecutive positions in an error trace and hence characterize statements in the trace that are irrelevant for the trace [2].

In the paper Explaining inconsistent code [3] the authors use the idea presented in Error Invariants to find and explain inconsistencies in a program using something called an "*ErrorInvariantAutomaton*". The error invariant automaton is an abstraction of the input code fragment that only mentions program statements and facts that are relevant for understanding the cause of the inconsistency.

2 Goals/Approach

2.1 Formalizations

During my Master Praktikum, I worked on a fault localization algorithm that find relevant statements in an error with respect to two error relevance criterion namely Flow Sensitive and Non-Flow Sensitive relevance criterion. I want to formalize these two notions of error relevance. There are also two sub categories in a relevance criteria which also need a formal definition.

- (1) Predetermined input (which we called the UC case)
- (2) Non-deterministic input (havoc).

Another task in this step would be to formalize our already implemented fault localization algorithm .

2.2 Performance Analysis of the Algorithm

I want to test the algorithm on big error traces and analyse its performance and find out where can the performance be improved. This might also require some modifications in the algorithm.

2.2.1 Non-Flow Sensitive Analysis

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2.2.2 Flow-Sensitive Analysis

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2.3 Security Bugs

We also discovered during my Praktikum that we may be able to perform a security analysis via our fault localization algorithm and distinguish security bugs in a program from all other bugs. I want to formalize what it means that a bug is a security bug and use the algorithm to precisely find them. I expect some modifications in our algorithm so that it can correctly classify an error as a security/non-security error. Following steps are expected to complete this task:

- 1) Analyse examples containing security bugs.
- 2) Formalize the definition of a security bug that satisfies all the examples.
- 3) Check if the formal definition fits correctly with the examples.
- 4) Modify our fault localization algorithm so that it models the definition correctly.

2.4 Verification of the results

I also want to make a mechanism to verify that what we are computing is correct that includes verifying the result of our fault localization algorithm and security bug analysis. For this task i plan to separately implement checks and compare it with our algorithms.

2.5 Broader analysis of the program

Currently we are doing the analysis only on the error trace (or counter example), i also want investigate how we can broaden our perspective from an error trace to a whole program. This may also give us some new definitions for relevance for example, statements that are directly responsible, statements that cause the execution of the directly responsible statements. As an example, consider the code below:

```
1  foo()
2  {
3  {
4      int z;
5      int x;
6      if(x==0)
7      {
8          if(z==0)
9          {
10             y := 1;
11         }
12         else
13         {
14             while(true)
15             {
16
17             }
18         }
19         assert(false);
```

```
20 }  
21 }
```

π in this example is:

$$\textit{assume}(x == 0); \textit{assume}(z == 0); y = 1)$$

and our algorithm shows that none of the statements is relevant. But if we look at the program from a broader perspective then an error trace, we will notice that the value of z is relevant because if $z \neq 0$ then we would be stuck in a never ending loop and would not be able to reach the error. So we cannot say that $\textit{assume}(z = 0)$ is irrelevant for the error.

3 Formalization of Relevancy Criterion

3.1 Error Relevancy

In a program containing a set of statements ST , let $\pi \in ST$ be a sequence of statements. Let ϕ be an assertion condition, such that if π is executed, the assertion condition ϕ is violated. We call this an error. It is possible that ϕ will not be violated after all possible executions of π . It will only be violated if we start the execution of π from a specific state which belongs to a set of states, which we call Error-Precondition EP .

A set of statements $REL \in P(\pi)$ is called relevant iff REL is a set which have a minimum size in $P(\pi)$ and the following property holds:

”If all the assignments in REL from π are replaced with a havoc statement to get a new path π' and if we now execute π' , starting from a state which is in EP , it is not guaranteed that the assertion condition ϕ is violated any more. i.e $\forall \psi \in EP$, the execution of π' is not guaranteed to violate ϕ ”.

[!!!!] This definition only holds for our flow sensitive case, not for the non-flowsensitive case because for some non-flow relevant statements, changing them with a havoc still guarantees that we end up violating the assertion condition in the end

[[REL must also represent a trace that is actually feasible [Define feasible trace here !]]].

Consider the code below:

```

1
2 foo ()
3 {
4   x := 1;
5   x--;
6   x++;
7   assert (x==0);
8 }
```

In the above example:

$\psi = True$

$\pi = \{x = 1, x --, x ++\}$

$\phi \Rightarrow assume(x == 0)$

$P(\pi) = \{\{x = 1\}, \{x --\}, \{x ++\}, \{x = 1, x --\}, \{x = 1, x ++\}, \{x --, x ++\},$

$\{x = 1, x --, x ++\}\}$

$Rel = \{x ++\}$ because if the statement in REL is replaced by a havoc statement in π to get a new path $\pi' = \{x = 1, x --, havoc(x)\}$ then there is no guarantee that the assertion condition ϕ is violated any more after the execution of π' .

Consider another example:

```

1
2 foo ()
3 {
4   x := 1;
```

```

5  y := 1;
6  y := 2;
7  If ( y == 2 )
8  {
9    x := 0;
10 }
11 assert (x==1);
12 }

```

In this example:

$\pi = \{x = 1; y = 1; y = 2; x = 0\}$

$\phi = \text{assume}[x == 1]$

$REL = \{x = 0\}$

$\pi' = \{x = 1, y = 1, y = 2, \text{havoc}(x)\}$

3.2 Non-Flow Sensitive Relevance criteria

3.2.1 Preliminary definitions (new ones)

Introduction:

Proof based fault localization techniques rely on encoding of an error trace into a so called error trace formula. A **error trace formula** is an unsatisfiable logical formula (or is it ? isn't it unsatisfiable together with the post condition). An error is observable by executing an error trace starting from a state that satisfies an **error pre-condition**.

We will encode the (error?)trace into a so called extended trace formula which consists the error precondition, correctness assertion and the trace formula of the trace. When the extended trace formula is unsatisfiable, we say that the trace in the extended trace formula is an error trace and we say that we are seeing an "error".

Formal Setting:

We present programs and their control flow structure using *program automata*. Program automata provide a simple model of programs that abstracts from the syntactic constructs and semantics of specific programming languages. A program automaton is a finite automaton. A state in a program automaton corresponds to a program location and transitions between two states are labelled with program statements. That is, program automaton accepts a regular language of finite words over statements. Each word in this language corresponds to one control flow path. Not every path of a program automaton gives rise to a feasible execution [COME BACK TO FEASIBILITY OF AN EXECUTION]. Formally, let Σ be a fixed set of program statements. A program automaton A is a tuple $(Loc, \delta, l_0, l_{exit}, l_{error})$ where

- Loc is a finite set of locations.
- $\delta \subseteq Loc \times \Sigma \times Loc$ is a finite transition relation.
- l_0 is an initial location.

- l_{exit} is a set of exit locations.
- l_{error} is a set of error locations.

We interpret A as a finite automaton over alphabet Σ with initial state l_0 and final states l_{exit} or l_{error} . A *run* ρ of A is a finite sequence of locations and statements $l_0 st_0 l_1 \dots st_{n-1} l_n$, such that for all $i \in [0, n)$, $(l_i, st_i, l_{i+1}) \in \delta$. A *trace* is a finite sequence of statements. We call $trace(\rho) = st_0 \dots st_{n-1}$ the trace associated with ρ . A run is accepting if its final state is in l_{exit} or l_{error} . We call a word $\pi \in \Sigma^*$ a trace of A if $\pi = trace(\rho)$ for some accepting run ρ of A . We present the semantics of program automata using formulas in first-order logic. Let X be a fixed set of program variables. A program state s is a function that assigns a value $s(x)$ to each program variable $x \in X$. A *state formula* F is a first-order constraint over free variables from X . We write $s \Rightarrow F$ to denote that a state s satisfies a state formula F .

For a variable $x \in X$ and $i \in \mathbb{N}$, we denote by $x^{<i>}$ the variable which models the value of x in a state that is shifted i times into the future. We extend this shift function from variables to sets of variables, as expected, and we denote by X' the set of variables $X^{<1>}$. For a formula F with free variables from Y , we write $F^{<i>}$ for the formula obtained by replacing each occurrence of a variable $y \in Y$ in F with the variable $y^{<i>}$. A *transition formula* T is a first order constraint over free variables from $X \cup X'$, where variables X' denote the values of the variables from X in the next state. That is, a transition formula T represents a binary relation on states. We write $s, s' \Rightarrow T$ to denote that states s and s' satisfy the transition formula T . We assume that every statement $st \in \Sigma$ has an associated transition formula $TF(st)$ that provides the semantics of st . For example the transition formula of an assume statement can be defined as the $TF(assume(F)) \equiv F \wedge X = X'$, where $X = X'$ stands for the conjunction of equalities $x = x'$ for all $x \in X$.

st	$T[st]$
assignment statement ($x := e$)	$x' = e \wedge \{X \setminus x\} = \{X' \setminus x'\}$
assume statement ($assume(cond)$)	$cond \wedge X = X'$
havoc statement $havoc(cond)$	$cond' \wedge \{X \setminus vars(cond)\} = \{X' \setminus vars(cond')\}$

For a trace $\pi \in \Sigma^*$ with $\pi = st_0 st_1 \dots st_n$, we define its *trace formula* $TF(\pi)$ as the conjunction of the shifted transition formulas of the statements in π

$$TF(\pi) = TF(st_0) \wedge TF(st_1)^{<1>} \wedge \dots \wedge TF(st_n)^{<n>}$$

An **execution** σ of a trace π of length n is a sequence of states $s_0 \dots s_n$ such that for all $0 \leq i \leq n$, $s_i, s_{i+1} \Rightarrow T[\pi[i]]$. We denote by $Execs(\pi)$ the set of all executions of π . A trace is called feasible if its trace formula is satisfiable, otherwise we call it infeasible.

 (!!Don't Read!!)((A program automata is safe if for every trace $\pi \in Trace(P)$ whose final statement is an error label statement and every execution σ of π ,

the final state of σ does not satisfies $\Phi(error).))$

A program is safe if all the feasible traces in the corresponding program automata don't have an error state as the end state.[OK?]

If a program is not safe, an error can be witnessed along a trace. The error corresponds to a set of executions that satisfies the transition formula of statement at the end of the trace.

Weakest Precondition Operator: For some given formula $R \in Preds(X)$, and a program statement st , the weakest precondition operator $wp()$ returns the weakest formula that must be true such that if we execute st , R holds.

$$wp(T[st], R) \wedge T[st] \Rightarrow R$$

$wp(st, R)$ is a predicate that characterizes all pre-states of st from which no execution will go wrong and from which every execution ends in a state satisfying R .

Error-Precondition: In a trace π that ends in an error state, an error-precondition can be defined as the following:

$$error\ precondition : wp(TF[\pi], False)$$

Error & Error Trace: For an unsafe program, with a trace $\pi = st_0; \dots st_{n-1}$; whose corresponding run's last state is an error state, an error precondition Ψ , a state formula ϕ , such that $TF[\pi[n-1]] = \phi$, we say that there is an **error** in the program if the following conditions hold :

- 1) $\Psi \wedge TF(\pi[0, n-2])$ is satisfiable.
- 2) $\Psi \wedge TF(\pi[0, n-2]) \wedge \phi^{<n-1>}$ is un-satisfiable.

In this case π is called an **error trace**. Hence for an error trace π , no execution of the trace π that starts in a state satisfying the error pre-condition Ψ ends in an exit state.

3.2.2 Preliminary definitions (to be discarded)

Weakest Precondition Operator (Def 1): For some given program statement S and some postcondition R there is a (possibly empty) set of program states such that if execution of S is initiated from one of these states then S is guaranteed to terminate in a state for which R is true. The weakest precondition of S with respect to R , normally written $wp(S, R)$ is a predicate that characterizes this set of states.

Def 2: For some given logical predicate R , and a program statement ST , $wp(R, ST)$ is defined to be the weakest logical predicate that must be true before executing S in order to prove that R is true afterwards. [4].

Let us assume variable x denotes the tuple of variables involved in statement ST . Then, a given Hoare Triple $\{P\}ST\{R\}$ is provable in Hoare Logic for total correctness if and only if the first-order predicate below holds:

$$\forall x, P \Rightarrow wp(ST, R)$$

3.2.3 Definition

Intuitively, in an error trace π an assignment statement in an error trace is relevant w.r.t non-flow sensitive relevancy criteria if after removing or replacing that statement with a non-deterministic *havoc()* to get a new trace π' , we can't be certain any more that we will reach the error state. We can also say that there exists an execution σ' in $Execs(\pi')$, such that $\Psi \wedge TF(\pi'[0, n-2]) \wedge \phi^{<n-1>}$ is now satisfiable.

For example:

```

1 foo ()
2 {
3   x := 1;
4   y := 2;
5   If ( x == 1 )
6   {
7     y++;
8   }
9   assert ( y != 3 );
10 }
```

Error Trace π will be:

```

1 x := 1;
2 y := 2;
3 assume( x==1 )
4 y := 3;
5 assume( y==3 )
```

If we replace $y := 3$ by *havoc*(y), then y can non-deterministically be assigned any value and there will be an execution for which condition in the end $\phi = (y \neq 3)$ will not be violated and the formula $\Psi \wedge TF(\pi'[0, n-2]) \wedge \phi^{<n-1>}$ is satisfiable.

Formal definition:

-If we start the execution of the program from a state satisfying the error-precondition Ψ , then if we start the execution of the program from that state then we will always end up in an error state.

- If we start the execution of an error trace from a state other than the error-precondition, then we can get **stuck** (does that mean that we might not be able to execute the error trace properly?).

- If we start the execution of an error trace from a state satisfying the error-precondition and we replace an assignment statement in the error trace with a *havoc* statement and we get stuck again after doing that, then that statement is called a **relevant statement**.

What does it mean to get stuck?

Well, it depends. We can say that getting stuck can have two meanings. It depends on what are we considering:

- 1) Program level scope
- 2) Error Trace

If we see it from the angle of **Program level scope**, then we can say that the execution of the program will not terminate and the program is stuck in a non-terminating loop or something. [NO !!!!! that does not make sense because all of our programs are terminating you idiot!]. [[SO REMOVE THIS ARGUMENT FROM HERE]] lol :D

But ! if we see it from an angle of the **error trace**, then it could mean that now you are getting blocked in the error trace and an assume statement can now become a **blocking statement**. Because in an error trace there are only assignment statements or assume statements. Assignment statements can't become blocking, only assume can. Now how to formalize this ? An assume statement becoming a blocking statement.

One possible way to define this could be that $\Psi \wedge TF(\pi'[0, n-2])$ is now unsatisfiable. So we might formally be able to define the meaning of getting stuck. That is , the error trace execution can get stuck if $\Psi \wedge TF(\pi'[0, n-2])$ is unsatisfiable. that also means that one of the assume statement is now a blocking statement.

3.2.4 Algorithm

Let π be an error trace of length n , $WP()$ the weakest pre-condition operator, $\pi[i]$ the i^{th} statement of π .

Lets call Ψ a pre-condition and $\Psi = \neg WP(False, \pi[i, n])$.

Lets call ϕ a post-condition and $\phi = WP(False, \pi[i+1, n])$.

Where n is the length of the error trace. A statement $\pi[i]$ in an error trace π is relevant with respect to the non-flow sensitive criteria (*) if the conjunction of the the three formulas $(\psi, \pi[i], \neg\phi)$ is unsatisfiable and $\pi[i]$ is in the unsatisfiable core.

$\pi[i]$ is considered relevant with respect to non-flow sensitive criteria (@) if $(\psi, \pi[i], \neg\phi)$ is satisfiable. (In this case, $\pi[i]$ is a non-deterministic input statement (*Havoc*)).

Algorithm 1: **nonFlow**(trace π)

Input: Trace π of length n .

Output: A list of relevant statements in the trace.

```

1 nonFlow ( trace )
2 {
3   formula wpList = [];
4   relevantStatements = [];
5   wpList.append (FALSE);
6   for (i=n-1 to 0)
7   {
8     formula wp = WeakestPrecondition (last element of wpList , trace [
9       i ] );
10    wpList.append (wp);
11  }
12  for (i=n-1 to 0)
13  {

```

```

13     formula pre = NEGATIVE(wpList[i+1])
14     relevance = UNSATCORE(pre, trace[i], wpList[i]);
15     if(relevance == "unsatisfiable" AND trace[i] is in
16         unsatisfiable core)
17     {
18         relevantStatements.append(trace[i]);
19     }
20 }
21 return(relevantStatements);
22 }

```

For example consider the following code:

```

1 foo()
2 {
3     int y;
4     int x;
5     int z;
6
7     y := 0;
8     z := 0;
9     if(y==0)
10    {
11        x := 1;
12        if(z==0)
13        {
14            z := 1;
15        }
16    }
17    else
18    {
19        y := 2;
20        y := 3;
21        y := 4;
22    }
23    assert(x == 0);
24 }

```

For the above code we will get the following error trace:

```

1 y := 0; [*]
2 z := 0; [*]
3 assume (y == 0);
4 x := 1; [*]
5 assume (z == 0);
6 z := 1;
7 assume !(x == 0);

```

In the above trace, for $i = 4$,

$$\begin{aligned} \psi &= \neg WP(False, \pi[4, 7]) \implies z = 0 \\ \pi[4] &\implies x = 1 \\ \phi &= WP(False, \pi[5, 7]) \implies x = 0 \vee \neg(z = 0) \end{aligned}$$

The formula $(\psi, \pi[4], \neg\phi)$ is unsatisfiable and $\pi[4]$ is in the unsatisfiable core.

Hence $\pi[4]$ is relevant with respect to Non-Flow Sensitive relevance criterion and we therefore label it with a $[*]$.

3.2.5 Example

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3.3 Flow Sensitive Relevance criteria

The definition of relevancy is actually similar to the non-flow sensitive case. The only thing different is that now we take branches in the trace into account. If there is a branch in the error trace, the relevancy of the statements inside the error trace will only be calculated if the branch as a whole is relevant to the error. We see that we can get slightly different results from the Non-Flow Sensitive case because it is possible that a statement might be relevant with respect to non-flow sensitive case but it lies inside a branch which is not relevant.

When we say that a branch $\pi[i, j]$ must be relevant to the error, we mean that the branch is reduced to a transition formula which we call a "*MarkhorFormula*" and it's relevancy is calculated in the same way as we did for a statement in the non-flow sensitive case (by checking if the conjunction of $(\psi, \pi[i], \neg\phi)$ is unsatisfiable and the statement is in the unsatisfiable core).

3.3.1 Markhor Formula

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4 Performance Analysis

4.1 Non-Flow Sensitive Fault Localization:

I think our algorithm is more effected not by the size of the trace, but the size of the formulas in the trace. That's why, sometimes even for small traces, the non-flow sensitive analysis is fast but it takes very long for flow-sensitive analysis to run, not because of the size of the trace , but the complexity of the formulas in it.

Therefore there might not be much we can do to increase the performance of non-flow sensitive analysis because there are not many steps involved and the formulas we compute here (wp, pre) are our most basic requirement to compute the relevance of a statement and their calculation cannot be further simplified.

5 Security Errors

A very interesting problem that we might be able to solve via our fault localization algorithm is to distinguish security error from all other kinds of errors.

According to our current definition an error is a security error iff an input statement (network/user) can cause the program execution to reach the error. Following are the (current) criterion for a security error.

1. There is some reachable location where the program reads input.
2. There is some input value, such that continuing from this location we definitely reach the error
3. There is some input that continuing from this location we do not reach the error.

From condition 2 and 3 we can deduce that the input is somehow relevant for the error.

For example:

```
1 foo()
2 {
3     int y;
4     int x;
5     x := 1;
6     y := user_input();
7     if (y==2)
8     {
9         y := y+1;
10    }
11    assert(y != 3);
12 }
```

In the above example, the input statement $y := user_input()$ determines if we reach the error or not. Hence there is a security error in this program.

References

- [1] J. Christ, E. Ermis, M. Schaf, and T. Wies. Flow-sensitive fault localization. In VMCAI, volume 7737, pages 189–208, Berlin, Heidelberg, 2013. Springer
- [2] E.Ermis, M. Schaf, and T. Wies. Error Invariants. In FM’12, pages 338–353. Springer, 2012.
- [3] M. Schaf, D. Schawrtz, T. Wies. Explaining Inconsistent Code. In Joint meeting of the European Software Engineering conference and the Symposium on the Foundations of Software Engineering, ESEC/FSE’13, pages: 521 - 531, Saint Petersburg, Russian Federation. August 18-26,2013
- [4] <https://courses.cs.washington.edu/courses/cse503/06sp/correctness2.pdf>