

Zeeshan Ali Ashraf

(Electronic Engineer)



Father Name: Muhammad Ashraf

Address: H#123, St#07, Pir Jamshed Colony KRL Road, Rawalpindi.

Mobile: +92-305-8736403

Date of Birth: 27-01-1994

Nationality: Pakistani

Email: zeeshan.bsee1767@gmail.com

PEC No: Electro/23482

Objective

- ❖ To secure a challenging and progressive position in the areas of engineering while exploiting my knowledge, analytic, hardworking and planning skills as Trainee. Looking to bring a continued whole hearted contribution for a reputed institution that rewards on merit.

Experience

- ❖ Trainee 3D Seismic Data Acquisition Observer BGP (Pakistan) International
From October 25, 2016 to February 09, 2017

Project: Kotri North 3D and Kotri South 3D
System: Sercel 428XL System
Sites: Hyderabad (Hala, Matiari), Jamshoro, Thatha-Sajawal (Rural)
- ❖ Attachment(04 weeks) Oil & Gas Training Institute Islamabad
From February 08, 2018 to March 08, 2018

Education

| QUALIFICATION | INSTITUTION | Year | CGPA/GRADE |
|-----------------------------|--|-----------|------------|
| BS (Electronic Engineering) | International Islamic University Islamabad | 2012-2016 | 3.74 |
| F.Sc (Pre – Engineering) | Islamabad College for Boys G-6/3 Islamabad | 2009-2011 | 62.8% |
| Matriculation | F.G. Boys Model School G-8/4 Islamabad | 2007-2009 | 65% |

Projects

Final Year Project

- ❖ Doppler Radar for Collision Avoidance.

Semester Projects

- ❖ Distance Measuring using 8051 Microcontroller.
- ❖ Variable Power Supply.
- ❖ Water Level Detector.
- ❖ Frequency to voltage converter.

Engineering Software Skills

428XL Linux Software(Client Server Based), Bloodshed Dev C++, Arduino, Keil Compiler, Genius Programmer, PSPICE, Proteus, MATLAB, AVR Studio, ATMEL Studio, Model Sim, Xilinx ISE

Professional Courses

- ❖ MS Office (Basic Course)

Joher Institute of Information Technology Islamabad

Duration: 03 Months

- ❖ Attended PLC Seminar

Sina Institute Islamabad

February 04, 2018

Area of Interest

- ❖ Control Systems
- ❖ Circuit Analysis
- ❖ Field Programmable Gate Array (FPGA)
- ❖ Digital Logic Design
- ❖ Power Electronics