Curriculum Vitae

PERSONAL INFORMATION



Zeeshan younas

🎙 Abid colony kot Farid khan street no.10. Sahiwal, Punjab, Pakistan

+92 300 9100547

Sex Male Date of birth 01/02/1994 Nationality Pakistani

CNIC 36502-6980031-9 Marital Status Single

JOB APPLIED FOR

To be placed in a company as an ELECTRICAL ENGINEER in which I can use my knowledge and leadership qualities for the succession of Organization and self growth

WORK EXPERIENCE

FOR FOUR MONTH Internship as a Trainee Engineer at Fazal Thermal Power plant (8 MW) Okara in shut down condition

FOR THREE MONTH

Internship at Solar power plant harapa as a supervisor with CNEEC Company project including testing and commissioning

EDUCATIONAL RECORD

October 2013 - September 2017

BSc Electrical Engineering Technology

Equal to 16 Years of Education

Government College University Faislabad (Sahiwal Campus).

• A wide range of subjects covering Electronics, Control System Power Generation, Transmission and Distribution, Electrical and Power Engineering and Telecommunication

CGPA 3.74/4 (93.5%)

May 2010 - October 2013

DAE(Electrical)

Equal to 12 Years of Education

Government College of Technology, Sahiwal, Pakistan Punjab Board of Technical Education Lahore. (Pakistan) (www.pbte.edu.pk)

- Subject related to basic engineering

Marks 2911/3500 (82%)

August 2008 - August 2010

Matriculation (Science)

Equal to Secondary school Education

Govt. Nangal Ambia High school No.1, Sahiwal, Pakistan

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Board of Intermediate and Secondary Education, Multan (Pakistan) (www.bisemultan.edu.pk)

Marks 806/1050 (76.76%)

PERSONAL SKILLS

Mother Tongue

Urdu Fluent

English

Beginner

Chinease

Communication skills

Good Communication skills are gained through studies.

Organisational / managerial skills

- Leadership
- Self-motivated, willing to learn, and interested in working in a team environment

ADDITIONAL INFORMATION

Computer skills

- A good command on of Microsoft Office.

TECHNICAL SKILLS

- Design circuit on proteuos software.
- Design power circuit on Matlab.
- Implementation of designed circuit on visio board.

Reference

• Reference will be on demand.