M. Ehtasham Ul Haq Khan

Intelligent and hardworking engineering graduate with proven leadership and technical skills seeking to apply my abilities to a position where I can benefit the society

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Rawalpindi, Pakistan

EDUCATION

Bachelors
Muhammad Ali Jinnah University, Islamabad
09/2013 – 07/2017

Electronics Engineering   *3.17*

Intermediate
Punjab College of Information Technology, Rawalpindi
06/2010 – 05/2012

Pre- Engineering   *65.27%*

Matriculation
Siddeq Public School, Rawalpindi
08/2008 – 05/2010

Science   *68.66%*

SKILLS AND SOFTWARES

MATLAB C++ Verilog Keil uVision STM32 Arduino Lab View Xilinx Code Composer Studio Proteus MS Office ModelSim Simulink MS Visual Studio Altium Designer ETAP(Power Distribution Designing)

AWARDS

Dean's Honor Award
Capital University of Science and Technology

ACHIEVEMENTS AND ACTIVITIES

Participated in Robotics Competition
Participated in NERC'15 held at EME NUST

LANGUAGES

- ENGLISH
- URDU
- PUNJABI

PROJECTS

Electronic Throttle Control (Final Year Project)
- Throttle of the Car was controlled Electronically
- Throttle Paddle was replaced with Electronic System also known as drive by wire system
- RPM were controlled using controller and the feedback of the controller was RPM of Engine
- PID was implemented on STM32 Discovery
- This system model has to follow the user set RPM and to avoid the disturbances

Motor Speed control using PID
- Hardware PID implementation using OP-AMPs

Line Follower Robot
- Line follower robot designed using logic gates. Motors were controlled with the help of H-bridge module.

4x4 LED Cube on FPGA
- 4x4 LED Cube on Spartan 3E FPGA. Different patterns were displayed using LED's on different frequencies.

Temperature Controlled Fan
- Hardware of temperature Controlled fan using OP-AMP and transistor as switch

Audio Equalizer on DSK6713
- Audio Equalizer on DSK6713. Digital FIR and IIR filter designing on Matlab and implementing them on DSP dsk6713 using code composer studio

Amplitude Modulator/Demodulator
- Designed and implemented AM on hardware

WORK EXPERIENCE

Hardware Design Engineer
TeReSol (Technological Research Solutions)
Aug 8, 2017 - present

Intern
OGDCL, Islamabad
Wireline Logging Department

FIELD INTERESTS

-Embedded Systems	-FPGA	-Control Systems
-C++ Programming	-Robotics	-Digital Designs
-Industrial Automation	-Power Systems	-Power Distribution