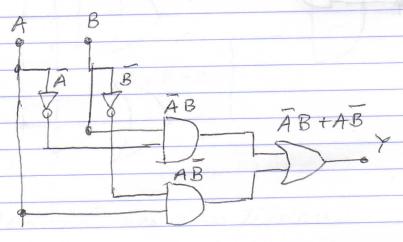


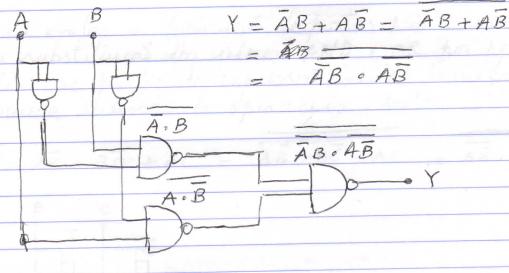


A	B	Y	2 1
0	0	0	$Y = \overline{A} \cdot B + AB$
0	1	1	
1	0	1	38
1	1	0	

a) Circuito com portas AND, OR & NOT



b) Circuito com portas NAND



2.
$$f = \overline{W} \times \overline{Y}Z + \overline{W} \times \overline{Y}Z + W \times \overline{Y}Z + W \times \overline{Y}Z$$

a) Simplificação com algebra broleana

 $f = \overline{W} \overline{Y}Z (X + \overline{X}) + W \overline{Y}Z (X + \overline{X})$
 $= \overline{W} \overline{Y}Z + W \overline{Y}Z = \overline{Y}Z (\overline{W} + W) = \overline{Y}Z$

b) Simplificação com mapa de Karmugh

 $\overline{Y}Z \overline{Y}Z YZ YZ \overline{Z}$
 $\overline{W}X \overline{W}X \overline$

