



7	=	B		
0,	=	D2	=	×

a)

Y (A, B, C)

_							
A	B	2	C	Dn	×	D'n	Y
0	0	0	0	Do	1	D'o	0
0	0	0	1	Do	1	DIA	1
0	1	1	0	D <sub>4</sub>	0	012	0
0	1	1	1	D <sub>1</sub>	0	Di3	1
1	0	0	0	D <sub>2</sub>	0	Do	0
1	0	0	1	Dz	0	Dia	0
1	1	1	0	D3	0	D'2	0
1	1	1	1	D3	0	D'3	1

A	B	C	Y
0	0	0	0
0	0	1	1
	1	0	0
0	1	1	1
1	0	0	0
1	0	1	0
1	1	0	0
1	1	1	11

b) MUX 4:1 e portas lógicas (MSB) (LSB)

A	В	8	
0	0	C	Do Do
0	1	C	D1
1	0	0	02
1	1	C	P3
		1	-

