

	DAY 1			DAY 2			DAY 3						
Hours	TRACK 1 (Room A)	TRACK2 (Room B)	Hours	TRACK 1 (Room A)	TRACK2 (Room B)	Hours	TRACK 1 (Room A)	TRACK2 (Room B)					
7:30	REGISTRATION OPENS												
8:20	WELCOME Room A												
8:30									REGISTRATION OPENS		REGISTRATION OPENS		
8:40													
8:50													
9:00	PLENARY 1 Sani Nassif Room A		9:00	PLENARY 2 Rob A. Rutenbar Room A		9:00	PLENARY 3 Helmut Graeb Room A						
9:10			9:10			9:10							
9:20			9:20			9:20							
9:30			9:30			9:30							
9:40			9:40			9:40							
9:50			9:50			9:50							
10:00	Behavioral Modeling and Topology Synthesis Room A	COMPETITION Session 1 Room B	10:00	Special Session on MEMS & Heterogeneous System Design Room A	Design Techniques 2 Room B	10:00	Variability and Aging Room A	RF and Antenna Design Room B					
10:10			10:10			10:10							
10:20			10:20			10:20							
10:30			10:30			10:30							
10:40			10:40			10:40							
10:50			10:50			10:50							
11:00			11:00			11:00							
11:10			11:10			11:10							
11:20	COFFEE BREAK & POSTERS Ground floor		11:20	COFFEE BREAK & POSTERS Ground floor		11:20	COFFEE BREAK Ground floor						
11:30			11:30			11:30							
11:40			11:40			11:40							
11:50	RF Design Room A	COMPETITION Session 2 Room B	11:50	Special Session Modeling techniques in RF/microwave circuit design Room A	COMPETITION Session 4 Room B	11:50	PANEL SESSION Georges Gielen (Chair) Room A						
12:00			12:00			12:00							
12:10			12:10			12:10							
12:20			12:20			12:20							
12:30			12:30			12:30							
12:40			12:40			12:40							
12:50			12:50			12:50							
13:00			13:00			13:00							
13:10			13:10			13:10							
13:20			13:20			13:20							
13:30	LUNCH Ground floor		13:30	LUNCH Ground floor		13:30	LUNCH Ground floor						
15:00	Analog Layout Room A		15:00	KEYNOTE Trends in Circuit/EM Cosimulation Alexander Pérez Room A		15:00	Test, Diagnosis and Robustness Room A	Device Modeling and Circuit Generation Room B					
15:10			15:10			15:10							
15:20			15:20			15:20							
15:30			15:30			15:30							
15:40			15:40			15:40							
15:50			15:50			15:50		Symbolic Analysis Room B					
16:00			16:00	Circuit-level Modeling and Simulation Room A	High-level Modeling and Simulation Room B	16:00							
16:10			16:10			16:10							
16:20	COFFEE BREAK Ground floor		16:20			16:20	CLOSING CEREMONY						
16:30			16:30			16:30							
16:40			Room A			Room B			16:40	END OF DAY 3 & CONFERENCE			
16:50	END OF DAY 2								16:50				
17:00									17:00			17:00	
17:10						17:10							
17:20													
17:30													
17:40													
17:50	END OF DAY 1												
	WELCOME COCKTAIL			GALA DINNER									