DAY 1

Hours	TRACK 1 (Room A)	TRACK2 (Room B)
7:30	REGISTRATION OPENS	
8:20 / 8:30 / 8:40 / 8:50	WELCOME — Room A	
9:00 – 9:50	PLENARY 1 — Sani Nassif — Room A	
10:00 – 11:10	Behavioral Modeling and Topology Synthesis — Room A	COMPETITION Session 1 — Room B
11:20 – 11:40	COFFEE BREAK & POSTERS — Ground floor	
11:50 – 13:20	RF Design — Room A	COMPETITION Session 2 — Room B
13:30	LUNCH — Ground floor	
15:00 – 16:10	Analog Layout — Room A	
16:20 – 16:40	COFFEE BREAK — Ground floor	
16:50 – 17:40	Design Techniques 1 — Room A	COMPETITION Session 3 — Room B
17:50	END OF DAY 1	
	WELCOME COCKTAIL	

DAY 2

Hours	TRACK 1 (Room A)	TRACK2 (Room B)
8:30	REGISTRATION OPENS	
9:00 – 9:50	PLENARY 2 — Rob A. Rutenbar — Room A	
10:00 – 11:10	Special Session on MEMS & Heterogeneous System Design — Room A	Design Techniques 2 — Room B
11:20 – 11:40	COFFEE BREAK & POSTERS — Ground floor	
11:50 – 13:20	Special Session Modeling techniques in RF/microwave circuit design — Room A	COMPETITION Session 4 — Room B
13:30	LUNCH — Ground floor	
15:00 – 15:50	KEYNOTE — Trends in Circuit/EM Cosimulation — Alexander Pérez — Room A	
16:00 – 16:50	Circuit-level Modeling and Simulation — Room A	High-level Modeling and Simulation — Room B
17:00	END OF DAY 2	
	GALA DINNER	

DAY 3

Hours	TRACK 1 (Room A)	TRACK2 (Room B)
8:30	REGISTRATION OPENS	
9:00 – 9:50	PLENARY 3 — Helmut Graeb — Room A	
10:00 – 11:10	Variability and Aging — Room A	RF and Antenna Design — Room B
11:20 – 11:40	COFFEE BREAK — Ground floor	
11:50 – 13:20	PANEL SESSION — Georges Gielen (Chair) — Room A	
13:30	LUNCH — Ground floor	
15:00 – 15:30	Test, Diagnosis and Robustness — Room A	Device Modeling and Circuit Generation — Room B
15:40 – 16:30	Test, Diagnosis and Robustness — Room A	Symbolic Analysis — Room B
16:40 – 17:00	CLOSING CEREMONY	
17:10	END OF DAY 3 & CONFERENCE	