Timo	1 us	\neg
Time vclk_tb		
vdata_tb[7:0]	00	-
vphase_tb		-
r_cr_dac_tb[7:0]	00	=
g_y_dac_tb[7:0]	00	=
b_cb_dac_tb[7:0]		≐
clk_dac_tb		-1
nblanking_tb		-
nc_sync_dac_tb		-
rgb_out_en_tb		_
<u> </u>		_