Timo	1 ius 2 ius 3 ius
Time vclk_tb	
	(00
vphase_tb	
r_cr_dac_tb[7:0]	00
	00
b_cb_dac_tb[7:0]	
clk_dac_tb	
nblanking_tb	
nc_sync_dac_tb	
rgb_out_en_tb	
L.	