Time	1 us 2 us 3 us
vclk_tb	
vdata_tb[7:0]	(00 [EF
vphase_tb	
r_cr_dac_tb[7:0]	(uu)(80)(01)(04)(10)(40)(80
g_y_dac_tb[7:0]	uu
b_cb_dac_tb[7:0]	(uu)(80)(02)(08)(20)(88)(80
clk_dac_tb	
nblanking_tb	U
nc_sync_dac_tb	U
rgb_out_en_tb	