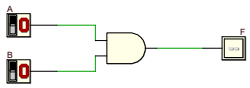
PART 1:

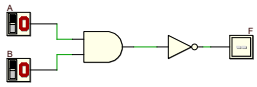


2 input AND gate

Logic Equation:
F = A and B

Input		Output
0	0	0
0	1	0
1	0	0
1	1	1

PART 2:

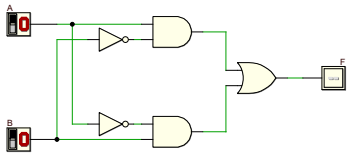


2 input NAND gate

Logic Equation:
F = (A and B)
= C not

Input		Output
0	0	1
0	1	1
1	0	1
1	1	0

PART 3:



2 input XOR gate

Logic Equation:
F = A xor B
= (A and B bar) + (A bar and B)

Input		Output
0	0	0
0	1	1
1	0	1
1	1	0