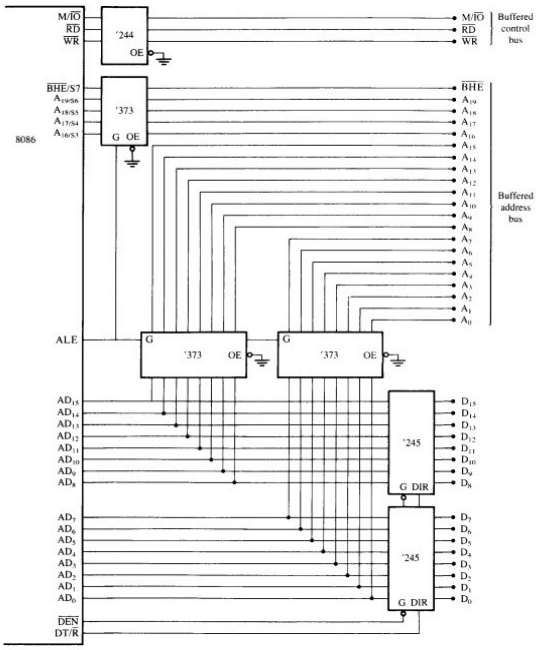

74LS245 octal bidirectional bus buffers;
and the control bus signals, M>IO, RD,
and WR use a 74LS244 buffer.
A fully buffered 8086 system requires
one 74LS244, two 74LS245s,
and three 74LS373s.
The 8086 requires one more buffer
than the 8088 because of the extra
eight data bus connections, D15–D8.
It also has a BHE signal that is buffered
for memory-bank selection.

depicts a fully buffered 8088 microprocessor.
Notice that the remaining eight address pins,
A15–A8, use a 74LS244 octal buffer;
the eight data bus pins, D7–D0, use
a 74LS245 octal bidirectional bus buffer;
and the control bus signals, M>IO, RD

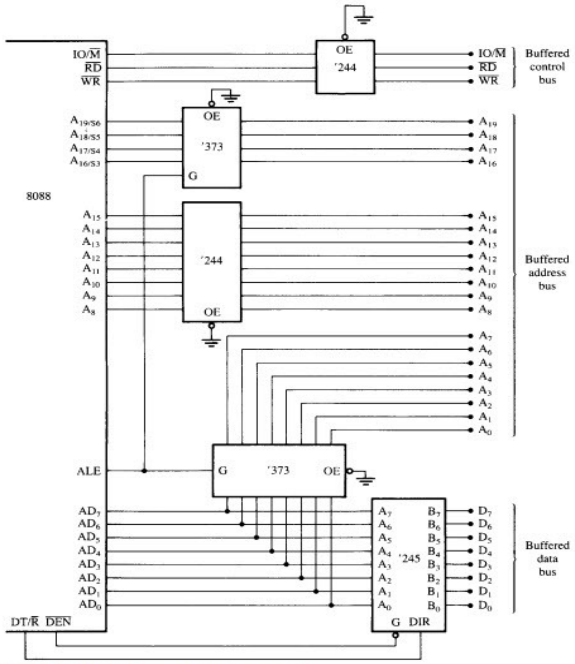

FIGURE 9–7 A fully buffered 8088 microprocessor.