

TBR Designs

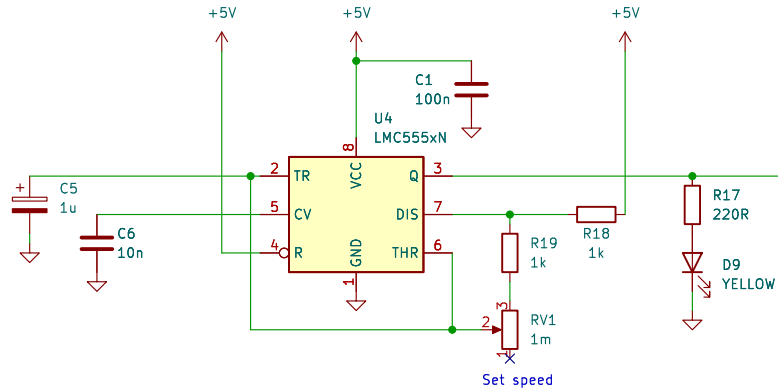
Sheet: /  
File: 8-bit-cpu.kicad\_sch

Title: 8-bit CPU

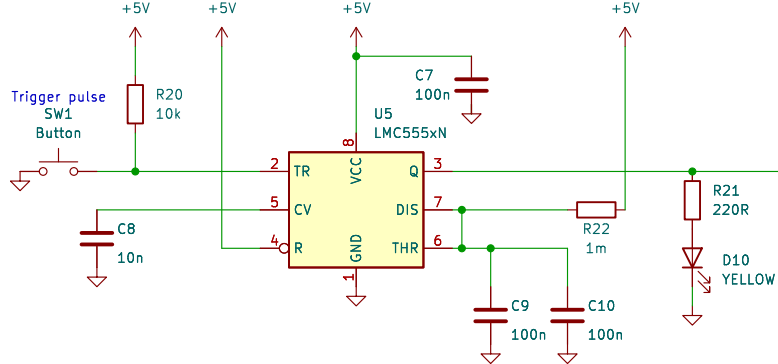
Size: A4 Date: 2023-11-28  
KiCad E.D.A. kicad 7.0.9

Rev: 1.0  
Id: 1/16

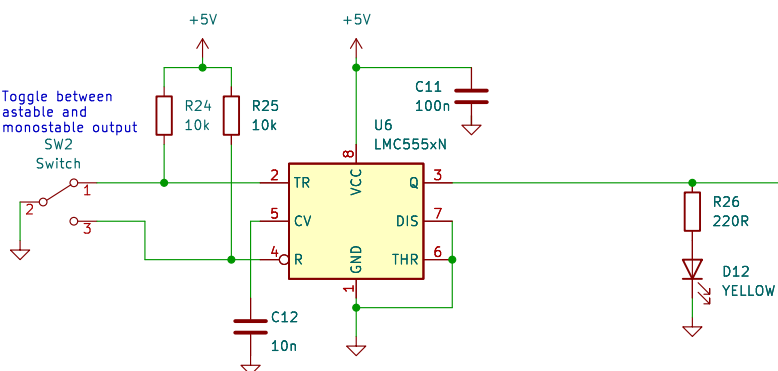
### ASTABLE 555 TIMER



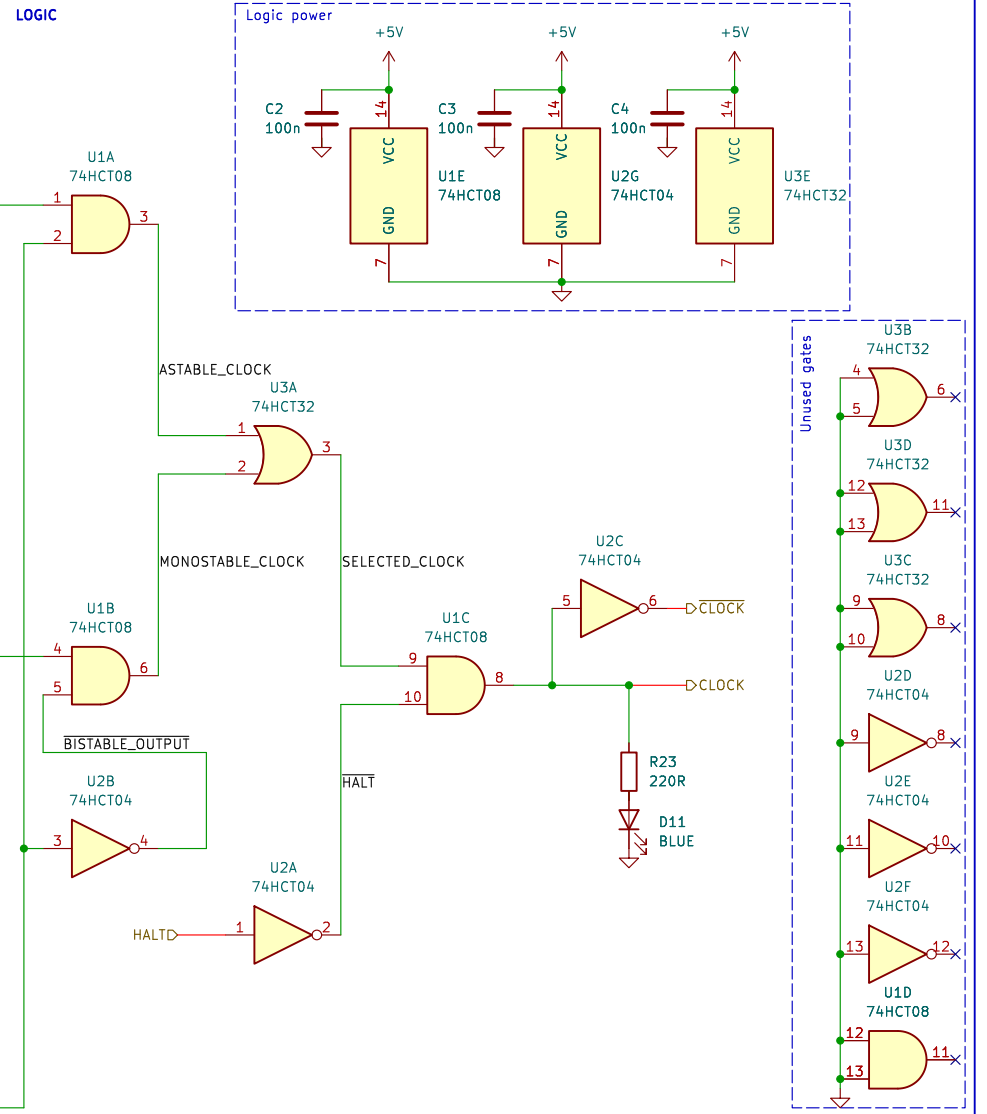
### MONOSTABLE 555 TIMER



### BISTABLE 555 TIMER



### LOGIC



Provides a clock pulse and inverted clock pulse.  
Speed can be set with the potentiometer.  
Manual pulse can be chosen with the switch and pulsed with the button.  
If HALT is HIGH no pulse is outputted.

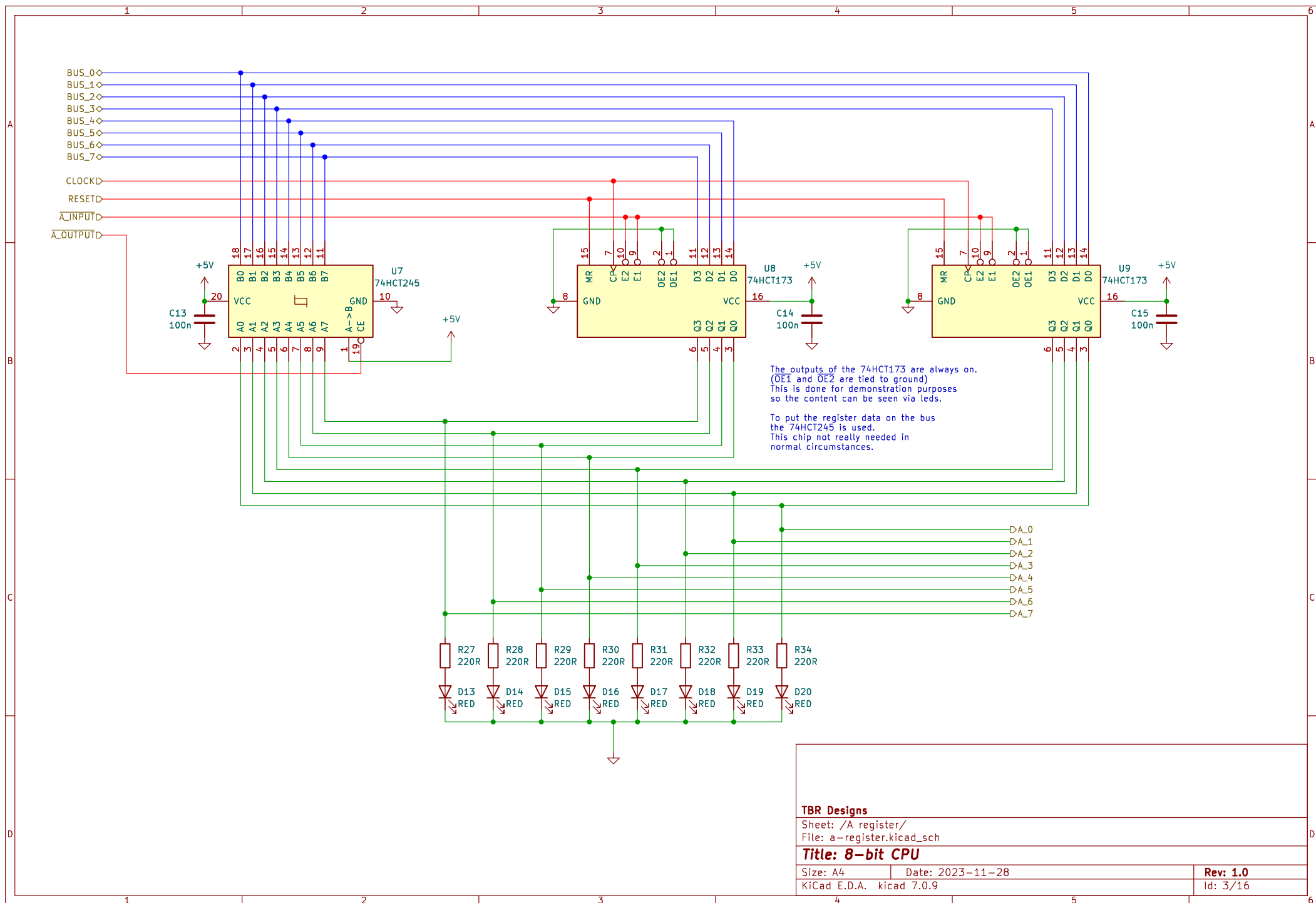
#### TBR Designs

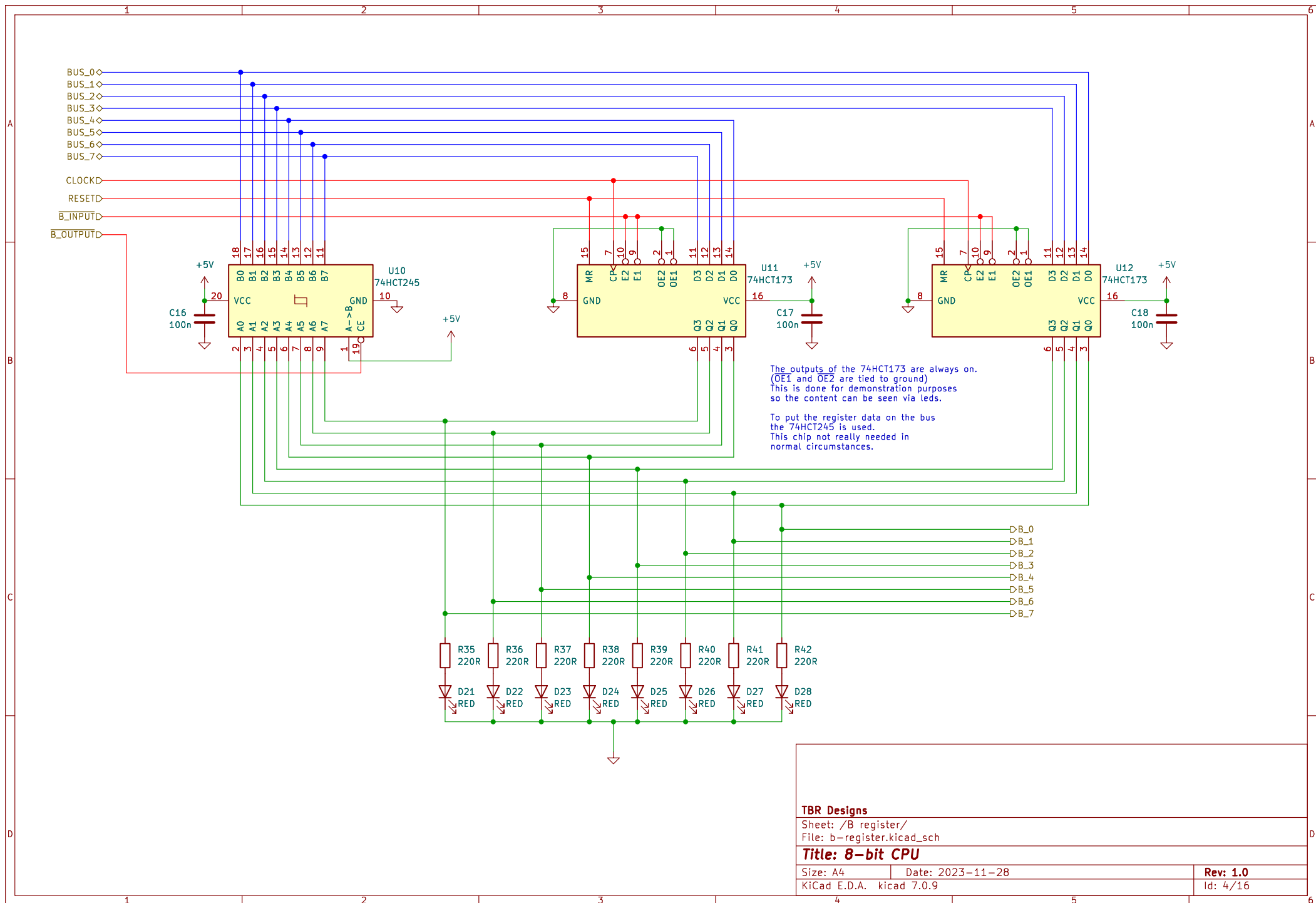
Sheet: /Clock/  
File: clock.kicad\_sch

#### Title: 8-bit CPU

Size: A4 Date: 2023-11-28  
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Sheet: /B register/

File: b-register.kicad\_sch

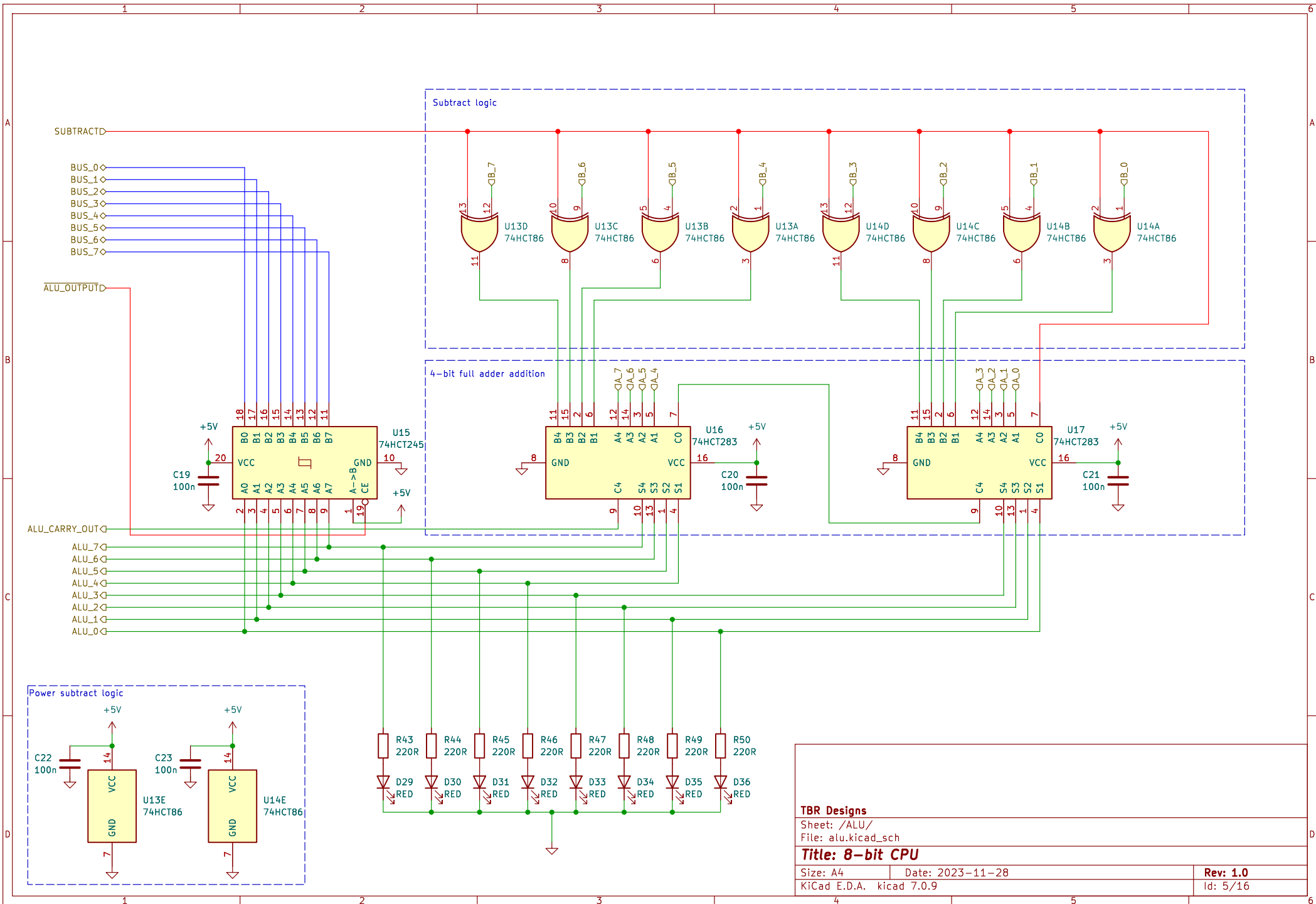
## Title: 8-bit CPU

Size: A4 Date: 2023-11-28

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Rev: 1.0

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TBR Designs

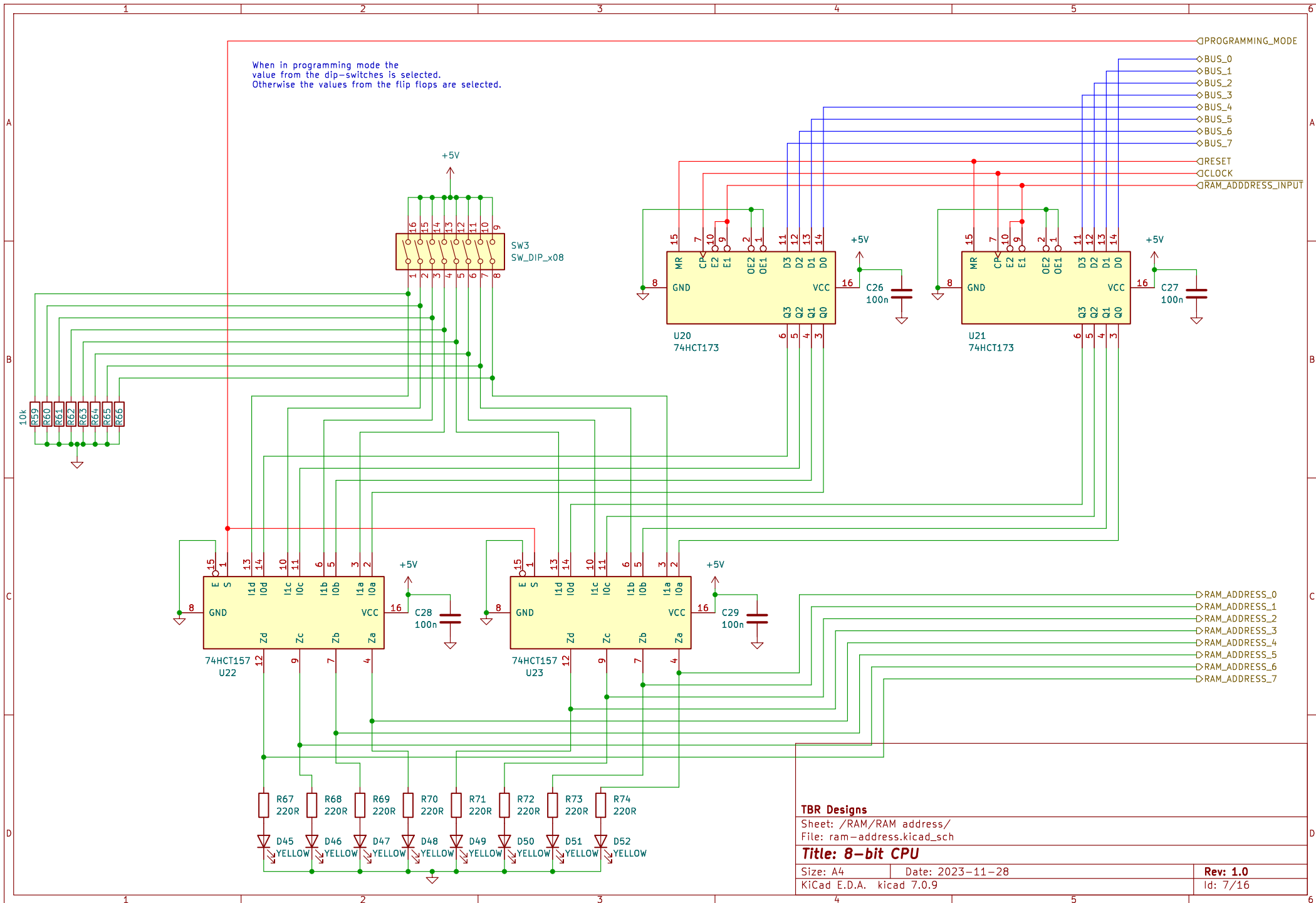
Sheet: /ALU/  
File: alu.kicad\_sch

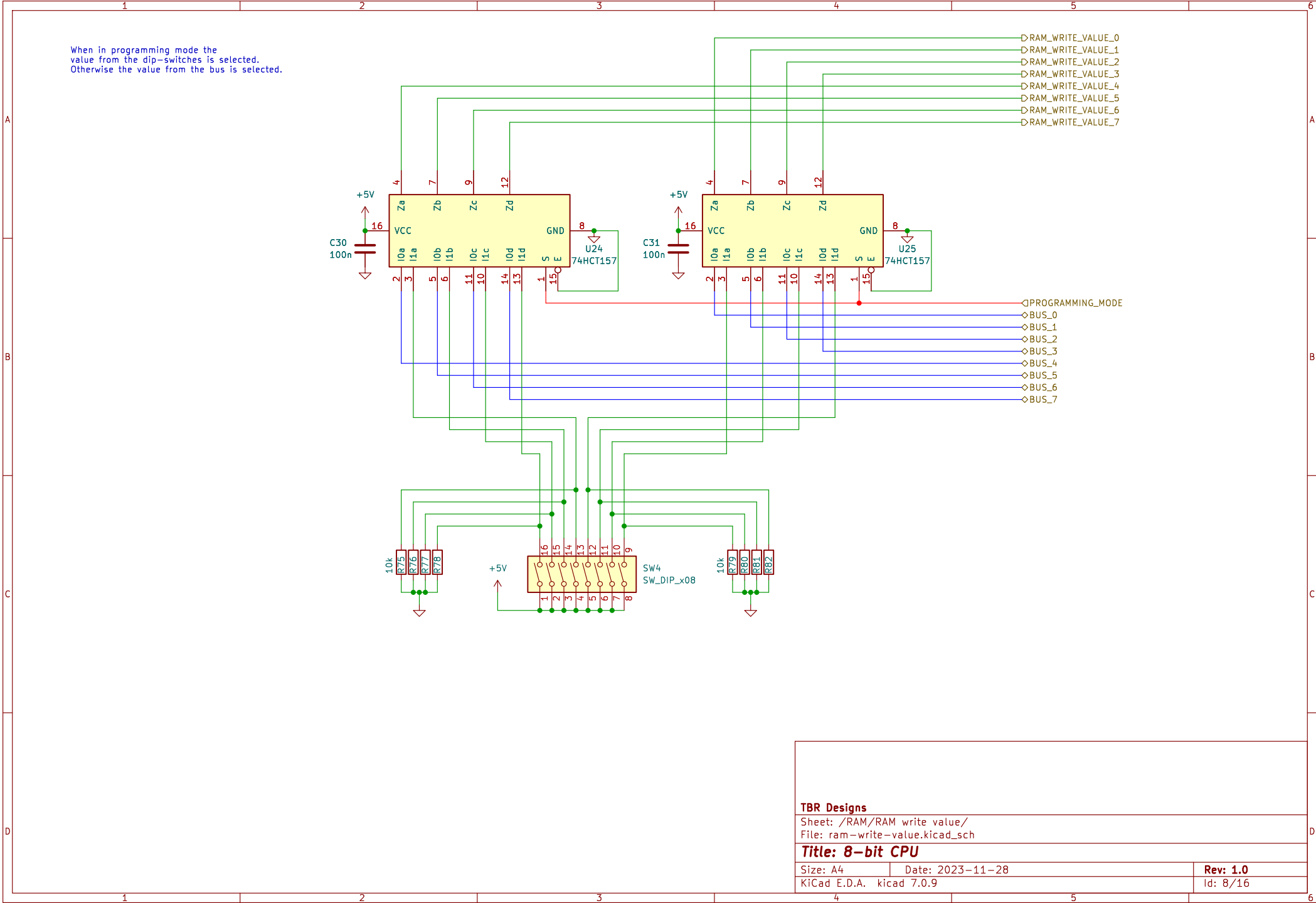
**Title: 8-bit CPU**

Size: A4 Date: 2023-11-28  
KiCad E.D.A. kicad 7.0.9

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**TBR Designs**

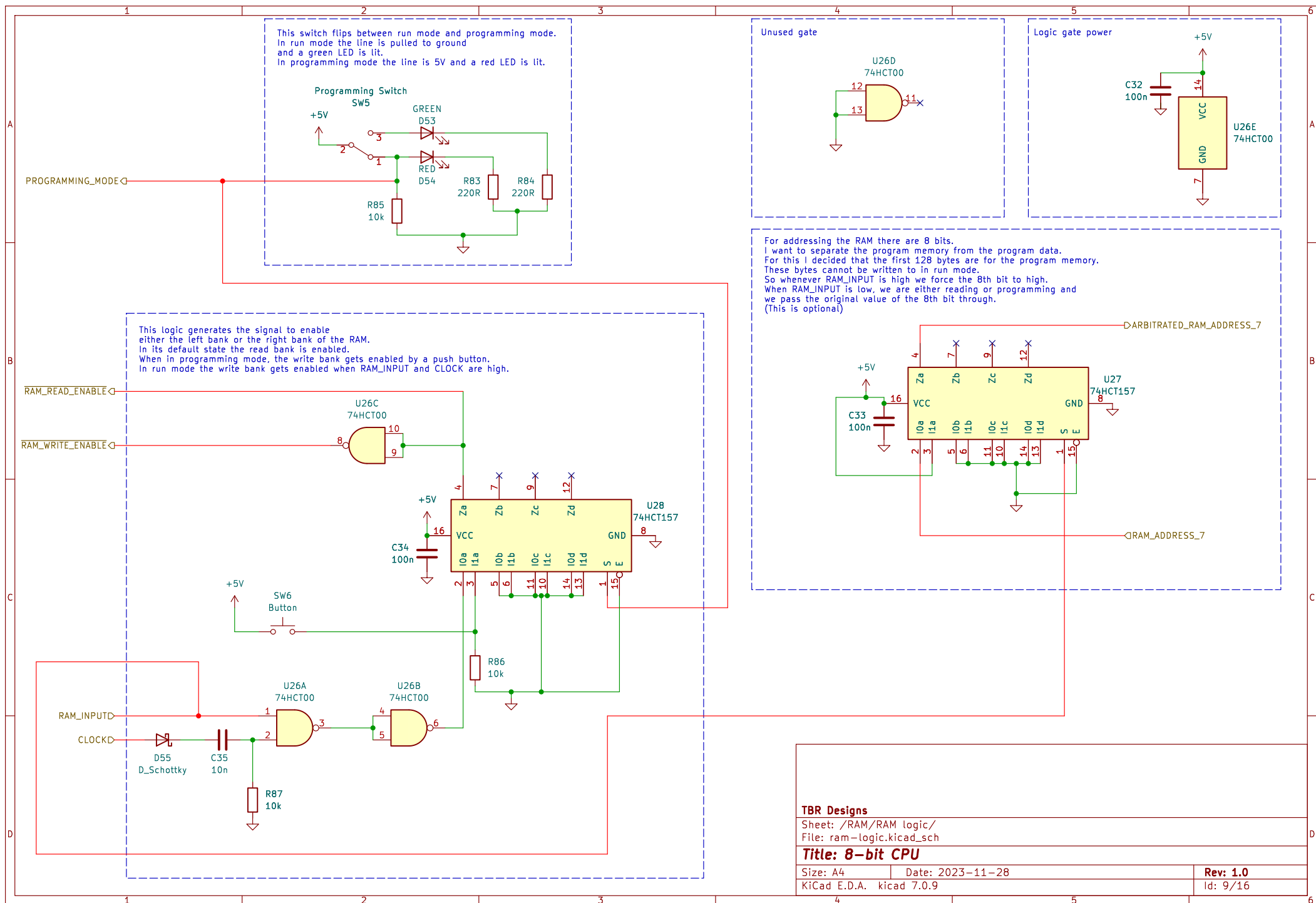
Sheet: /RAM/RAM write value/  
File: ram-write-value.kicad\_sch

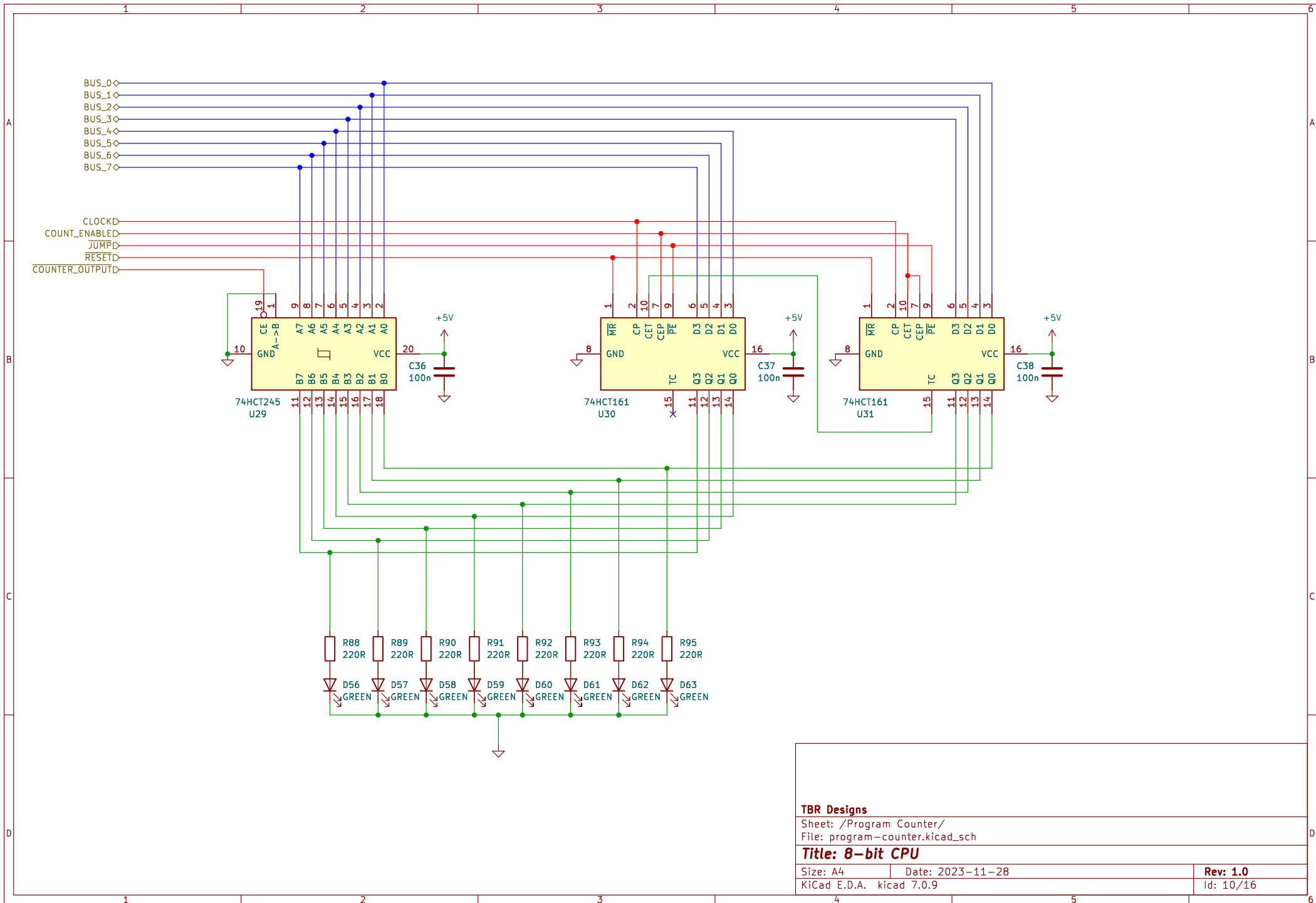
**Title: 8-bit CPU**

Size: A4 Date: 2023-11-28  
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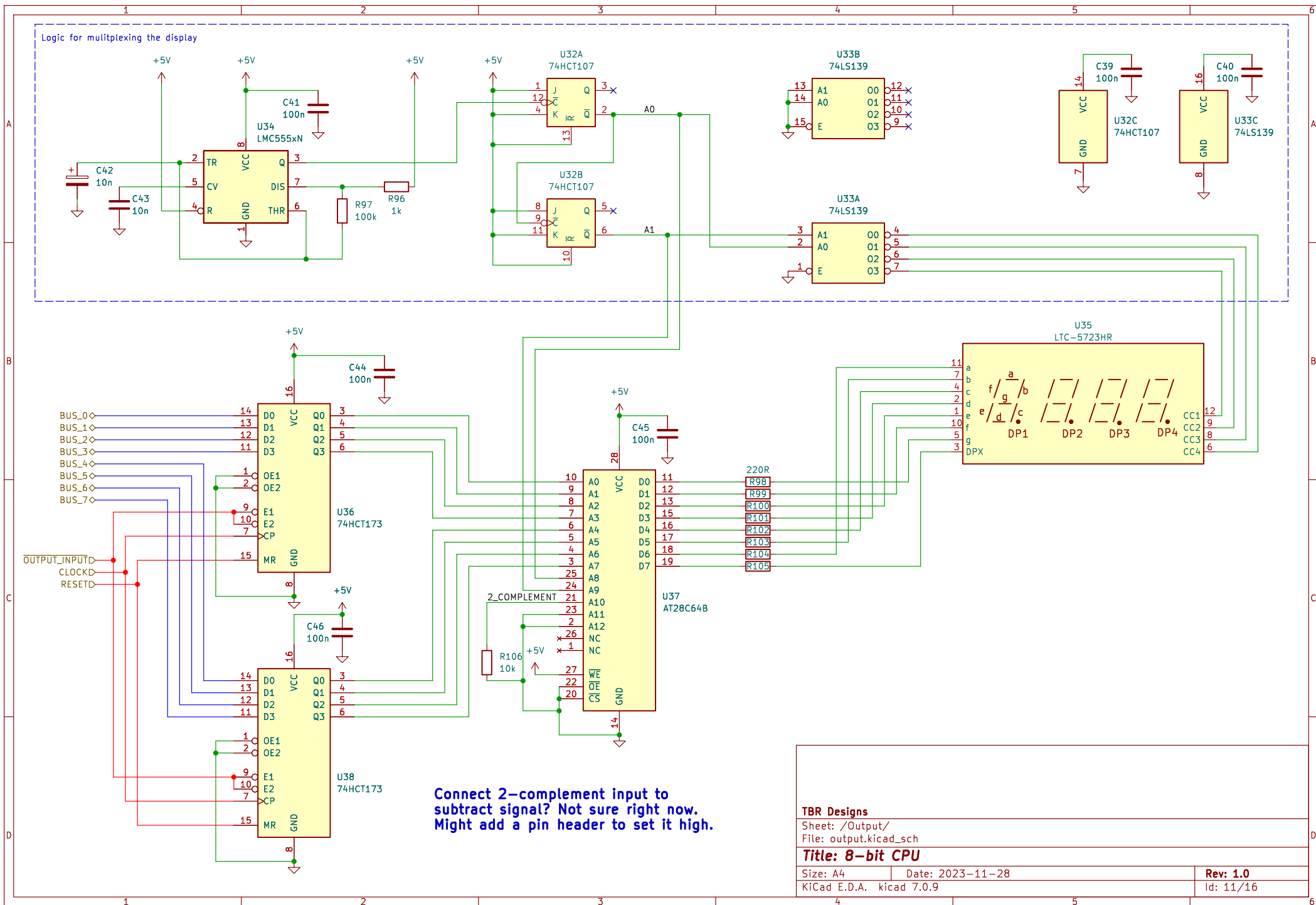
**TBR Designs**

Sheet: /Program Counter/  
File: program-counter.kicad\_sch

**Title: 8-bit CPU**

Size: A4 Date: 2023-11-28  
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Sheet: /Output/  
File: output.kicad\_sch

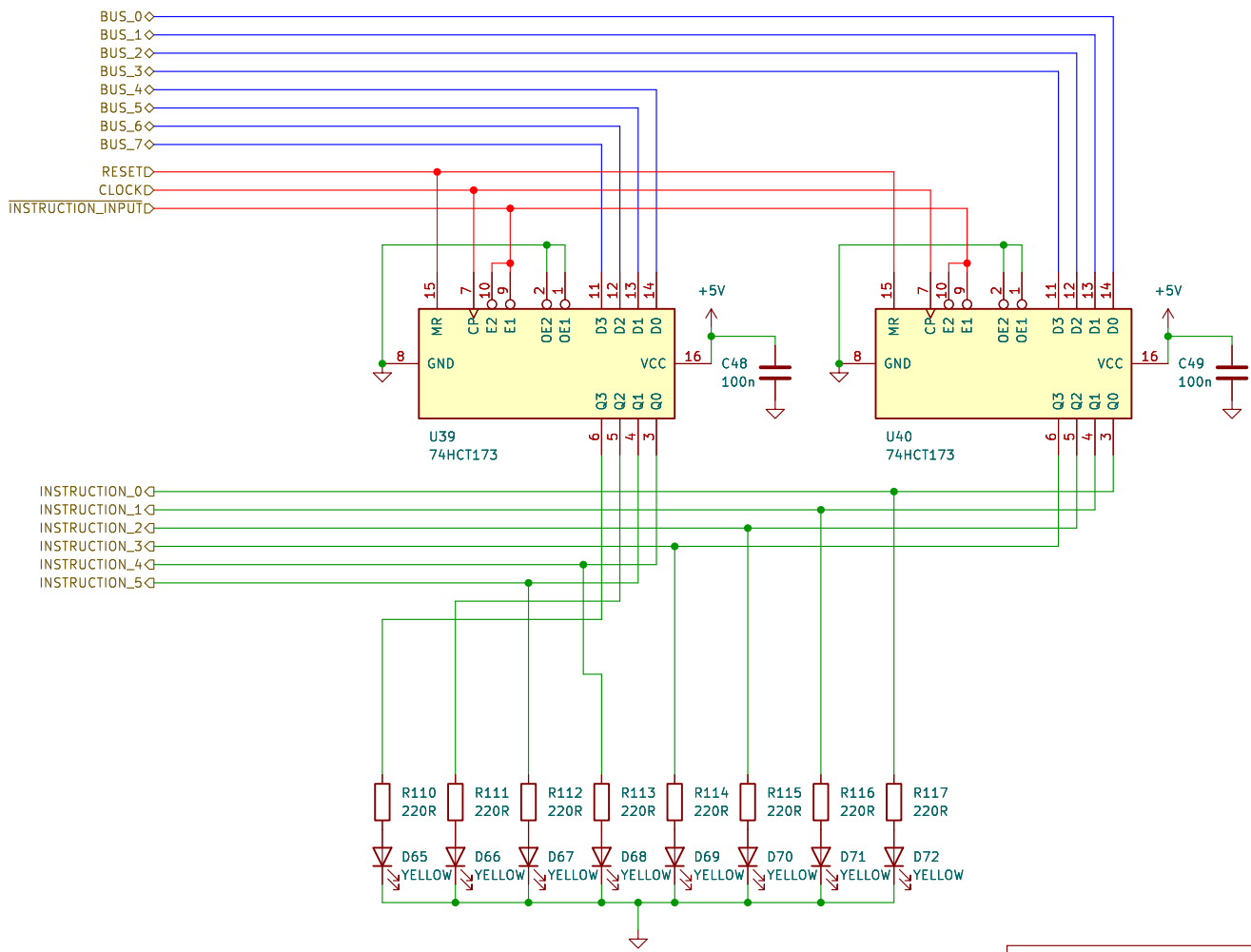
**Title: 8-bit CPU**

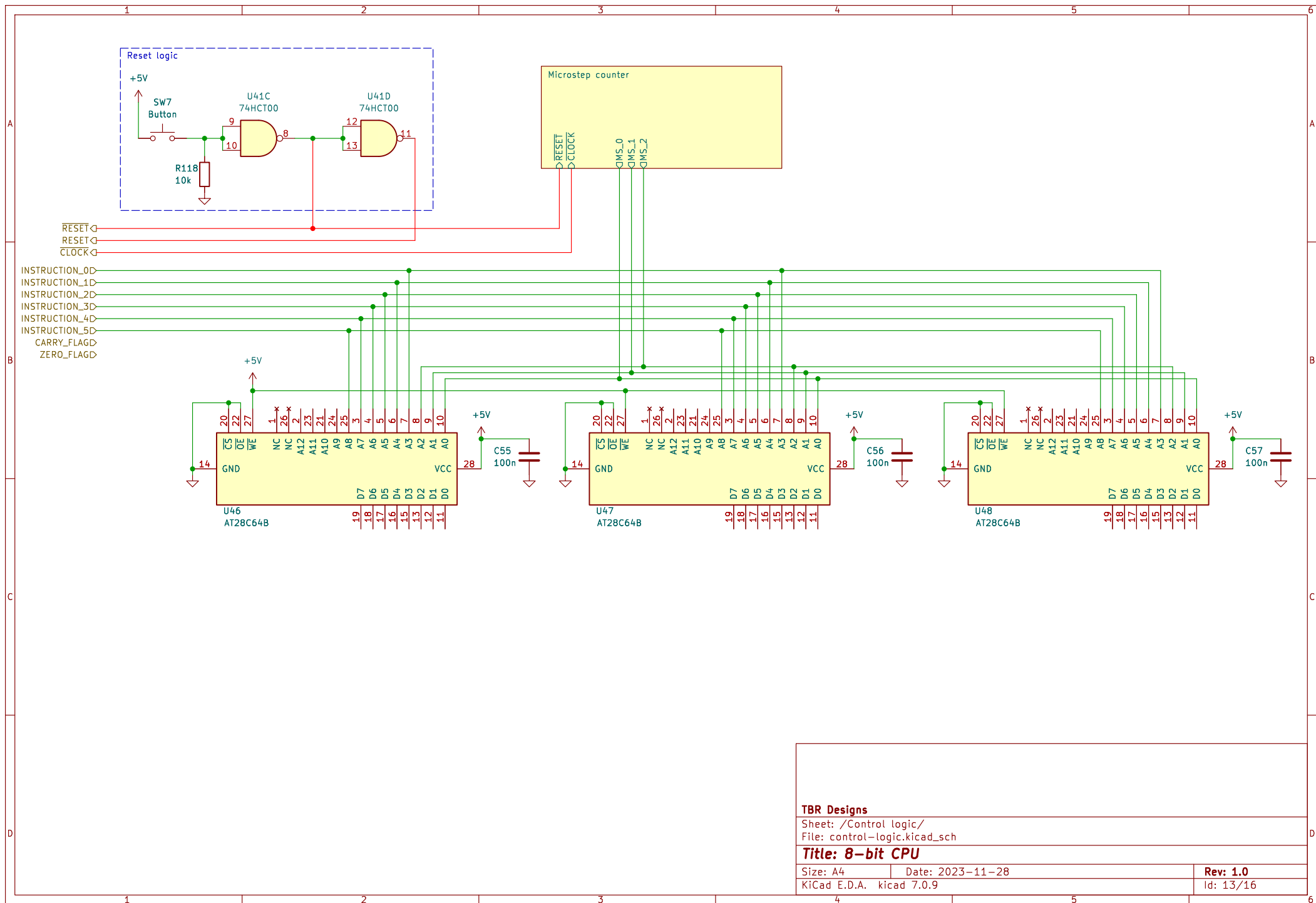
Size: A4 Date: 2023-11-28

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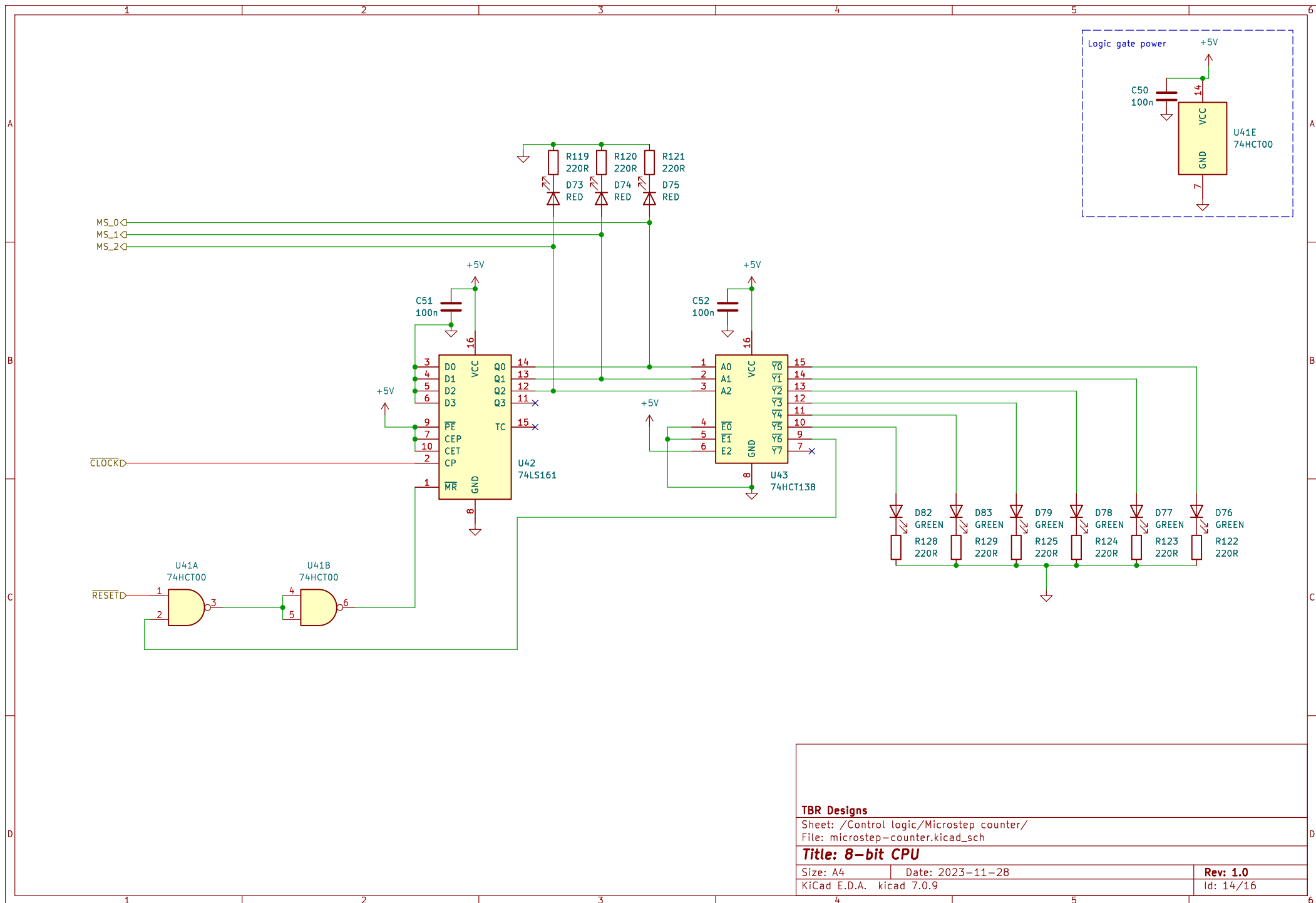
**TBR Designs**

Sheet: /Control logic/  
File: control-logic.kicad\_sch

**Title: 8-bit CPU**

Size: A4 Date: 2023-11-28  
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# TBR Designs

Sheet: /Control logic/Microstep counter/  
File: microstep-counter.kicad\_sch

## Title: 8-bit CPU

Size: A4 Date: 2023-11-28

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Rev: 1.0

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