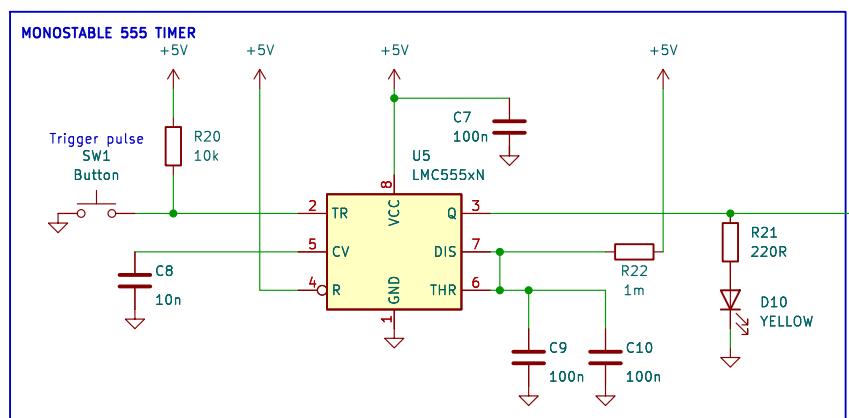
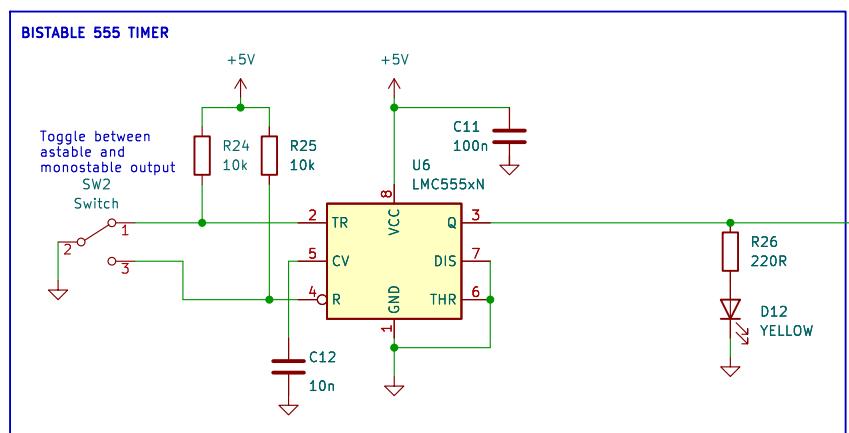


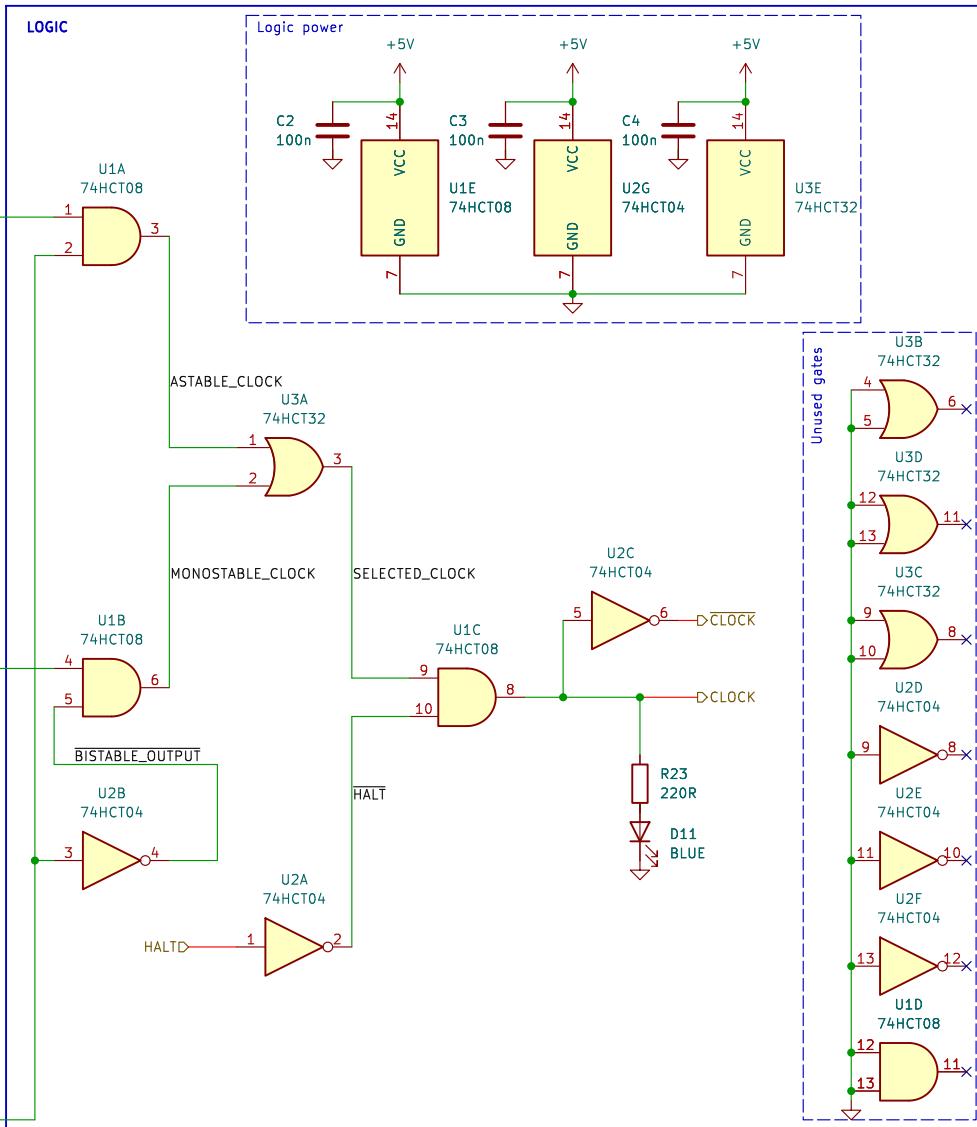
ASTABLE_OUTPUT



MONOSTABLE_OUTPUT



BISTABLE_OUTPUT



Provides a clock pulse and inverted clock pulse.
Speed can be set with the potentiometer.
Manual pulse can be chosen with the switch and pulsed with the button.
If HALT is HIGH no pulse is outputted.

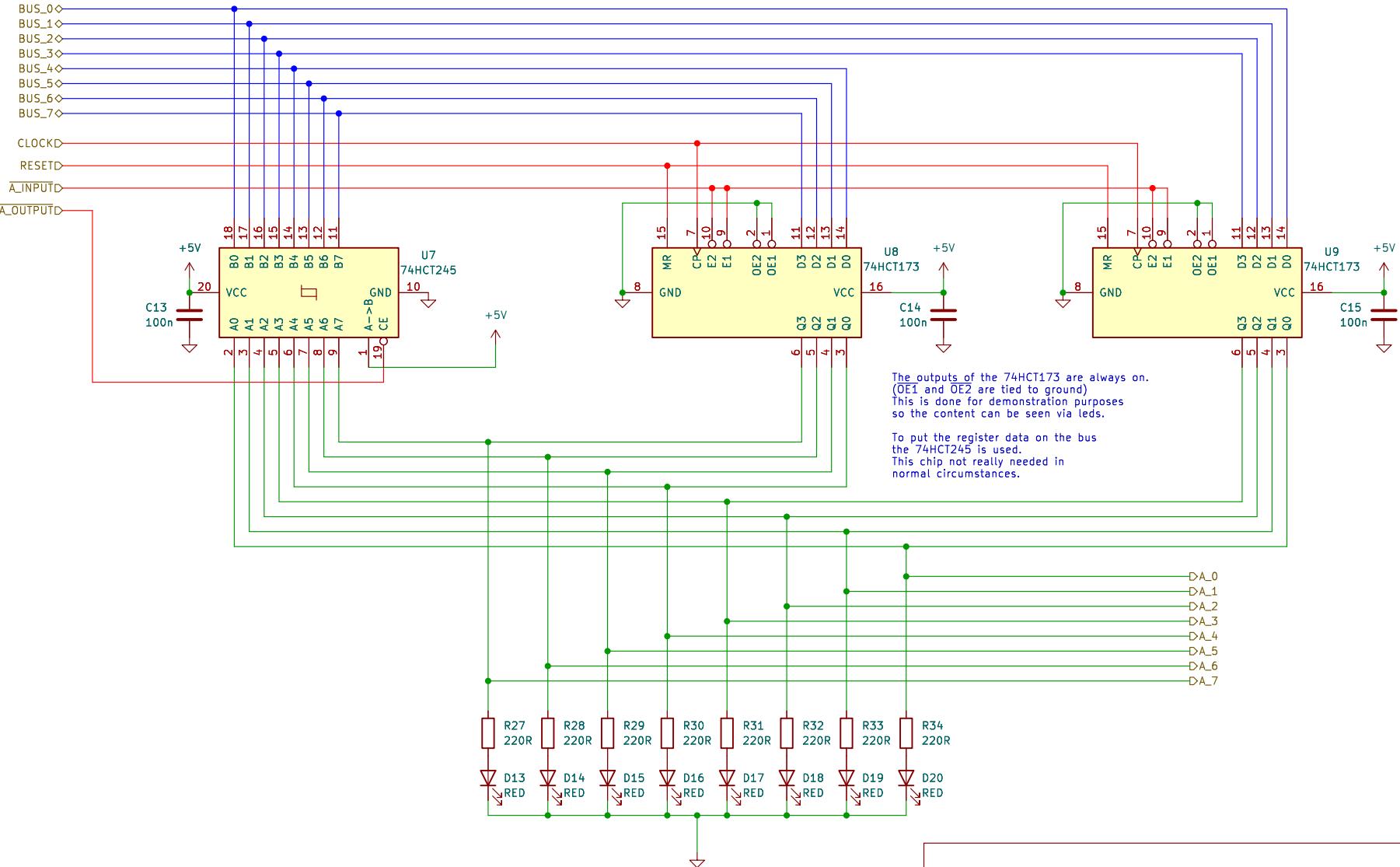
TBR Designs

Sheet: /Clock/
File: clock.kicad_sch

Title: 8-bit CPU

Size: A4 Date: 2023-11-28
KiCad E.D.A. kicad 7.0.9

Rev: 1.0
Id: 2/18



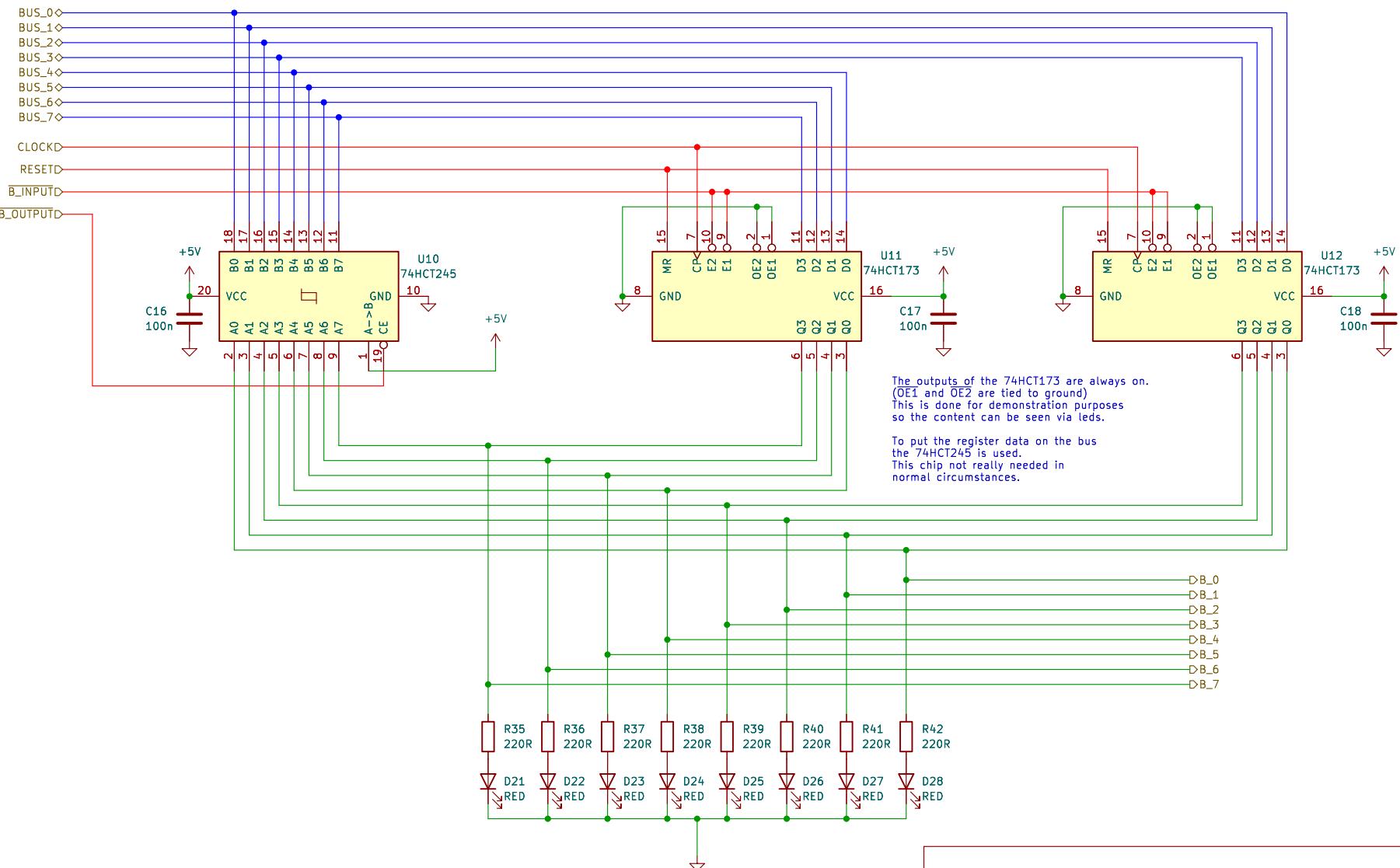
TBR Designs

Sheet: /A register/
File: a-register.kicad_sch

Title: 8-bit CPU

Size: A4 | Date: 2023-11-28
KiCad E.D.A. kicad 7.0.9

Rev: 1.0
Id: 3/18



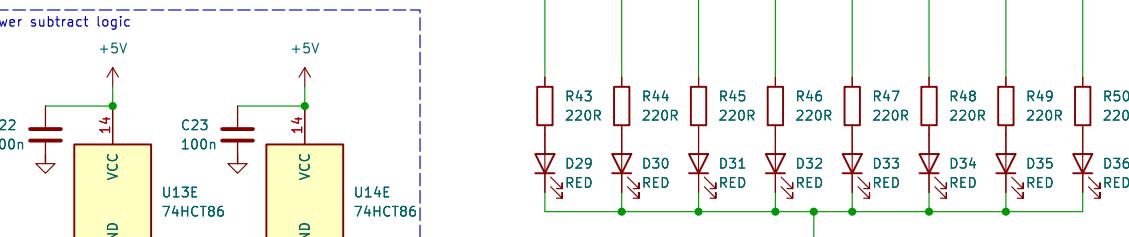
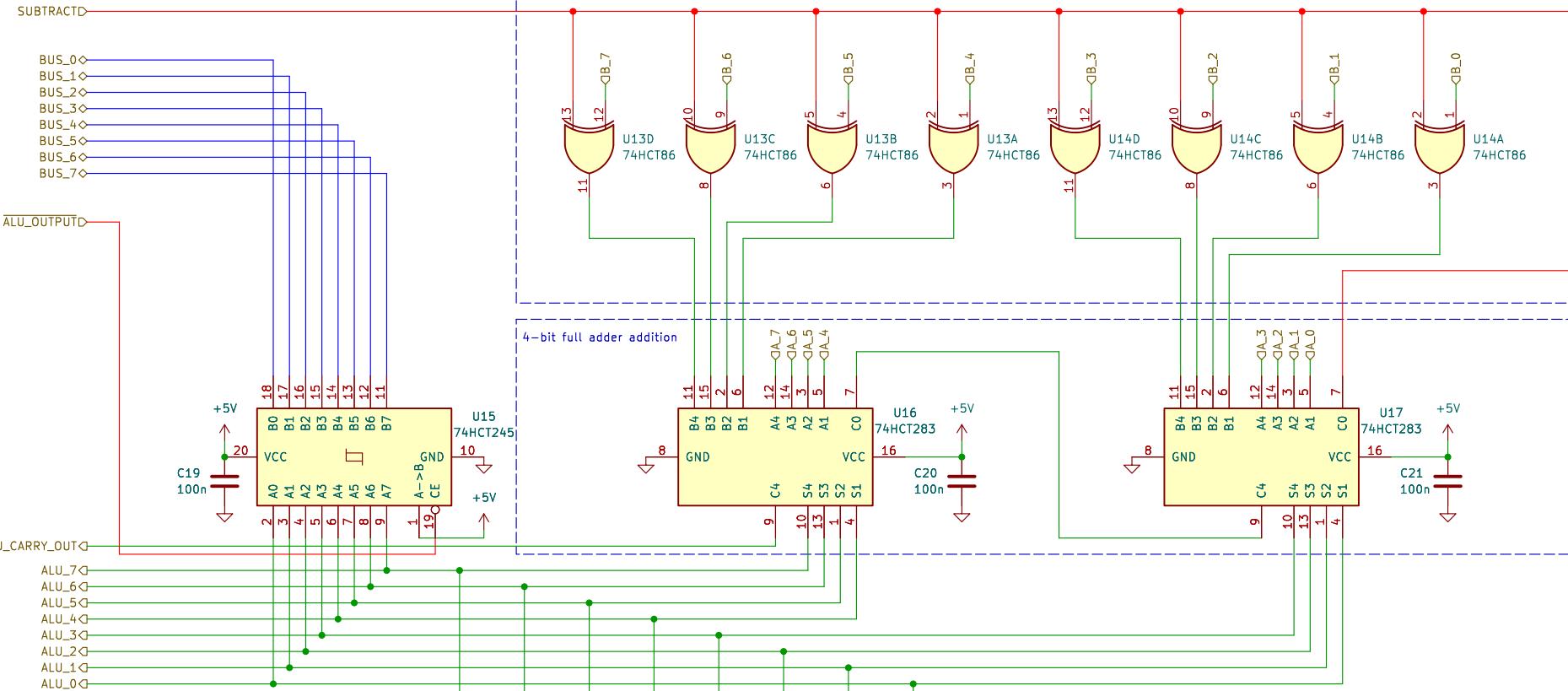
TBR Designs

Sheet: /B register/
File: b-register.kicad_sch

Title: 8-bit CPU

Size: A4 | Date: 2023-11-28
KiCad E.D.A. kicad 7.0.9

Rev: 1.0
Id: 4/18



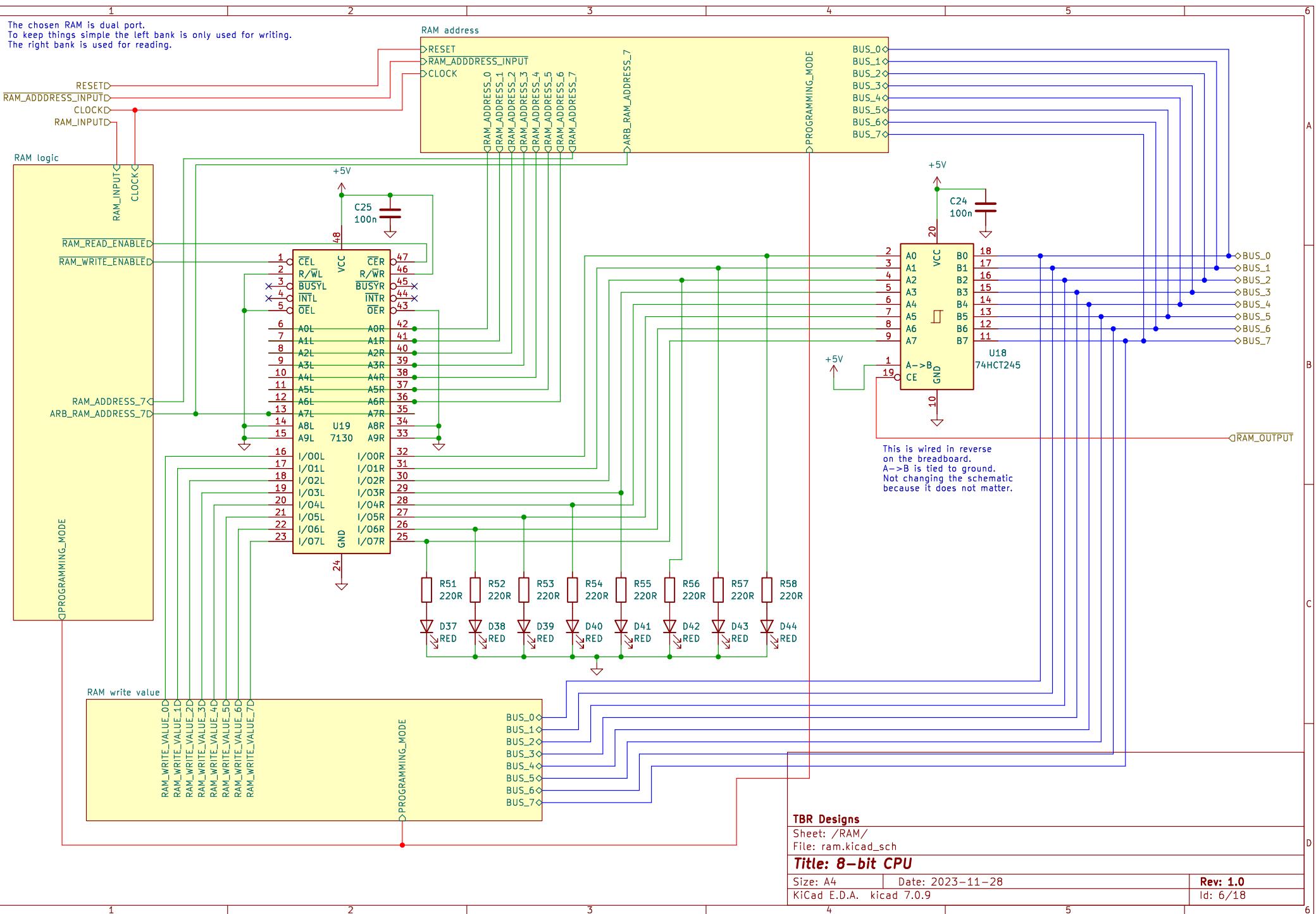
TBR Designs

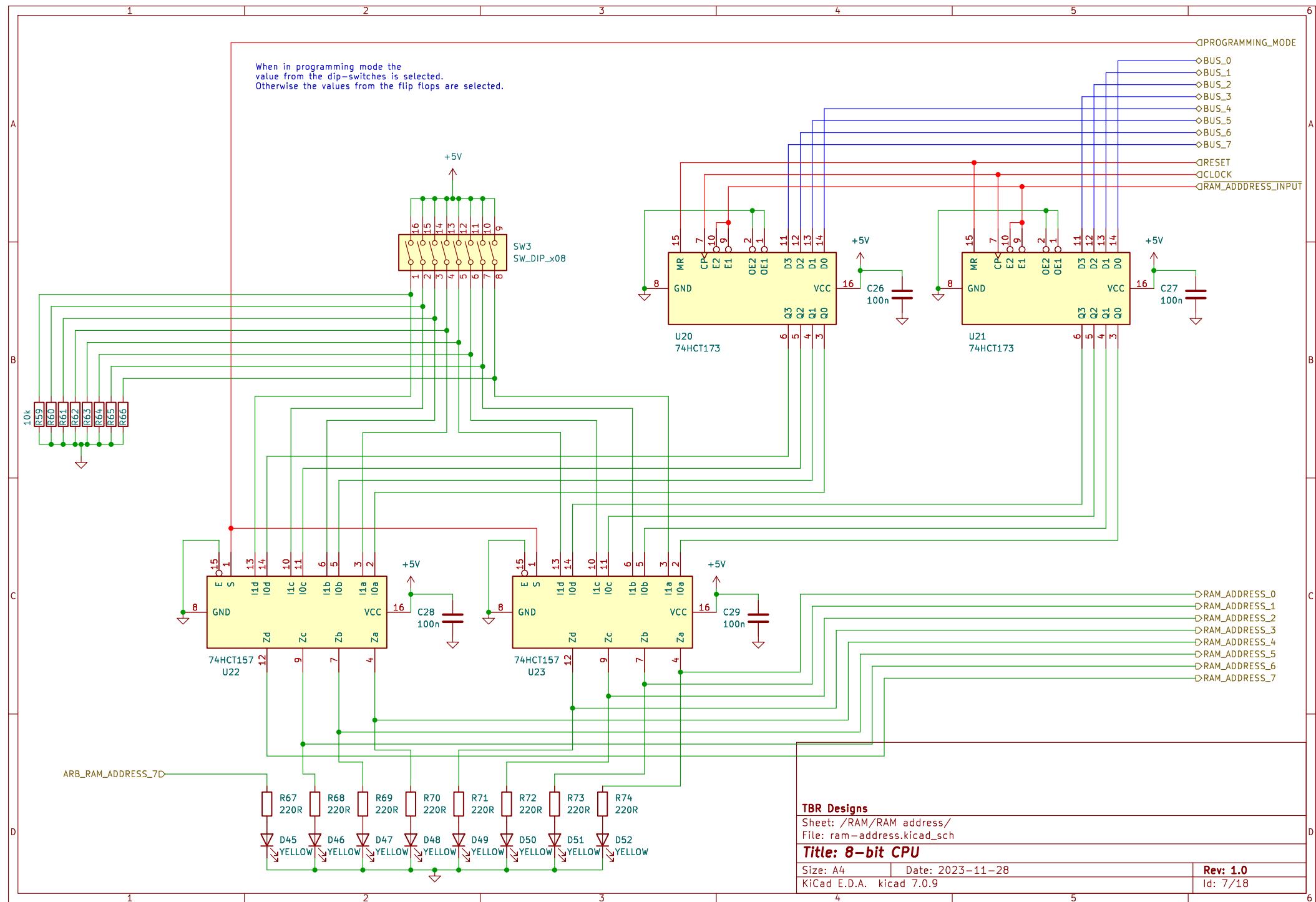
Sheet: /ALU/
File: alu.kicad_sch

Title: 8-bit CPU

Size: A4 Date: 2023-11-28
KiCad E.D.A. kicad 7.0.9

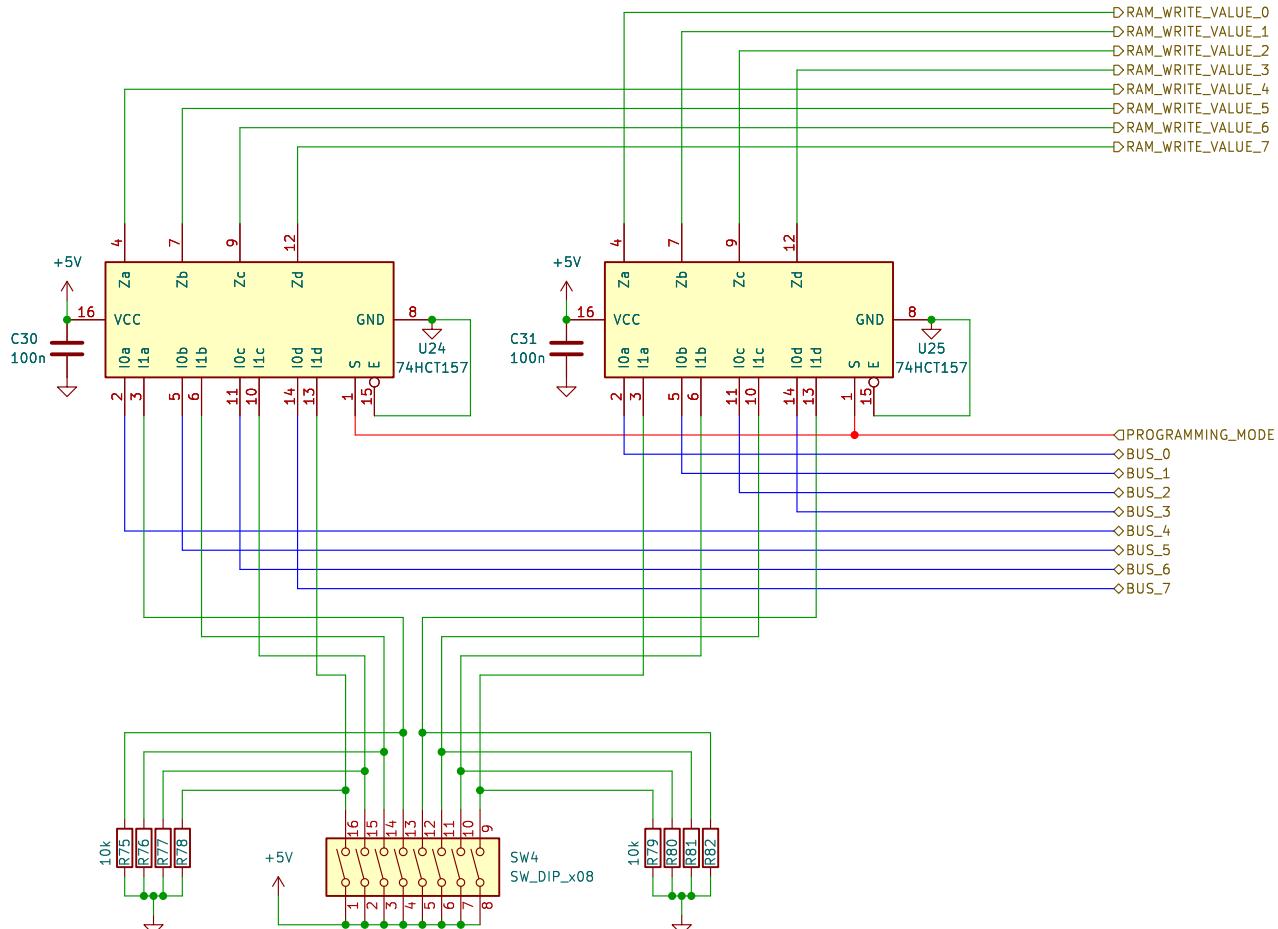
Rev: 1.0
Id: 5/18





1 2 3 4 5 6

When in programming mode the value from the dip-switches is selected. Otherwise the value from the bus is selected.



TBR Designs

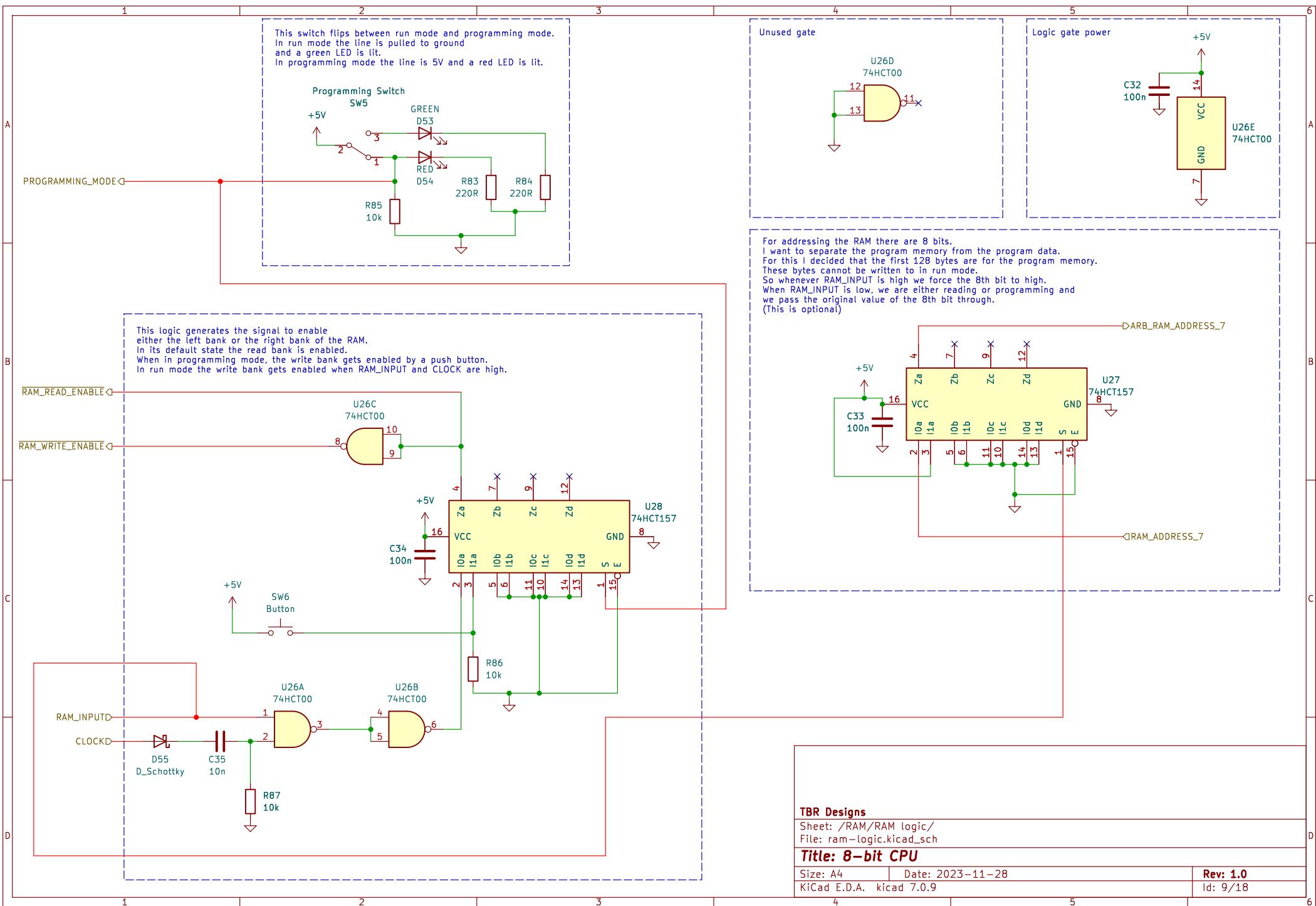
Sheet: /RAM/RAM write value/
File: ram-write-value.kicad_sch

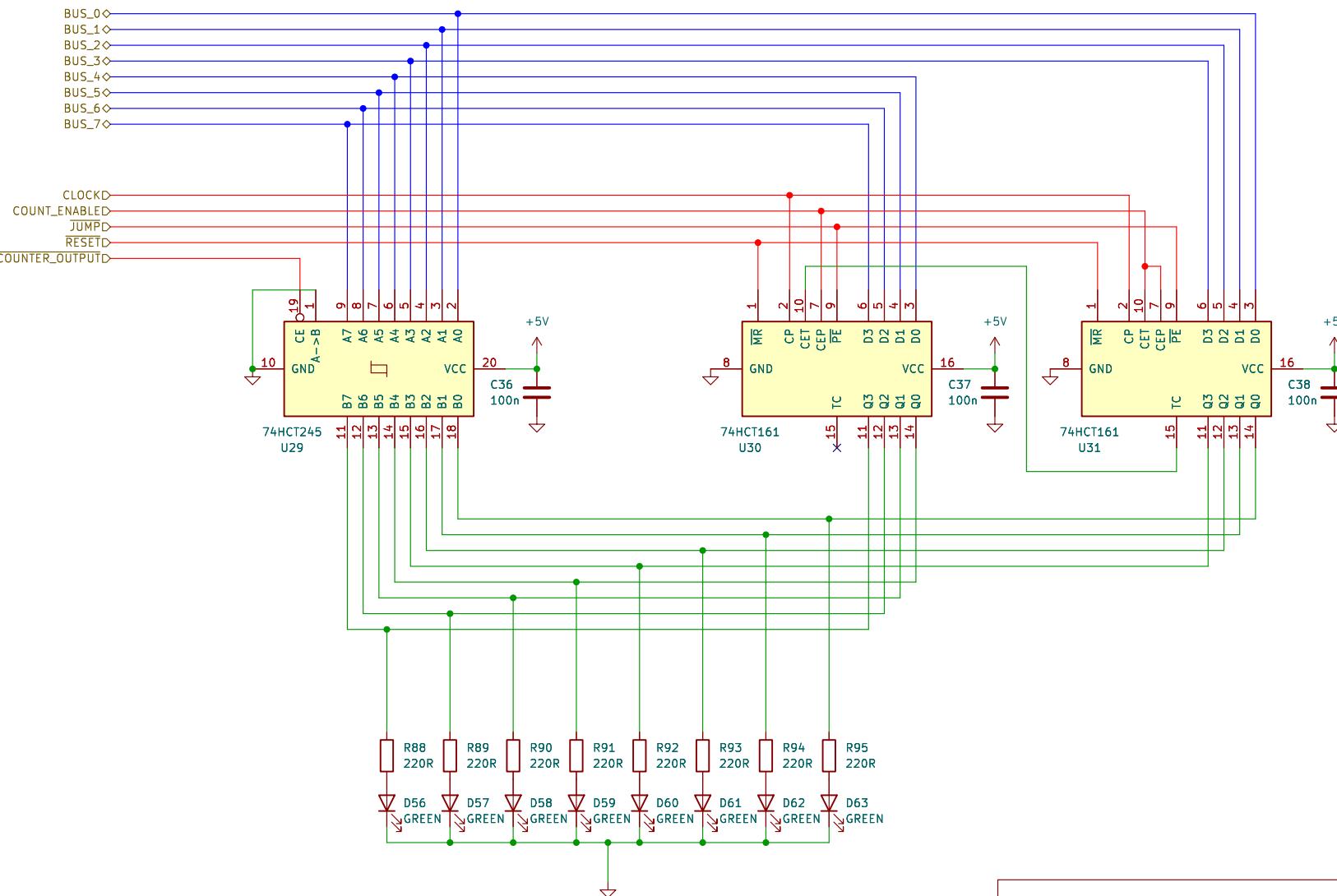
Title: 8-bit CPU

Size: A4 Date: 2023-11-28
KiCad E.D.A. kicad 7.0.9

Rev: 1.0
Id: 8/18

1 2 3 4 5 6





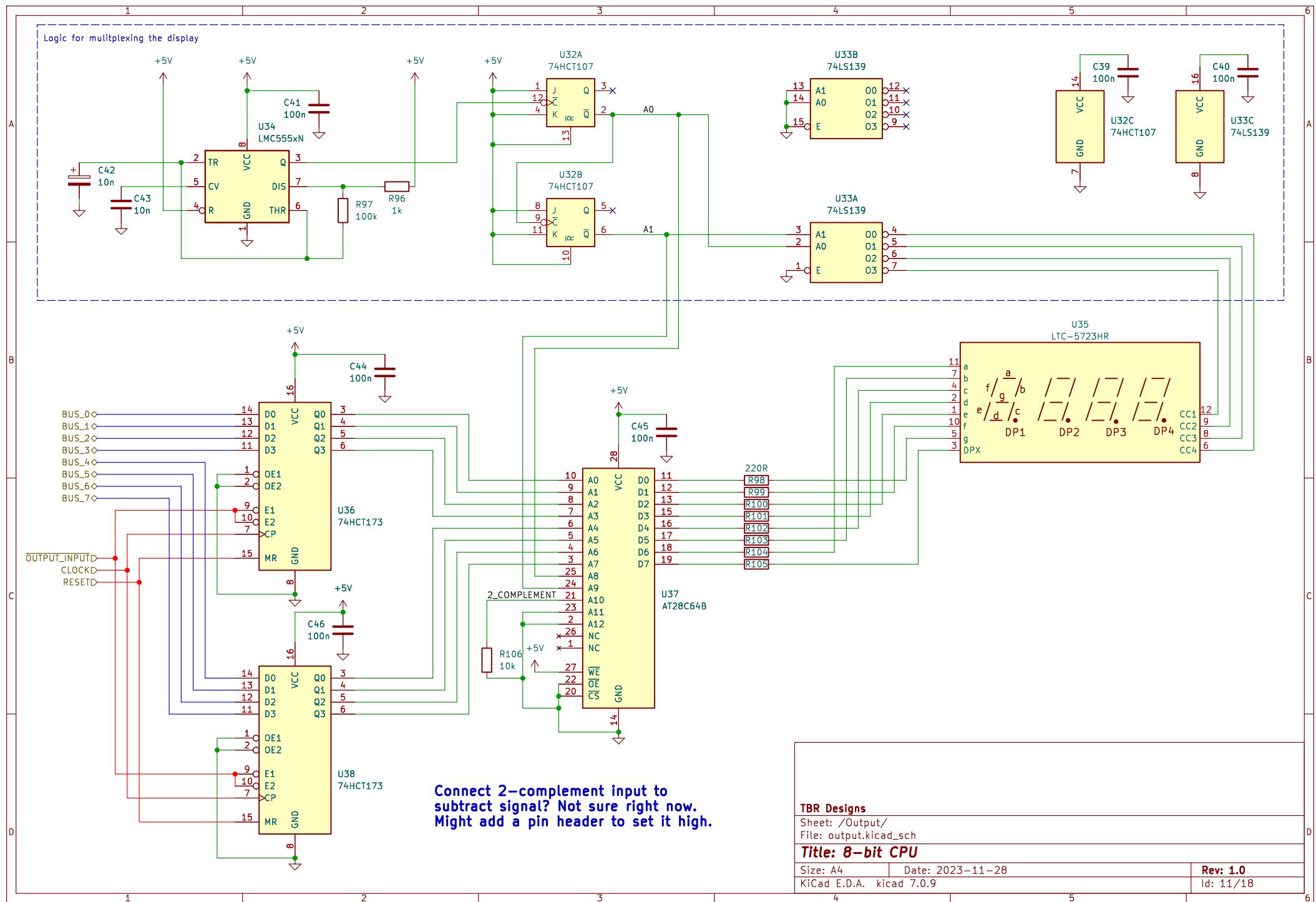
TBR Designs

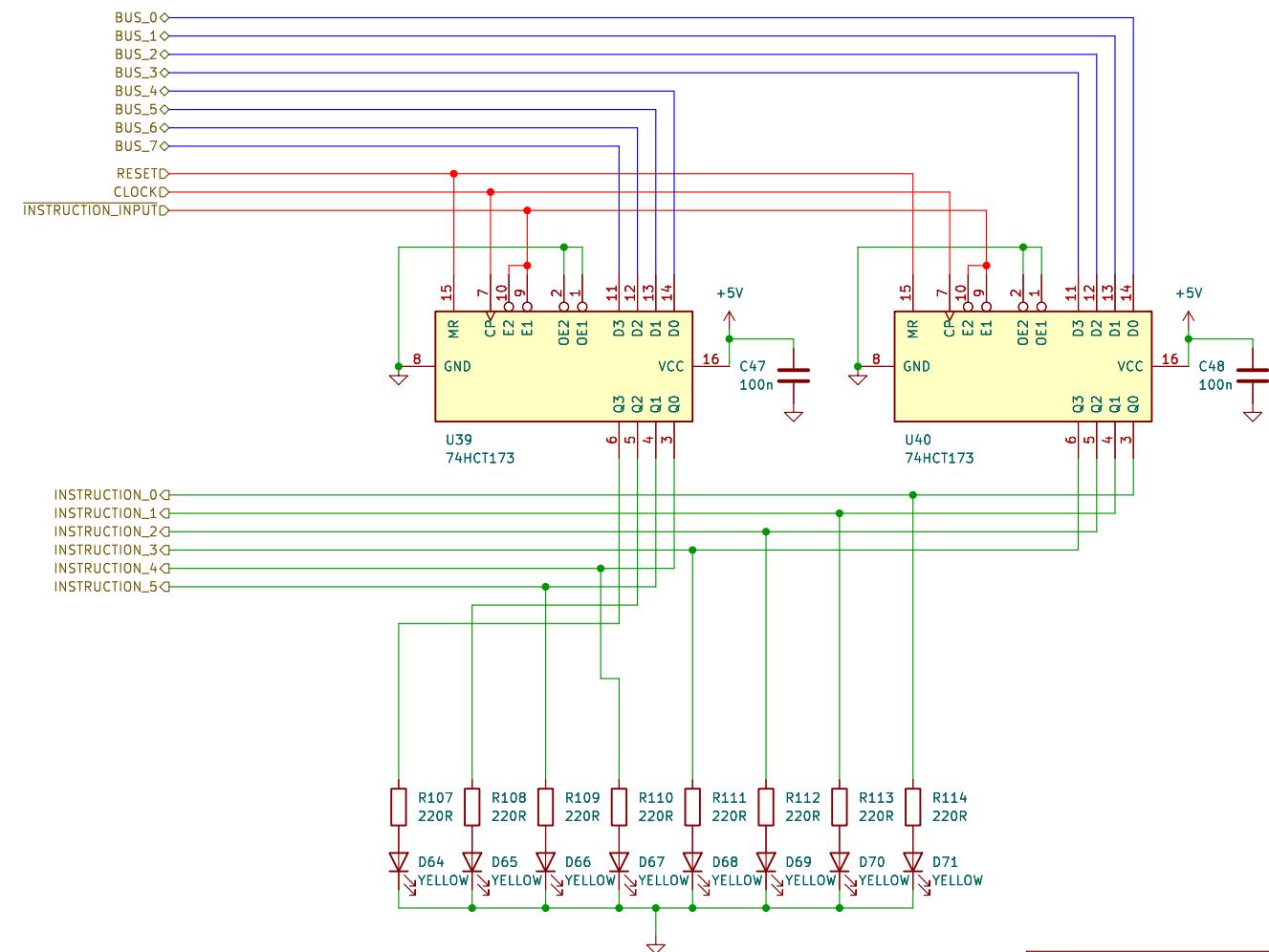
Sheet: /Program Counter/
File: program-counter.kicad_sch

Title: 8-bit CPU

Size: A4 | Date: 2023-11-28
KiCad E.D.A. kicad 7.0.9

Rev: 1.0
Id: 10/18





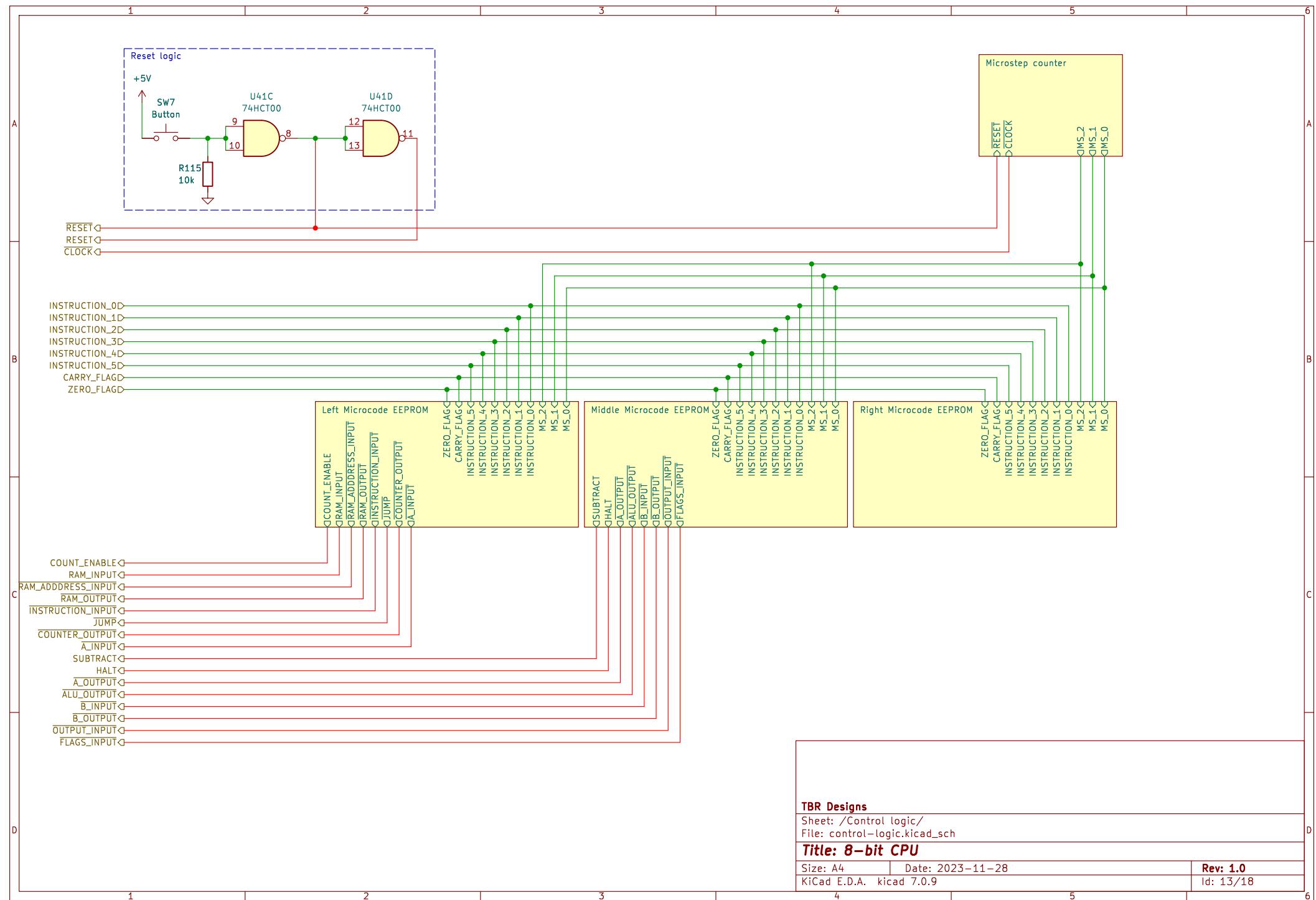
TBR Designs

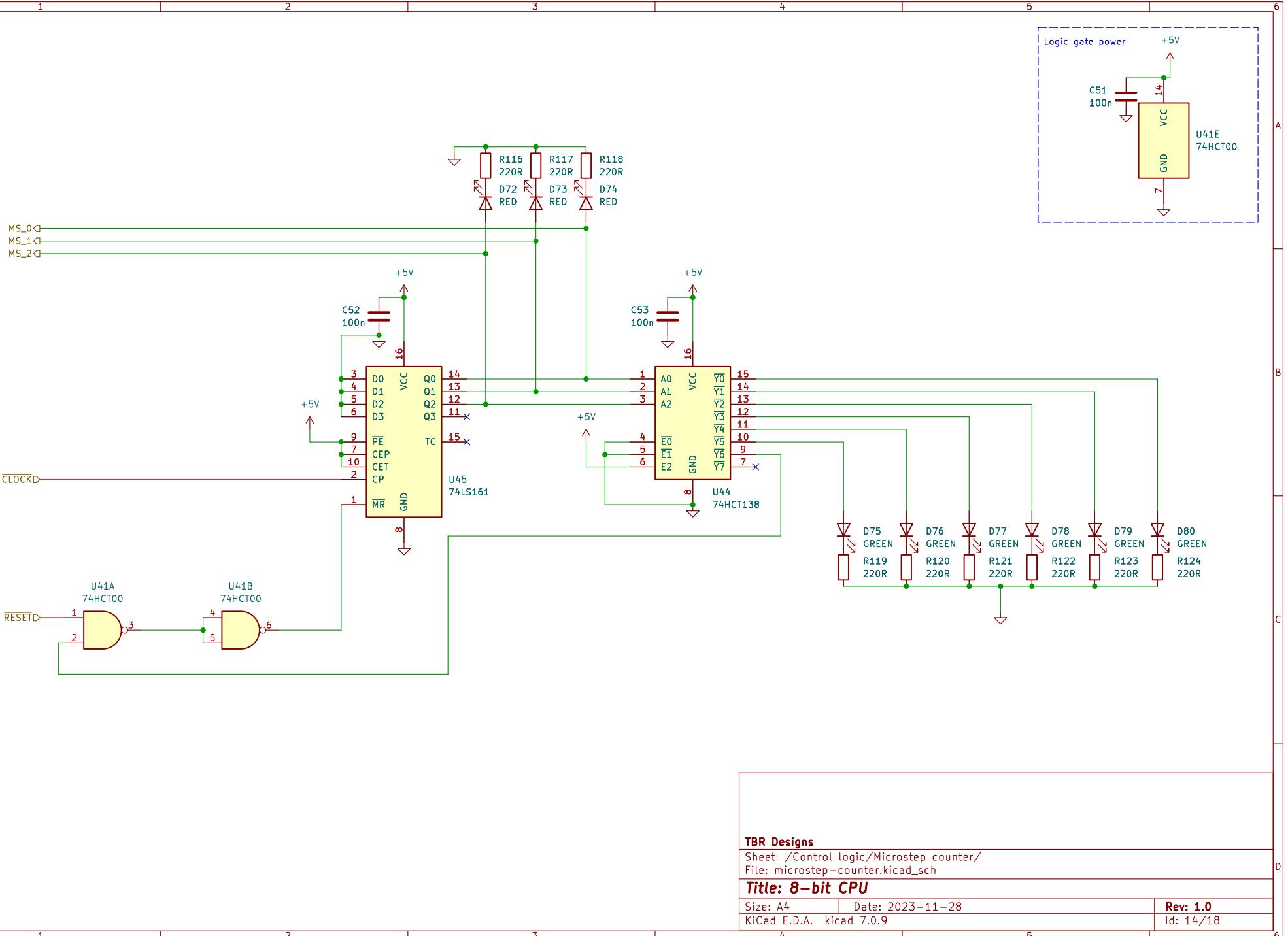
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 File: instruction-register.kicad_sch

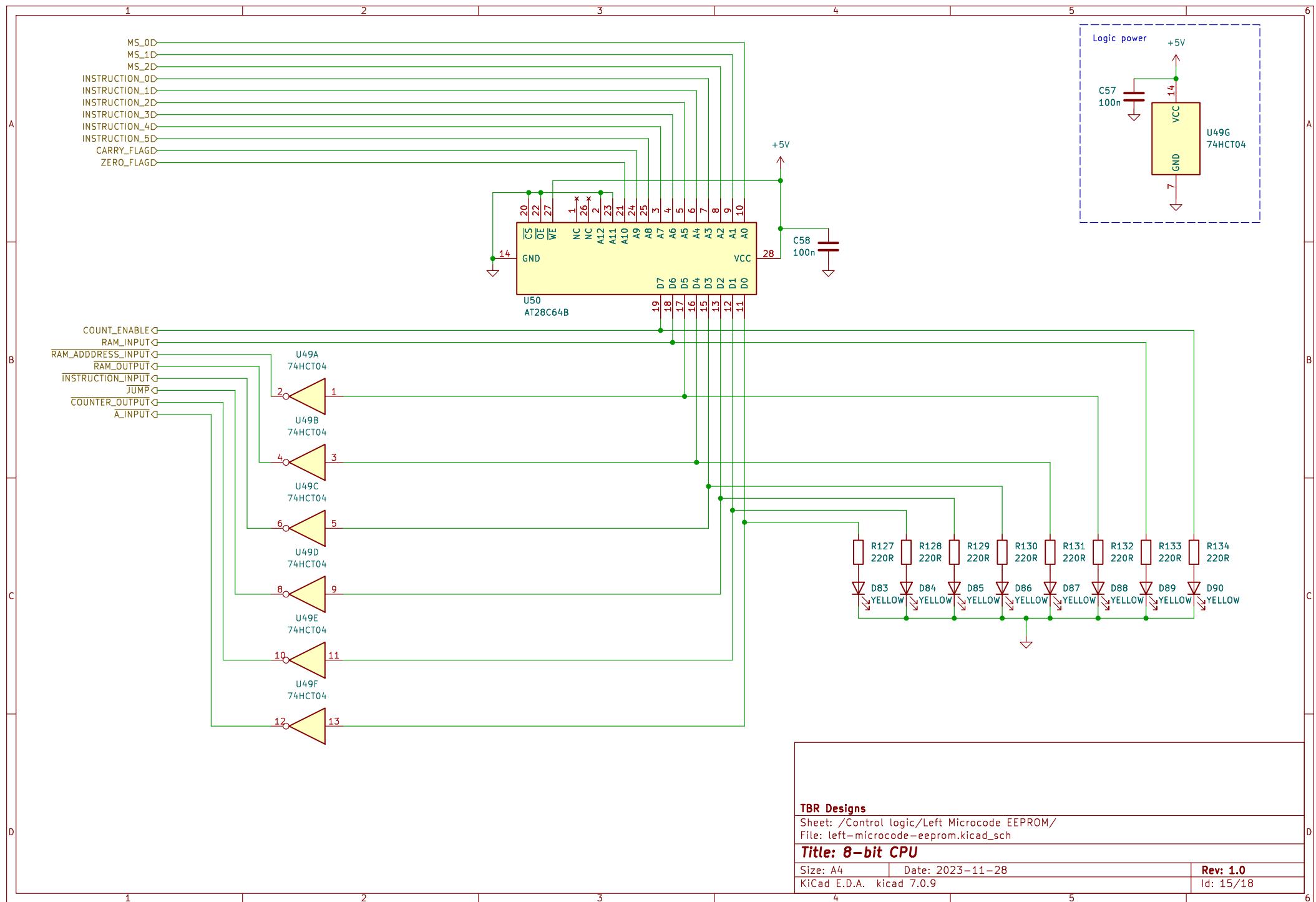
Title: 8-bit CPU

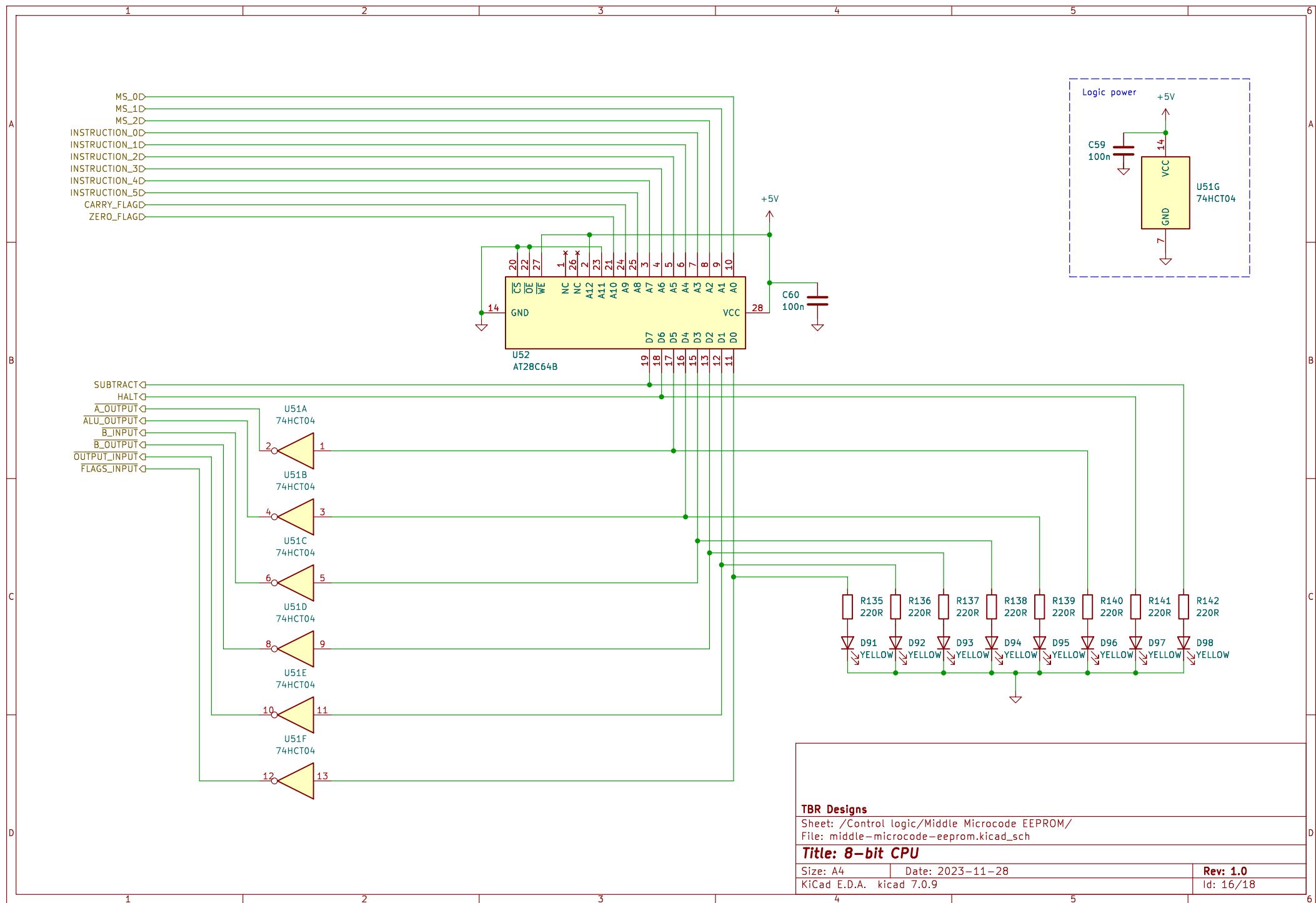
Size: A4 | Date: 2023-11-28
 KiCad E.D.A. kicad 7.0.9

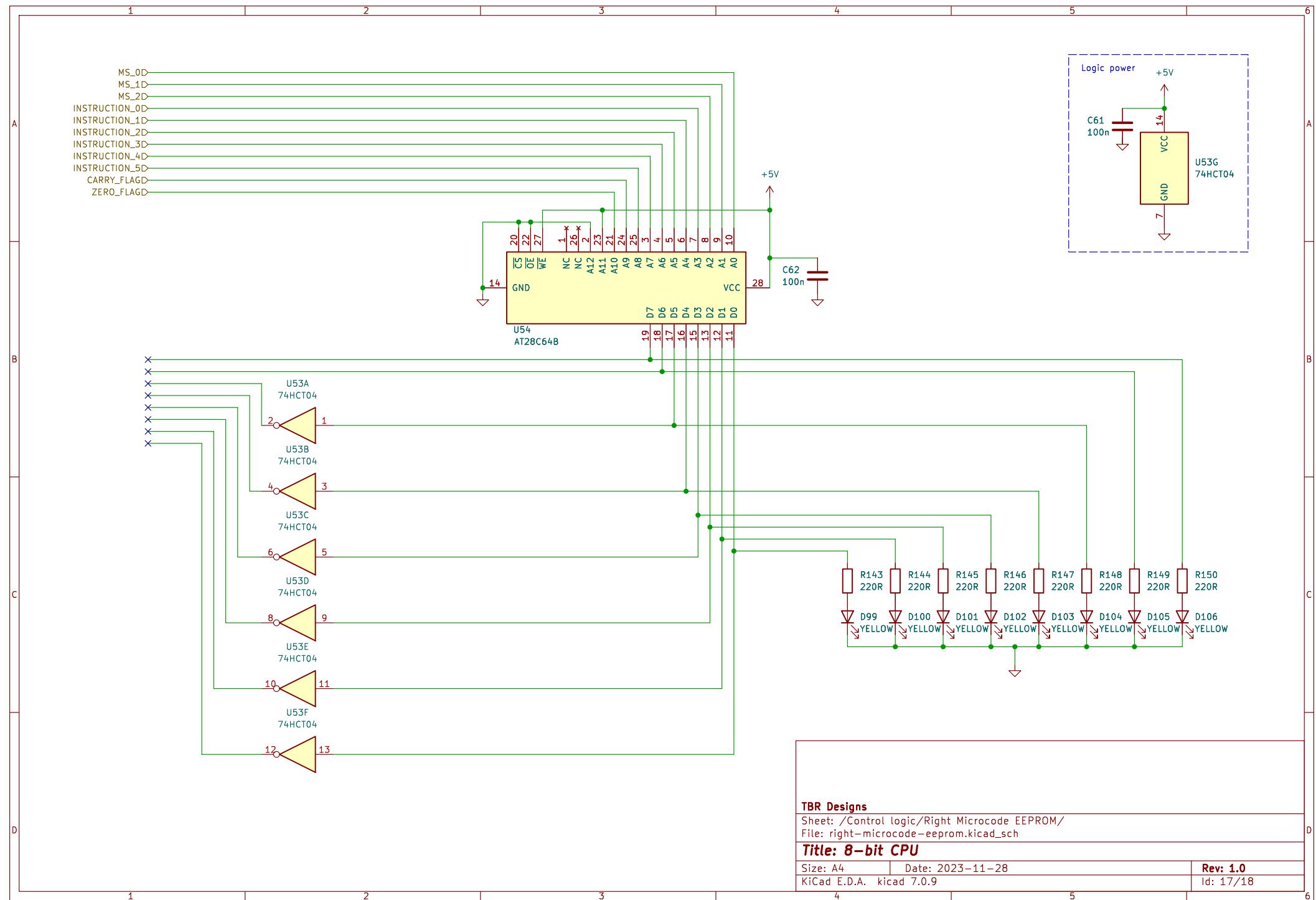
Rev: 1.0
 Id: 12/18



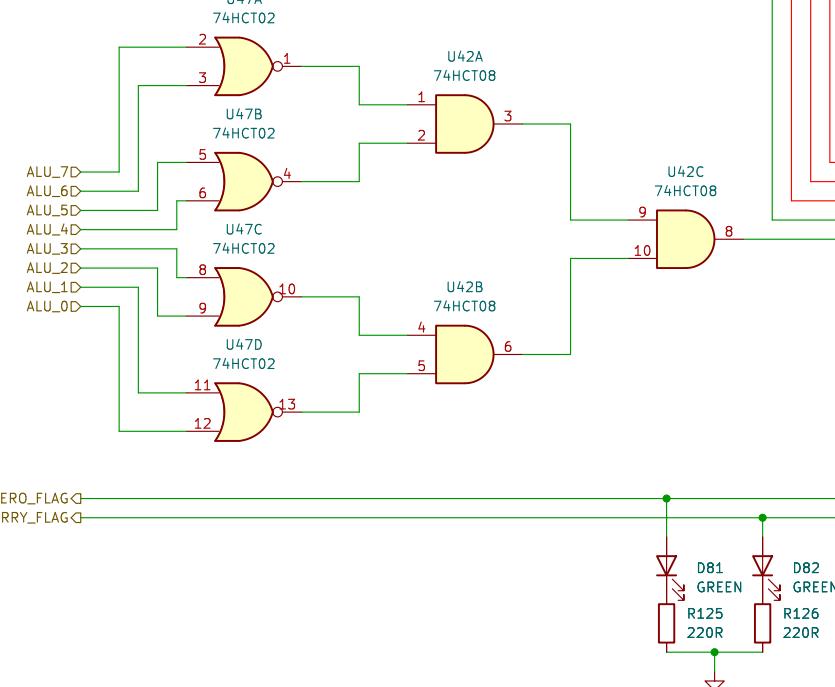








FLAGS_INPUT
CLOCK
RESET
ALU_CARRY_OUT



TBR Designs

Sheet: /CPU_flags/
File: cpu-flags.kicad_sch

Title: 8-bit CPU

Size: A4 | Date: 2023-11-28
KiCad E.D.A. kicad 7.0.9

Rev: 1.0
Id: 18/18