

TBR Designs

Sheet: /

File: 8-bit-cpu.kicad_sch

Title: **8-bit CPU**

Size: A4

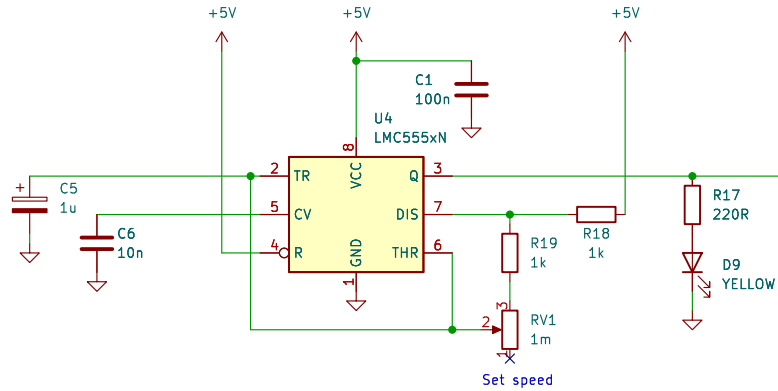
Date: 2023-11-28

KiCad E.D.A. kicad 7.0.9

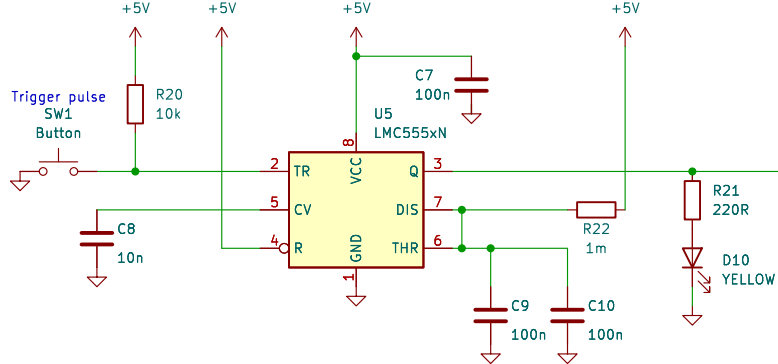
Rev: **1.0**

Id: 1/15

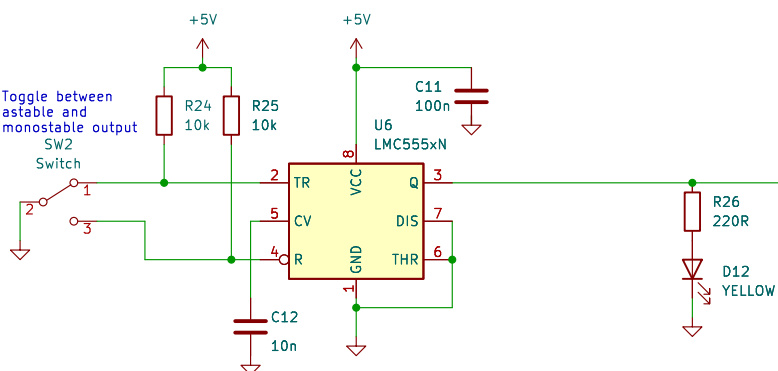
ASTABLE 555 TIMER



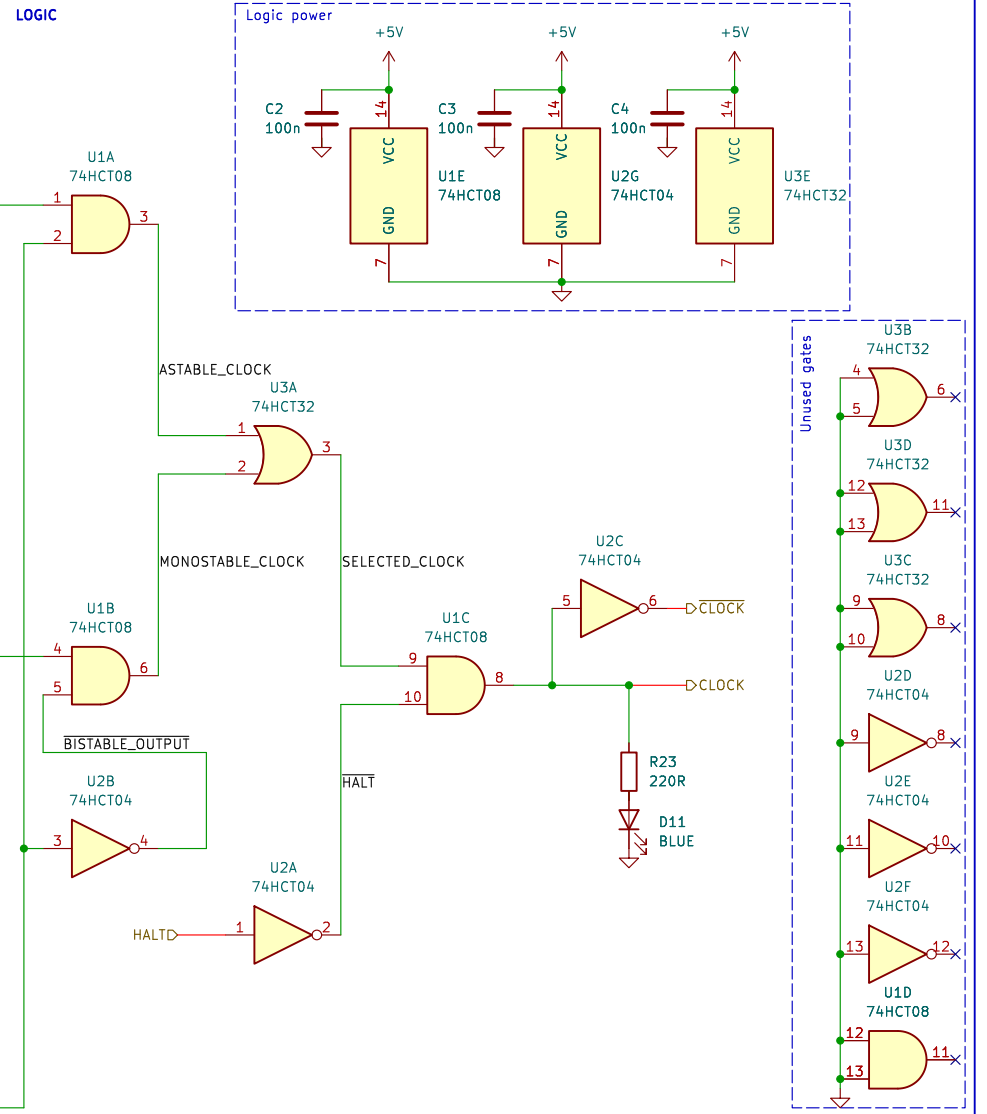
MONOSTABLE 555 TIMER



BISTABLE 555 TIMER



LOGIC



Provides a clock pulse and inverted clock pulse.
Speed can be set with the potentiometer.
Manual pulse can be chosen with the switch and pulsed with the button.
If HALT is HIGH no pulse is outputted.

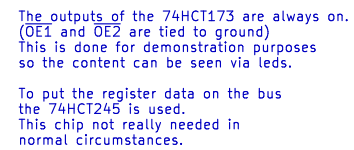
TBR Designs

Sheet: /Clock/
File: clock.kicad_sch

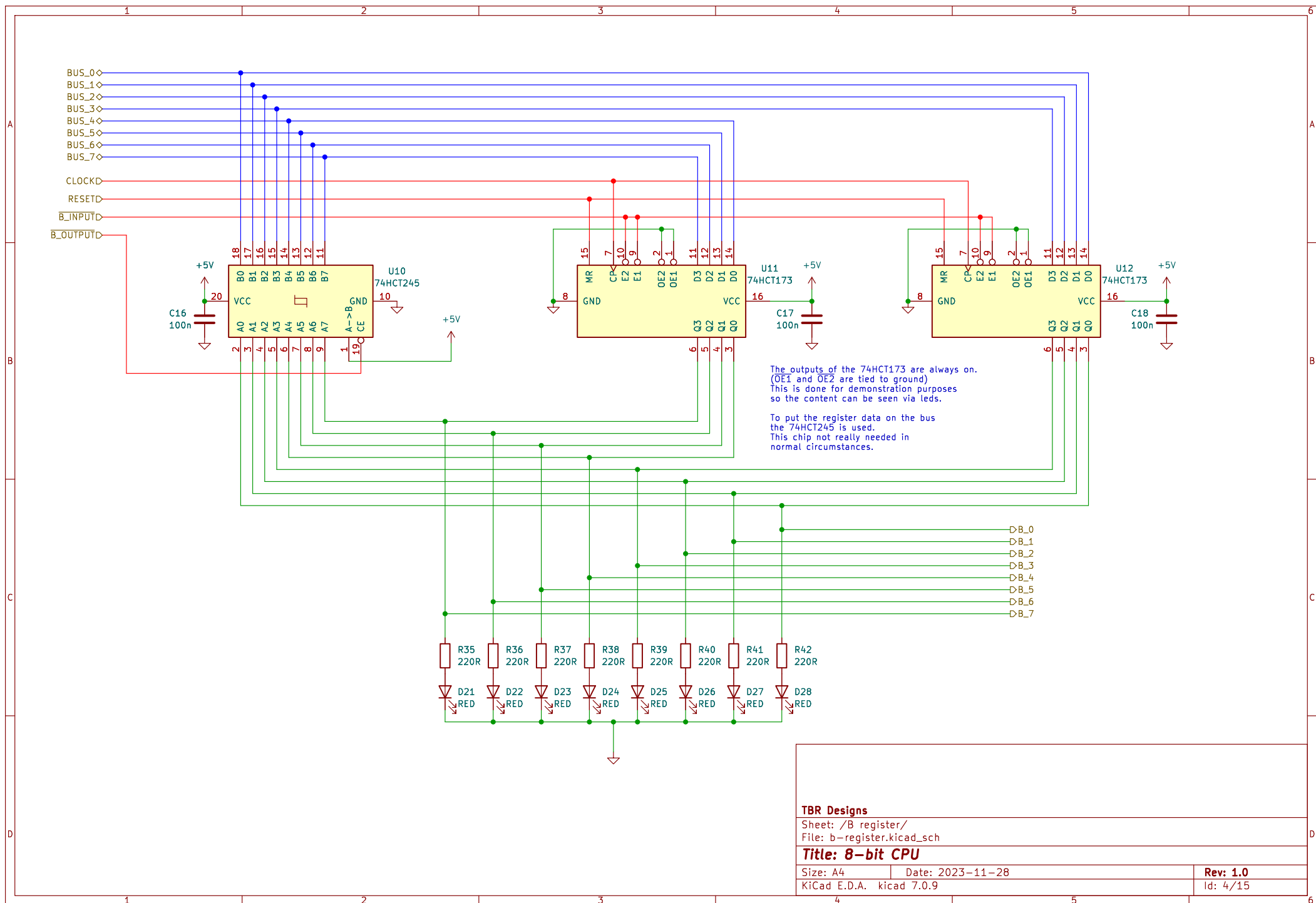
Title: 8-bit CPU

Size: A4 Date: 2023-11-28
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Rev: 1.0
Id: 2/15



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Sheet: /B register/

File: b-register.kicad_sch

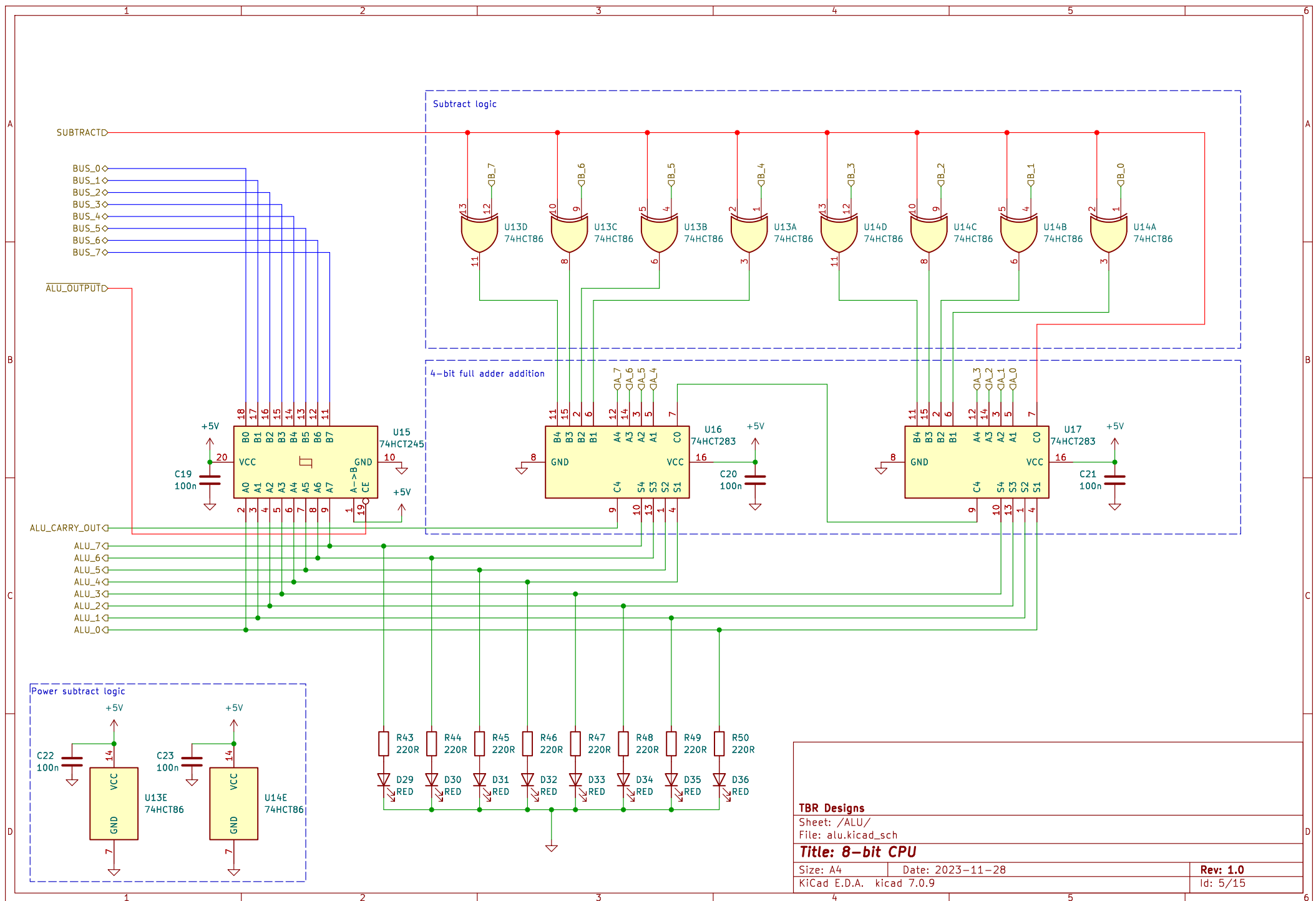
Title: 8-bit CPU

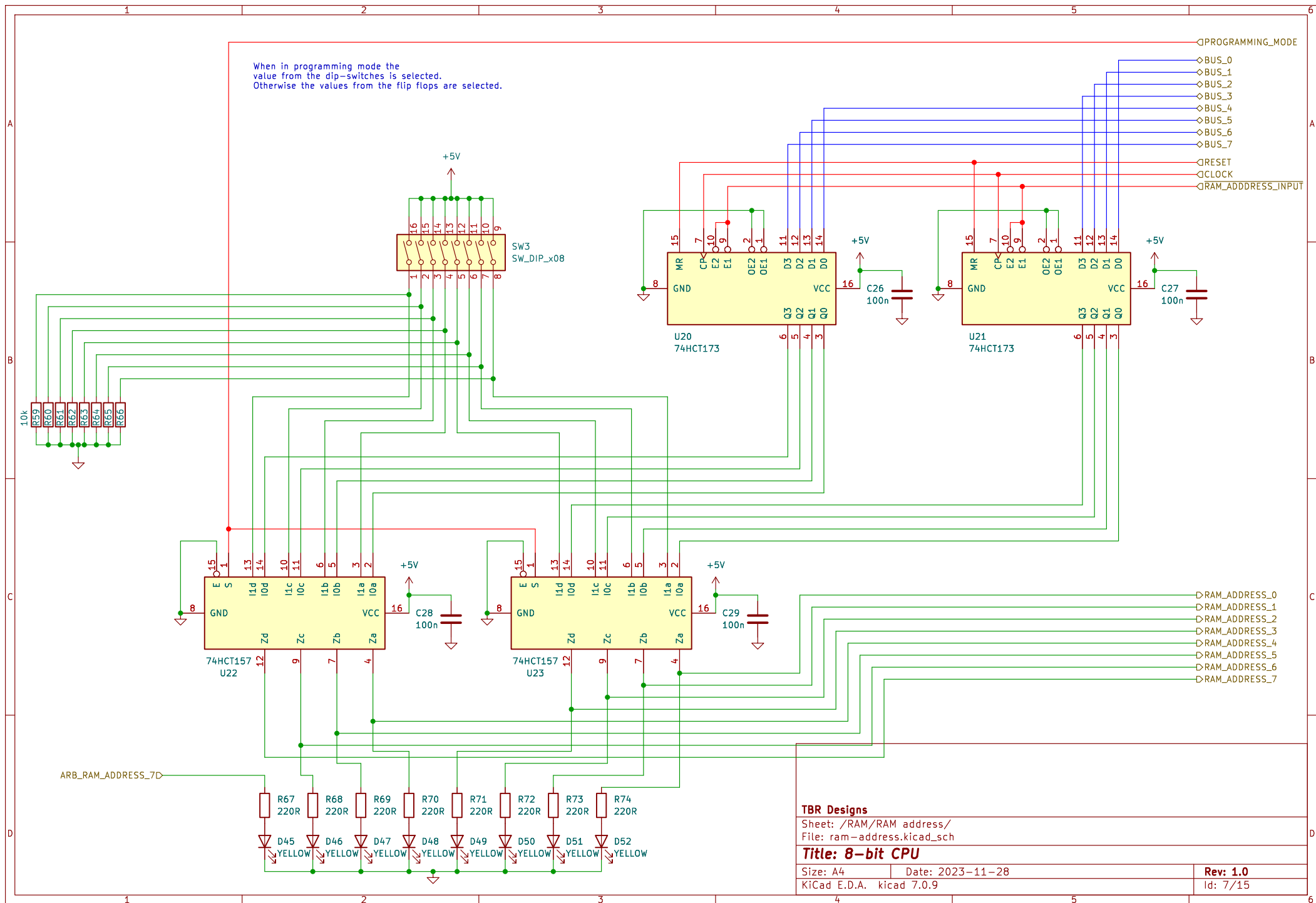
Size: A4 Date: 2023-11-28

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Rev: 1.0

Id: 4/15





TBR Designs

Sheet: /RAM/RAM address/

File: ram-address.kicad_sch

Title: 8-bit CPU

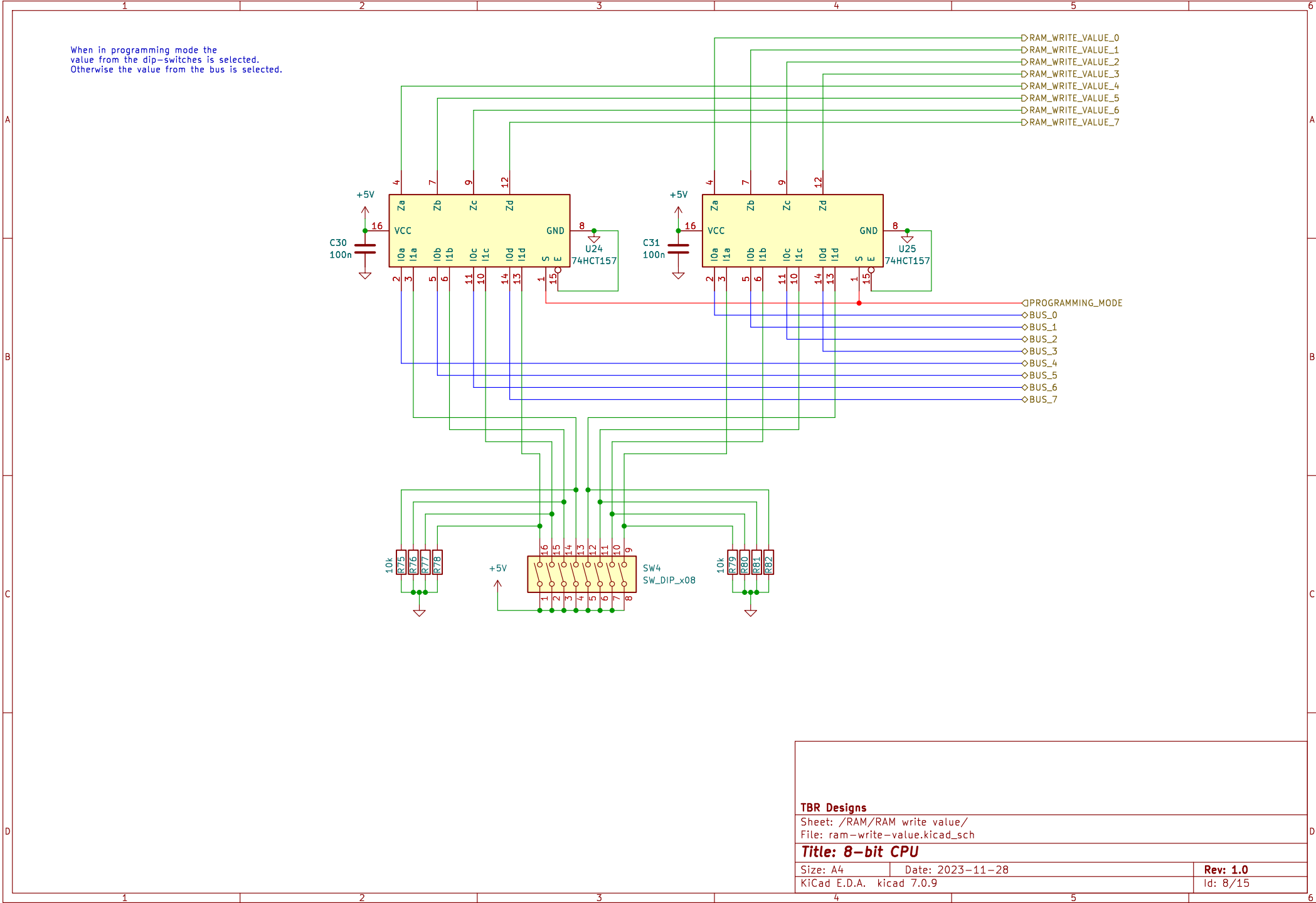
Size: A4

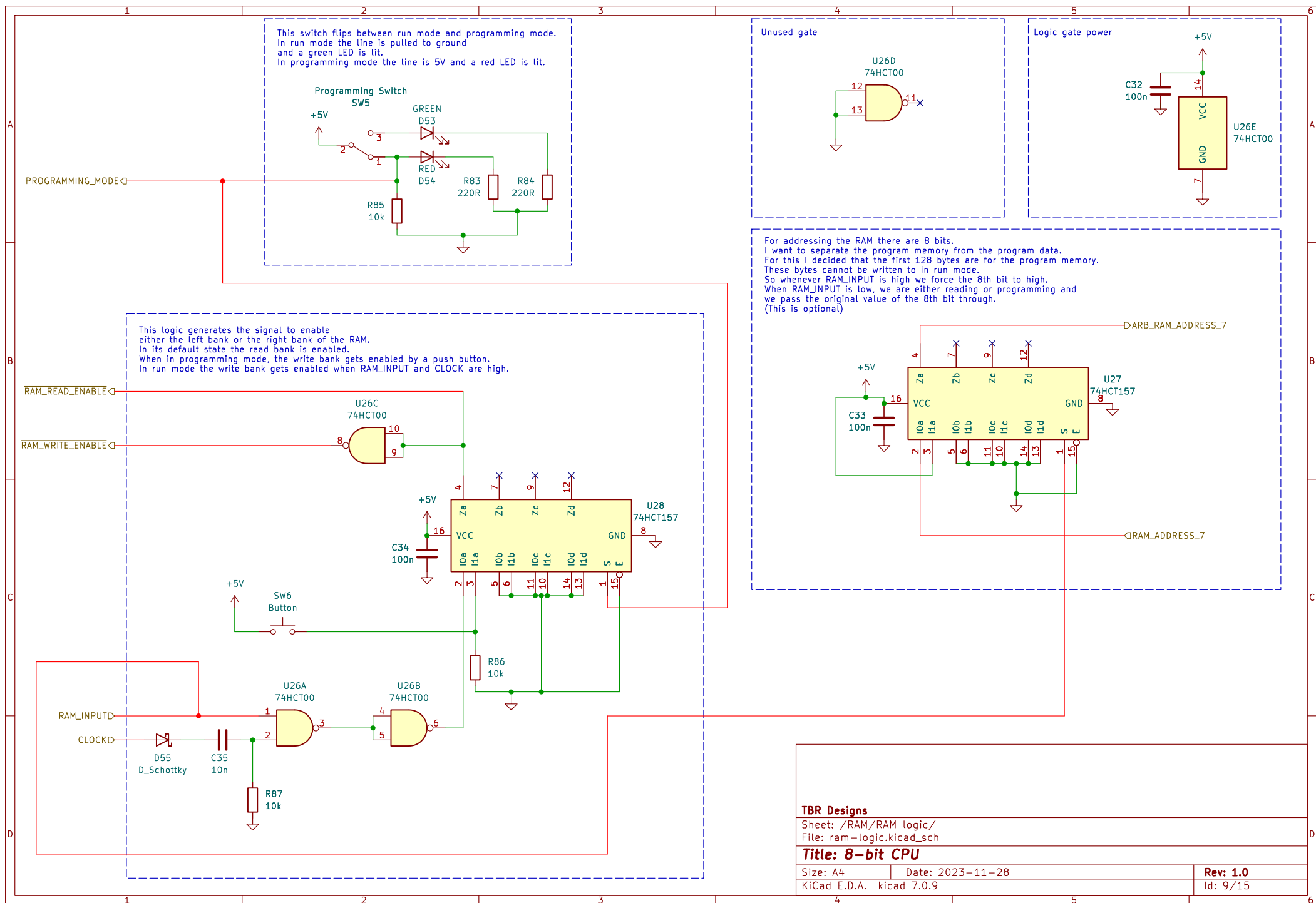
Date: 2023-11-28

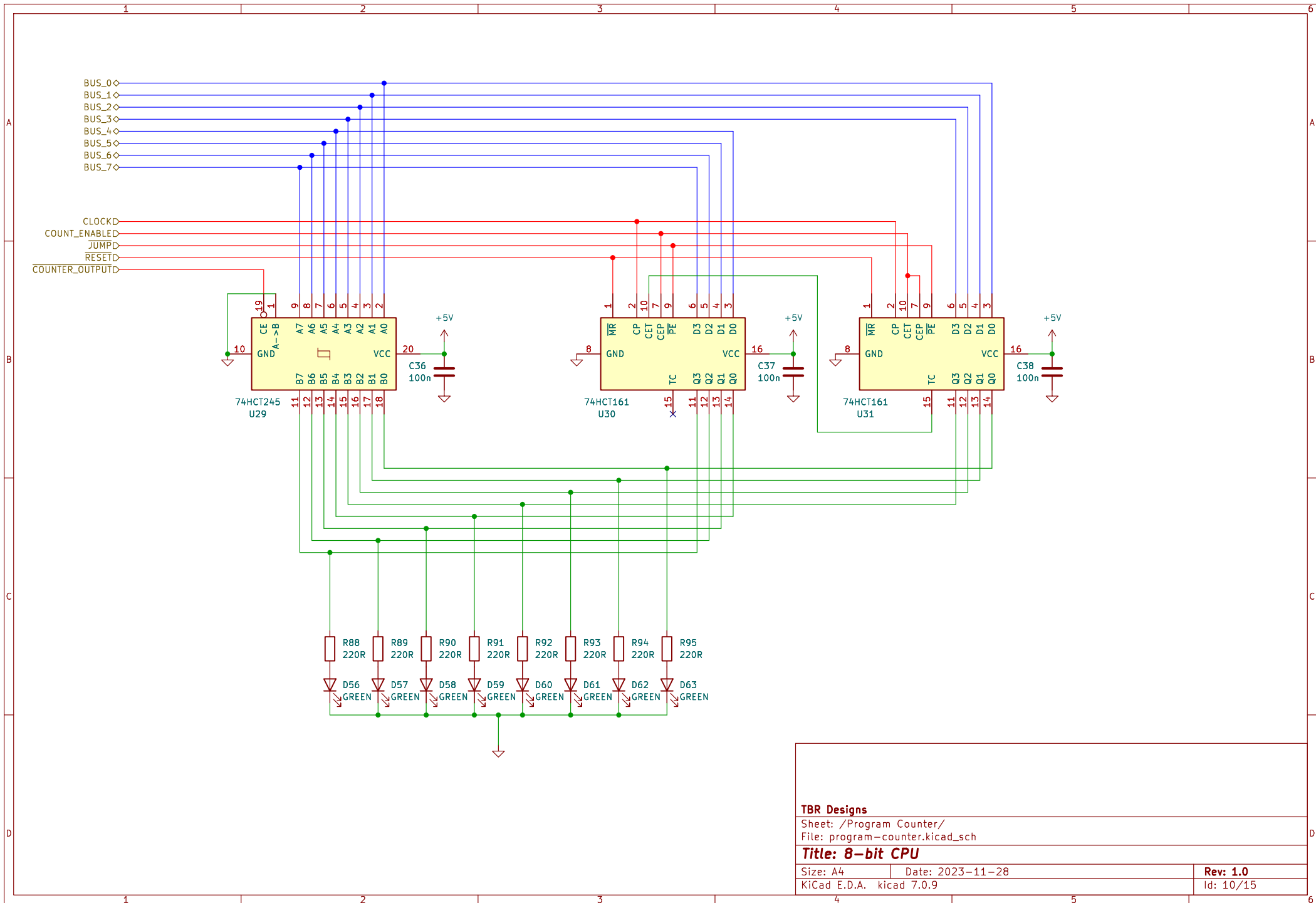
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Rev: 1.0

Id: 7/15







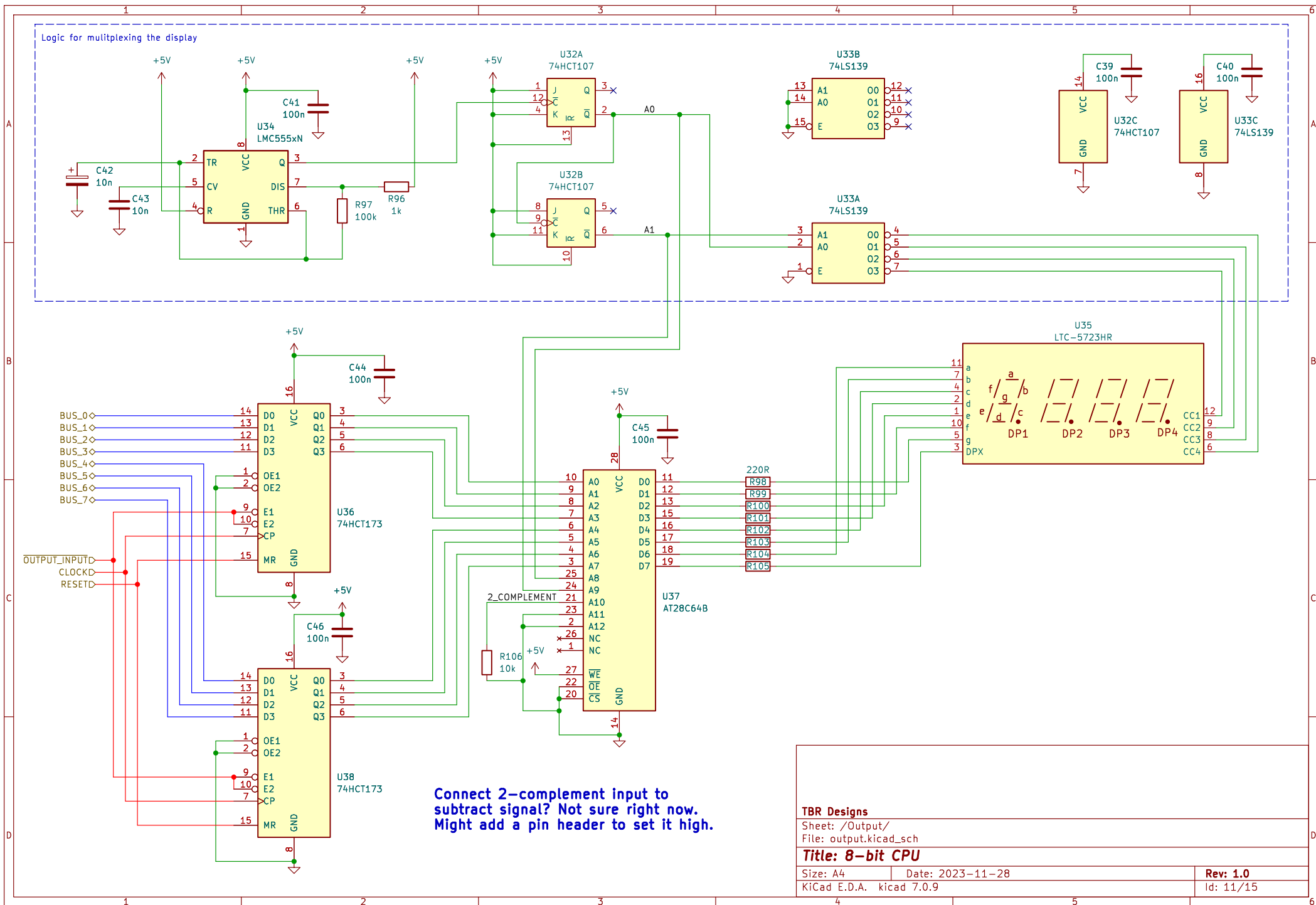
TBR Designs

Sheet: /Program Counter/
File: program-counter.kicad_sch

Title: 8-bit CPU

Size: A4 Date: 2023-11-28
KiCad E.D.A. kicad 7.0.9

Rev: 1.0
Id: 10/15



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Sheet: /Output/
File: output.kicad_sch

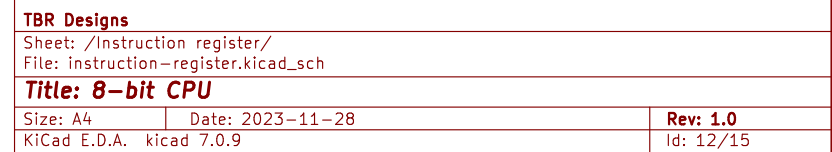
Title: 8-bit CPU

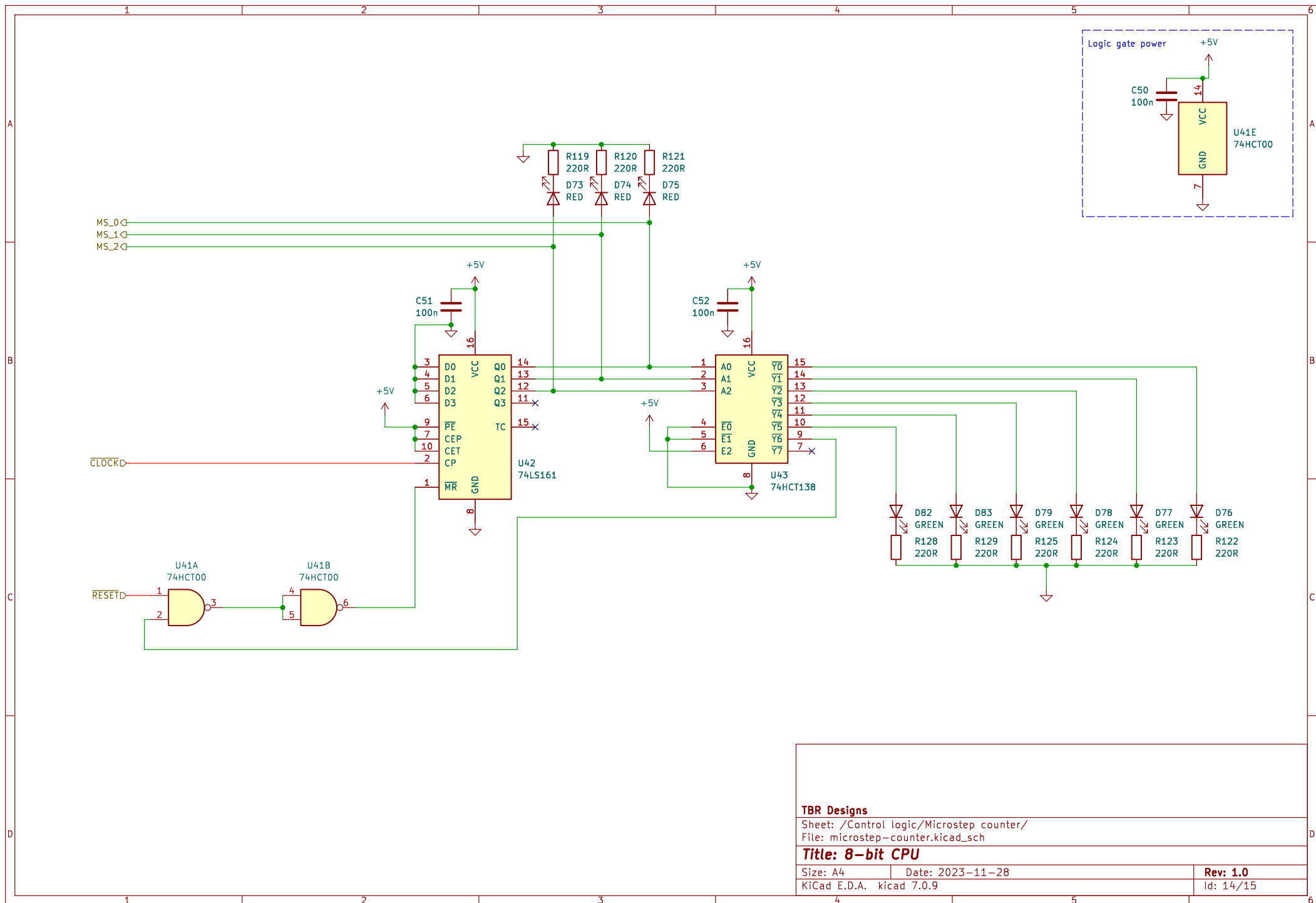
Size: A4 Date: 2023-11-28

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Rev: 1.0

Id: 11/15





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Sheet: /Control logic/Microstep counter/
File: microstep-counter.kicad_sch

Title: 8-bit CPU

Size: A4 Date: 2023-11-28

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Rev: 1.0

Id: 14/15

