## Micro code EEPROM layout

	Instruction code							Step			Flags		eft	Dii	rect			To inv	erter	s		Мі	ddle	Di	irect			To inv	erters	6		Ri	ght								
									CF	ZF			CE	RI	Ral	RO	II	J	СО	AI			s	HLT	AO	AIO	ВІ	во	OI	FI			INC	DCR	SUP	SDN	SA	so	ΧI	хо	
	<b>A8</b>	<b>A</b> 7	<b>A6</b>	A5 .	A4	А3	A2	<b>A1</b>	A0	A9	A10	A11	A12	D7	D6	<b>Y</b> 5	Y4	<b>Y</b> 3	Y2	Y1	Y0	A11	A12	D7	D6	<b>Y</b> 5	Y4	<b>Y</b> 3	Y2	Y1	Y0	A11	A12	D7	D6	<b>D</b> 5	D4	D3	Y2	D1	Y0
NOP Is also the	0	0	0	0	0	0	0	0	0	Х	Х	0	0			1				1		0	1									1	0								
start of each instruction							0	0	1	Х	Х	0	0	1			1	1				0	1									1	0								
LDA	0	0	0	0	0	1	0	1	0	Х	Х	0	0			1				1		0	1									1	0								
							0	1	1	Х	Х	0	0	1		1	1					0	1									1	0								
							1	0	0	Х	Х	0	0				1				1	0	1									1	0								
ADD	0	0	0	0	1	0	0	1	0	Х	Х	0	0			1				1		0	1									1	0								
Add what is							0	1	1	X	Х	0	0	1		1	1					0	1									1	0								
at memory address to A							1	0	0	Х	Х	0	0				1					0	1					1				1	0								
							1	0	1	Х	Х	0	0								1	0	1				1				1	1	0								
SUB	0	0	0	0	1	1	0	1	0	Х	Х	0	0			1				1		0	1									1	0								
Subtract what							0	1	1	Х	Х	0	0	1		1	1					0	1									1	0								
is at memory address from							1	0	0	Х	Х	0	0				1					0	1					1				1	0								
Α							1	0	1	Х	Х	0	0								1	0	1	1			1				1	1	0								
STA	0	0	0	1	0	0	0	1	0	Х	Х	0	0			1				1		0	1									1	0								
							0	1	1	Х	Х	0	0	1		1	1					0	1									1	0								
							1	0	0	Х	Х	0	0		1							0	1			1						1	0								
LDI Load direct	0	0	0	1	0	1	0	1	0	Х	Х	0	0			1				1		0	1									1	0								
into A							0	1	1	Х	Х	0	0	1			1				1	0	1									1	0								
JMP	0	0	0	1	1	0	0	1	0	Х	Х	0	0			1				1		0	1									1	0								
Jump to address							0	1	1	Х	Х	0	0				1		1			0	1									1	0								
JC	0	0	0	1	1	1	0	1	0	Х	Х	0	0			1				1		0	1									1	0								
Jump to address if							0	1	1	0	Х	0	0	1								0	1									1	0								
carry out							0	1	1	1	Х	0	0				1		1			0	1									1	0								
JZ Jump to	0	0	1	0	0	0	0	1	0	Х	Х	0	0			1				1		0	1									1	0								
address if							0	1	1	Х	0	0	0	1								0	1									1	0								
zero							0	1	1	Х	1	0	0				1		1			0	1									1	0								
LDB	0	0	1	0	0	1	0	1	0	Х	Х	0	0			1				1		0	1									1	0								
							0	1	1	Х	Х	0	0	1		1	1					0	1									1	0								
							1	0	0	Х	Х	0	0				1					0	1					1				1	0								
STB	0	0	1	0	1	0	0	1	0	Х	Х	0	0			1				1		0	1									1	0								
							0	1	1	Х	Х	0	0	1		1	1					0	1									1	0								

		Instruction code						Step	)	FI	ags	.	Left		Direct			To inv	erter	s		Мі	ddle	Di	rect			To inv	erter	s		R	ight								
										CF	ZF	İ		CE	RI	Ral	RO	II	J	СО	AI	İ		s	HLT	AO	AIO	ВІ	во	OI	FI	ĺ		INC	DCR	SUP	SDN	SA	so	ΧI	хо
	<b>A8</b>	A7	A6	<b>A</b> 5	A4	АЗ	A2	A1	A0	A9	A10	A11	A12	D7	D6	<b>Y</b> 5	Y4	<b>Y</b> 3	Y2	Y1	Y0	A11	A12	D7	D6	<b>Y</b> 5	Y4	<b>Y</b> 3	Y2	Y1	Y0	A11	A12	D7	D6	D5	D4	D3	Y2	D1	Y0
							1	0	0	Х	Х	0	0		1							0	1						1			1	0								
LDIB	0	0	1	0	1	1	0	1	0	Х	Х	0	0			1				1		0	1									1	0								
Load direct into B							0	1	1	Х	Х	0	0	1			1					0	1					1				1	0								
OUT	0	0	1	1	0	0	0	1	0	Х	Х	0	0									0	1			1				1		1	0								
OUTB	0	0	1	1	0	1	0	1	0	Х	Х	0	0									0	1						1	1		1	0								
OUTX	0	0	1	1	1	0	0	1	0	Х	Х	0	0									0	1							1		1	0								1
HLT	0	0	1	1	1	1	0	1	0	Х	Х	0	0									0	1		1							1	0								
LDX	0	1	0	0	0	0	0	1	0	Х	Х	0	0			1				1		0	1									1	0								
							0	1	1	Х	Х	0	0	1		1	1					0	1									1	0								
							1	0	0	Х	Х	0	0				1					0	1									1	0							1	
STX	0	1	0	0	0	1	0	1	0	Х	Х	0	0			1				1		0	1									1	0								
							0	1	1	Х	X	0	0	1		1	1					0	1									1	0								
							1	0	0	Х	Х	0	0		1							0	1									1	0								1
LDIX	0	1	0	0	1	0	0	1	0	Х	Х	0	0			1				1		0	1									1	0								
							0	1	1	Х	Х	0	0	1			1					0	1									1	0							1	
PSH	0	1	0	0	1	1	0	1	0	Х	X	0	0			1						0	1									1	0					1	1		
							0	1	1	Х	Х	0	0		1							0	1			1						1	0			1		1			
POP	0	1	0	1	0	0	0	1	0	Х	X	0	0									0	1									1	0				1				
							0	1	1	Х	Х	0	0			1						0	1									1	0					1	1		
							1	<u> </u>	0	Х	Х	0	0				1				1	0	1									1	0					1			
PSHB	0	1	0	1	0	1	0		0	Х	Х	0	0			1						0	1									1	0					1	1		
							0	1	1	X	X	0	0		1							0	1						1			1	0			1		1			
POPB	0	1	0	1	1	0	0	1	0	X	X	0	0									0	1									1	0				1				
							0	1	1	X	X	0	0			1						0	1									1	0					1	1		
IOD	_	_	0	_		_	1	0	0	X	X	0	0			_	1					0	1					1				1	0					1	_		
JSR	U	'	U	'	1	'	0	1	0	X	X	0	0		1	1						0	1									1	0			4		1	1		
							0	0	0	X	X	0	0		!	1				1		0	1									1	0			1		'			
							1	0	1	X	X	0	0			'	1		1	'		0	1									1	0								
RTS	0	1	1	n	0	n	0	1	0	X	X	0	0									0	1									1	0				1				
1110				U	U	U	0	1	1	X	X	0	0			1						0	1									1	0					1	1		
							1	0	0	X	X	0	0			'	1		1			0	1									1	0					1	'		
							1		1	X	X	0	0	1								0	1									1	0								
								U		_ ^		L o	U									U											U								

	Instruction code					nstruction code			n code			)	Fla	ags	Left		Direct				To in	verter	s		Mi	Middle		Direct			To inv	erters	3		Ri	ight								
										CF	ZF			CE	RI	Ral	RO	II	J	СО	AI	1		s	HLT	AO	AIO	ВІ	во	OI	FI			INC	DCR	SUP	SDN	SA	so	ΧI	хо			
	<b>A8</b>	Α7	<b>A6</b>	<b>A5</b>	<b>A4</b>	А3	A2	A1	A0	A9	A10	A11	A12	D7	D6	<b>Y</b> 5	Y4	<b>Y</b> 3	Y2	Y1	Y0	A11	A12	D7	D6	<b>Y</b> 5	Y4	<b>Y</b> 3	Y2	Y1	Y0	A11	A12	D7	D6	D5	D4	D3	Y2	D1	Y0			
INCX	0	1	1	0	0	1	0	1	0	Х	Х	0	0									0	1									1	0	1										
							0	1	1	х	Х	0	0									0	1								1	1	0											
DECX	0	1	1	0	1	0	0	1	0	Х	Х	0	0									0	1									1	0		1									
							0	1	1	Х	Х	0	0									0	1								1	1	0											
										Х	Х	0	0									0	1									1	0											
										х	Х	0	0									0	1									1	0											
										Х	Х	0	0									0	1									1	0											