Micro code EEPROM layout

	Instruction code							Step	Step Flags		Flags Left		eft	Dii	Direct			To inv	erter	s		Мі	ddle	Di	rect			To inv	erters	6		Ri	ght			Direct			То	invert	ers
									CF	ZF			CE	RI	Ral	RO	II	J	СО	AI			s	HLT	AO	AIO	ВІ	во	OI	FI			INC	DCR	SUP	SDN	SA	so	ΧI	хо	
	A8	A7	A6	A 5	A4	А3	A2	A1	A0	A9	A10	A11	A12	D7	D6	Y 5	Y4	Y 3	Y2	Y1	Y0	A11	A12	D7	D6	Y 5	Y4	Y 3	Y2	Y1	Y0	A11	A12	D7	D6	Y 5	Y 4	Y 3	Y2	Y1	Y0
NOP Is also the	0	0	0	0	0	0	0	0	0	Х	Х	0	0			1				1		0	1									1	0								
start of each instruction							0	0	1	Х	Х	0	0	1			1	1				0	1									1	0								
LDA	0	0	0	0	0	1	0	1	0	х	Х	0	0			1				1		0	1									1	0								
							0	1	1	X	Х	0	0	1		1	1					0	1									1	0								
							1	0	0	Х	Х	0	0				1				1	0	1									1	0								
ADD	0	0	0	0	1	0	0	1	0	Х	Х	0	0			1				1		0	1									1	0								
Add what is							0	1	1	X	Х	0	0	1		1	1					0	1									1	0								
at memory address to A							1	0	0	х	Х	0	0				1					0	1					1				1	0								
							1	0	1	Х	Х	0	0								1	0	1				1				1	1	0								
SUB	0	0	0	0	1	1	0	1	0	Х	Х	0	0			1				1		0	1									1	0								
Subtract what							0	1	1	Х	Х	0	0	1		1	1					0	1									1	0								
is at memory address from							1	0	0	Х	Х	0	0				1					0	1					1				1	0								
Α							1	0	1	Х	Х	0	0								1	0	1	1			1				1	1	0								
STA	0	0	0	1	0	0	0	1	0	Х	Х	0	0			1				1		0	1									1	0								
							0	1	1	Х	X	0	0	1		1	1					0	1									1	0								
							1	0	0	Х	Х	0	0		1							0	1			1						1	0								
LDI Load direct	0	0	0	1	0	1	0	1	0	Х	Х	0	0			1				1		0	1									1	0								
into A							0	1	1	Х	Х	0	0	1			1				1	0	1									1	0								
JMP	0	0	0	1	1	0	0	1	0	х	Х	0	0			1				1		0	1									1	0								
Jump to address							0	1	1	x	Х	0	0				1		1			0	1									1	0								
JC	0	0	0	1	1	1	0	1	0	Х	Х	0	0			1				1		0	1									1	0								
Jump to address if							0	1	1	0	Х	0	0	1								0	1									1	0								
carry out							0	1	1	1	Х	0	0				1		1			0	1									1	0								
JZ	0	0	1	0	0	0	0	1	0	Х	Х	0	0			1				1		0	1									1	0								
Jump to address if							0	1	1	х	0	0	0	1								0	1									1	0								
zero							0	1	1	Х	1	0	0				1		1			0	1									1	0								
LDB	0	0	1	0	0	1	0	1	0	х	Х	0	0			1				1		0	1									1	0								
							0	1	1	х	Х	0	0	1		1	1					0	1									1	0								
							1	0	0	х	Х	0	0				1					0	1					1				1	0								
STB	0	0	1	0	1	0	0	1	0	х	Х	0	0			1				1		0	1									1	0								
							0	1	1	Х	X	0	0	1		1	1					0	1									1	0								

	1	Instruction code						Step)	FI	ags		_eft	Dii	ect			To in	verter	rs		м	iddle	Di	irect			To inv	erter	s		B	light			Direct			То	inverte	ers
										CF	ZF	1		CE	RI	Ral	RO	II	J	СО	Al	ĺ		s	HLT	AO	AIO	ВІ	во	OI	FI	1		INC	DCR	SUP	SDN	SA	so	ΧI	хо
	A8	A7	A6	A5	A4	АЗ	A2	A1	A0	A9	A10	A11	A12	D7	D6	Y 5	Y4	Y 3	Y2	Y1	Y0	A11	A12	D7	D6	Y 5	Y 4	Y 3	Y2	Y1	Y0	A11	A12	D7	D6	Y 5	Y4	Y 3	Y2	Y1	Y0
							1	0	0	Х	Х	0	0		1							0	1						1			1	0								
LDIB	0	0	1	0	1	1	0	1	0	Х	Х	0	0			1				1		0	1									1	0								
Load direct into B							0	1	1	Х	Х	0	0	1			1					0	1					1				1	0								
OUT	0	0	1	1	0	0	0	1	0	Х	Х	0	0									0	1			1				1		1	0								
OUTB	0	0	1	1	0	1	0	1	0	Х	Х	0	0									0	1						1	1		1	0								
OUTX	0	0	1	1	1	0	0	1	0	Х	Х	0	0									0	1							1		1	0								1
HLT	0	0	1	1	1	1	0	1	0	Х	Х	0	0									0	1		1							1	0								
LDX	0	1	0	0	0	0	0	1	0	Х	Х	0	0			1				1		0	1									1	0								
							0	1	1	Х	Х	0	0	1		1	1					0	1									1	0								
							1	0	0	Х	Х	0	0				1					0	1									1	0							1	
STX	0	1	0	0	0	1	0	1	0	Х	Х	0	0			1				1		0	1									1	0								
						ļ	0	1	1	Х	X	0	0	1		1	1					0	1									1	0								
							1	0	0	Х	Х	0	0		1							0	1									1	0								1
LDIX	0	1	0	0	1	0	0	1	0	Х	Х	0	0			1				1		0	1									1	0								
							0	1	1	Х	Х	0	0	1			1					0	1									1	0							1	
PSH	0	1	0	0	1	1	0	1	0	Х	Х	0	0			1						0	1									1	0					1	1		
							0	1	1	Х	Х	0	0		1							0	1			1						1	0			1		1			
POP	0	1	0	1	0	0	0	1	0	Х	Х	0	0									0	1									1	0				1				
						ļ	0	1	1	Х	Х	0	0			1						0	1									1	0					1	1		
							1	<u> </u>	0	Х	Х	0	0				1				1	0	1									1	0					1			
PSHB	0	1	0	1	0	1	0		0	Х	Х	0	0			1						0	1									1	0					1	1		
							0	1	1	X	X	0	0		1							0	1						1			1	0			1		1			
POPB	0	1	0	1	1	0	0	1	0	X	X	0	0									0	1									1	0				1				
						ł	0	1	1	X	X	0	0			1						0	1									1	0					1	1		
100						_	1	0	0	X	X	0	0				1					0	1					1				1	0					1			
JSR	0	1	U	1	1	1	0	1	0	X	X	0	0			1						0	1									1	0			_		1	1		
						ł	0	1	1	X	X	0	0		1	1				1		0	1									1	0			1		1			
							1	0	0	X	X	0	0			1	1		1	1		0	1									1	0								
RTS	0	1	1	0	0	0	0	1	0	X	X	0	0						+			0	1									1	0				1				
1110				U	U	0	0	1	1	X	X	0	0			1						0	1									1	0					1	1		
							1	0	0	X	X	0	0			'	1		1			0	1									1	0					1			
							1		1	X	X	0	0	1								0	1									1	0								
								U		_ ^		J	U									Ů											J								

	Instruction code					nstruction code			•	Flags		Left		Direct				To in	verter	s		Mi	Middle		Direct			To inv	erters	,		Ri	ght						То	To inverters		
									CF	CF ZF			CE	RI	Ral	RO	II	J	СО	AI	1		s	HLT	AO	AIO	ВІ	во	OI	FI			INC	DCR	SUP	SDN	SA	so	ΧI	хо		
	A 8	A7	A6	A5	A4	А3	A2	A1	A0	А9	A10	A11	A12	D7	D6	Y 5	Y4	Y 3	Y2	Y1	Y0	A11	A12	D7	D6	Y 5	Y4	Y 3	Y2	Y 1	Y0	A11	A12	D7	D6	Y5	Y4	Y 3	Y2	Y1	Y0	
INCX	0	1	1	0	0	1	0	1	0	Х	Х	0	0									0	1									1	0	1								
							0	1	1	х	Х	0	0									0	1								1	1	0									
DECX	0	1	1	0	1	0	0	1	0	Х	Х	0	0									0	1									1	0		1							
							0	1	1	Х	Х	0	0									0	1								1	1	0									
										Х	Х	0	0									0	1									1	0									
										Х	Х	0	0									0	1									1	0									
										Х	Х	0	0									0	1									1	0									