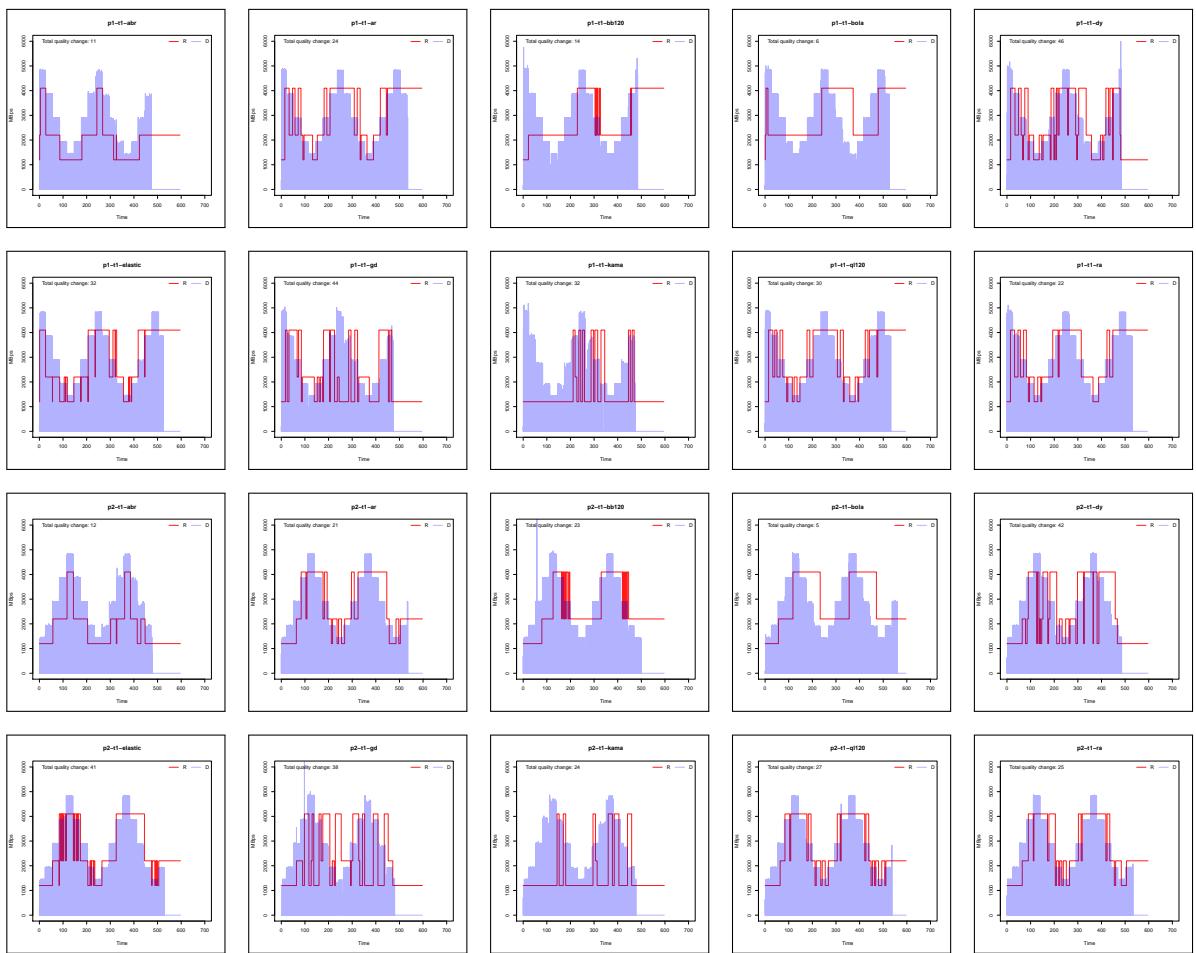


Results

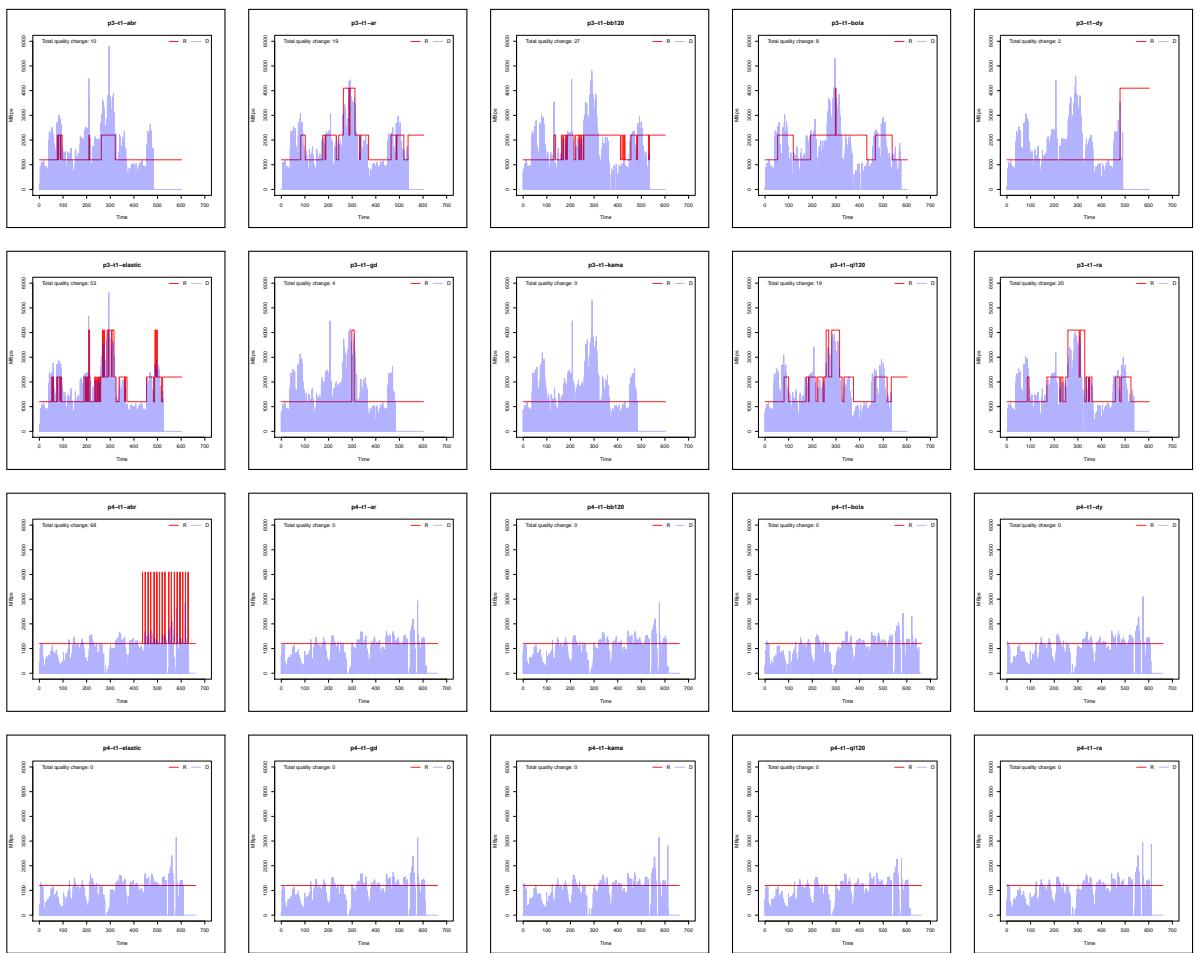
January 21, 2017

Buffer : 120

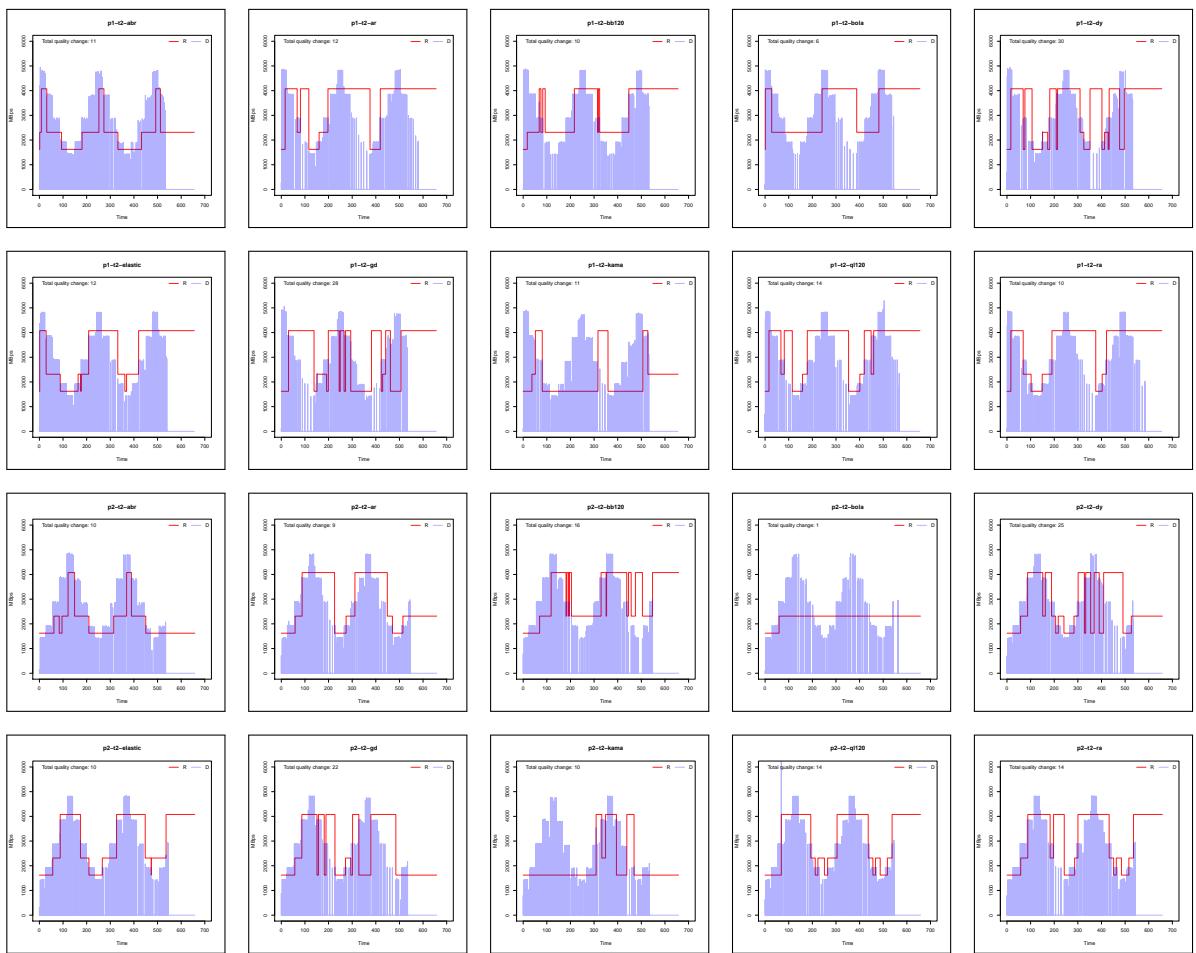
1 T1, Throughput



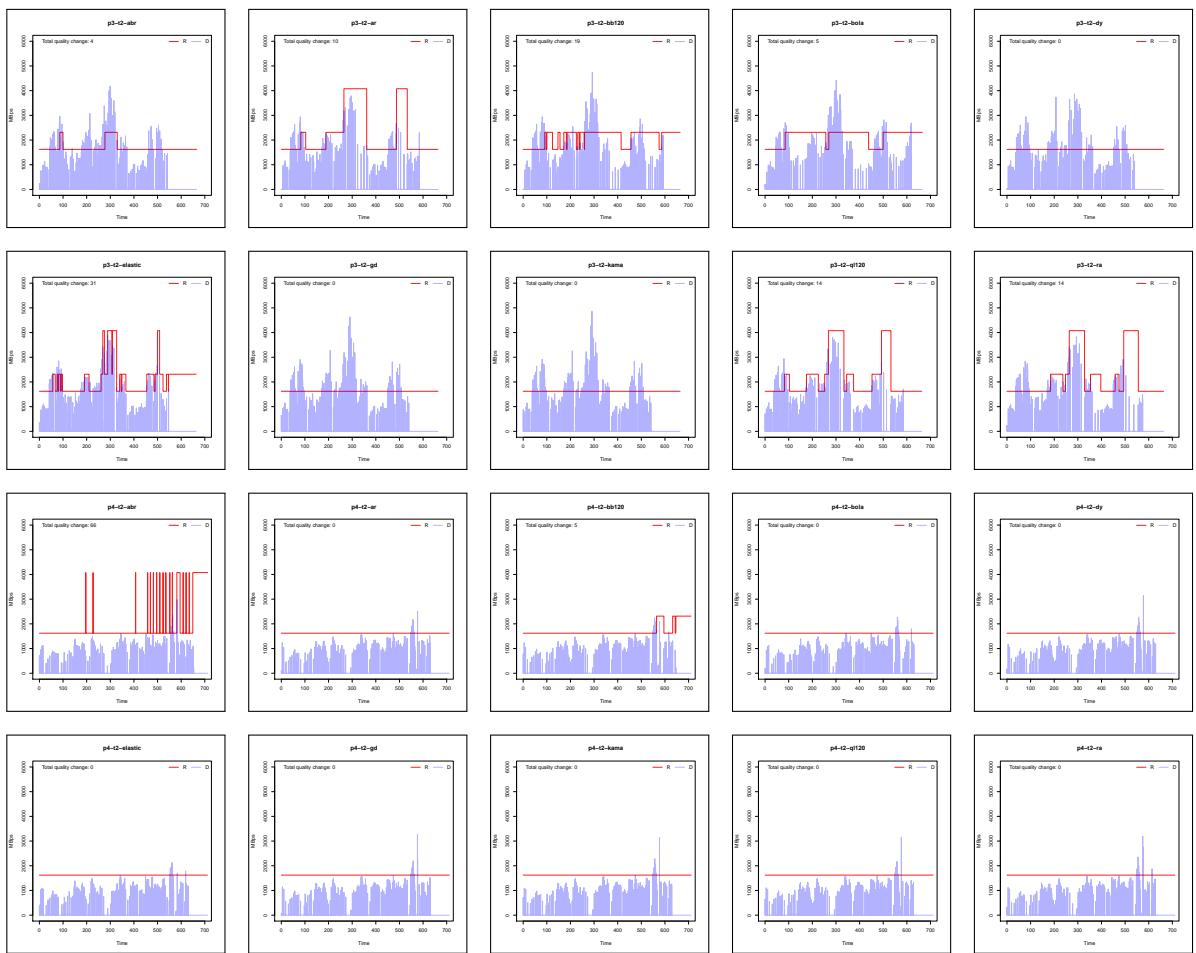
2 T1, Throughput



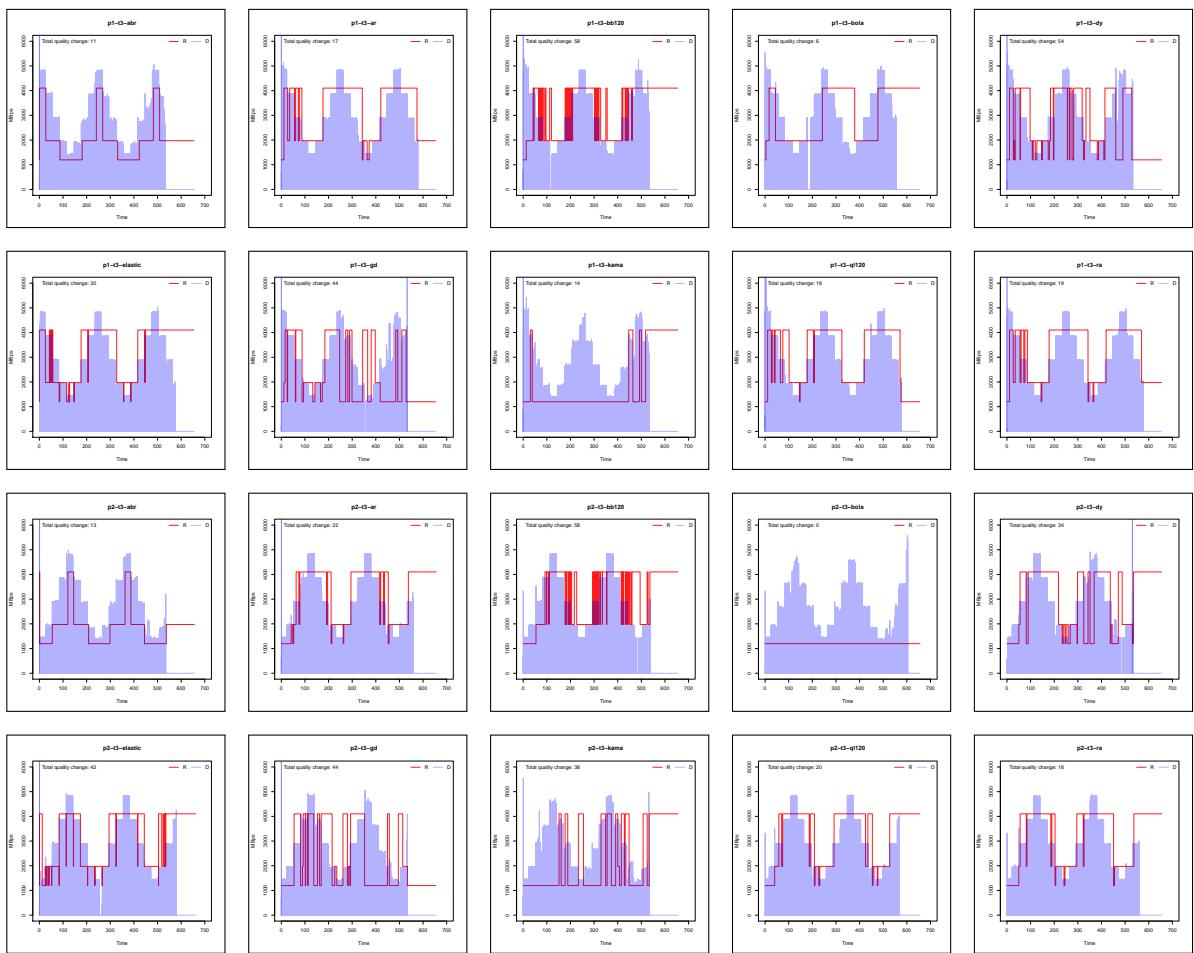
3 T2, Throughput



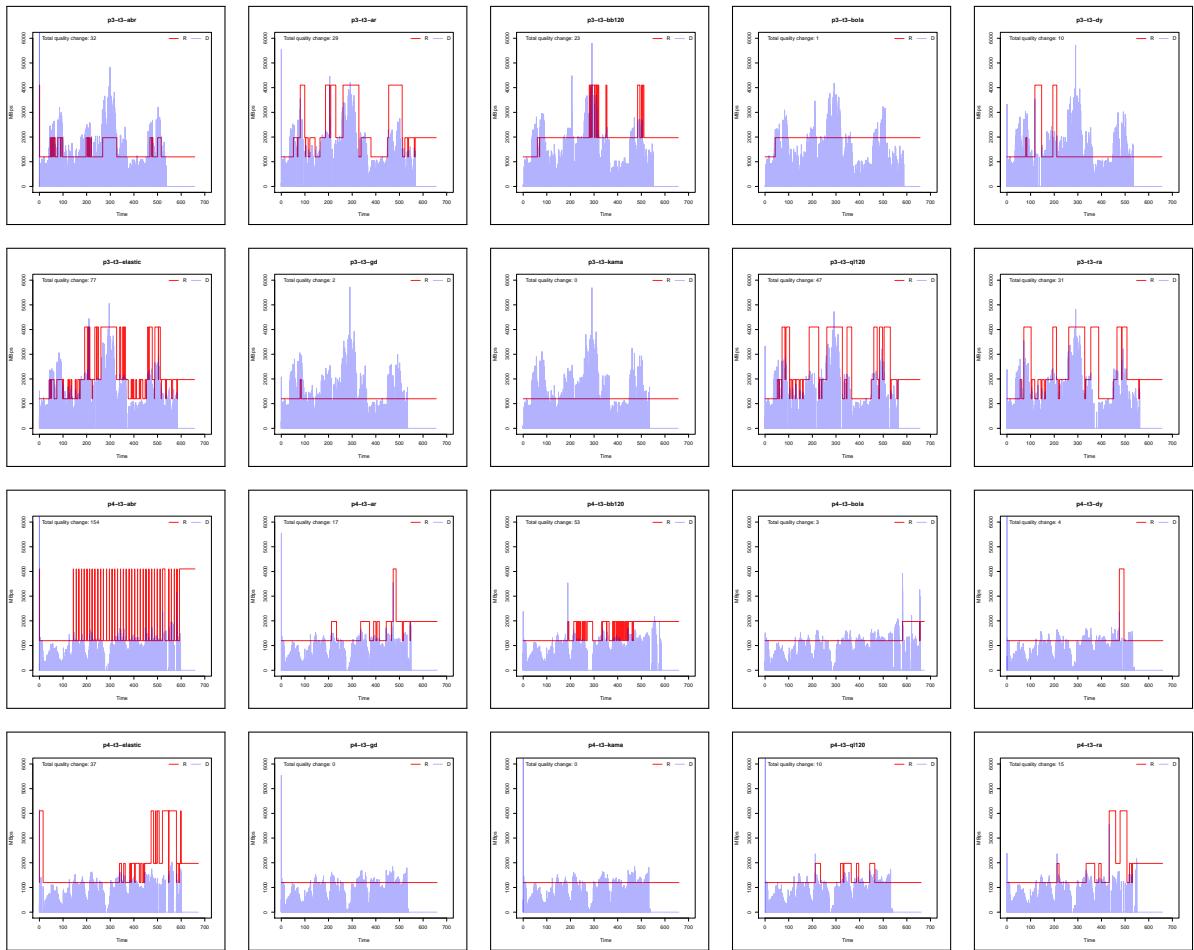
4 T2, Throughput



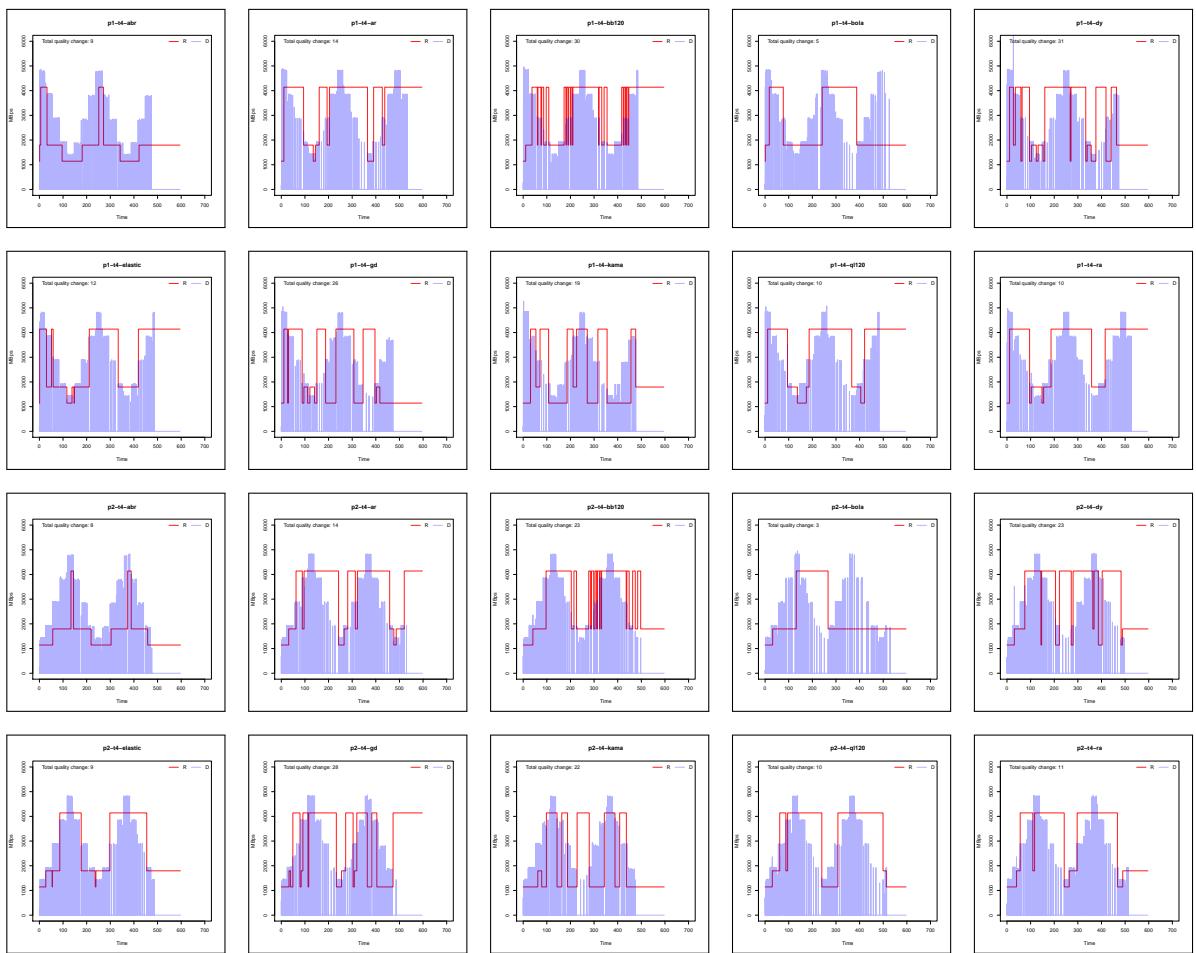
5 T3, Throughput



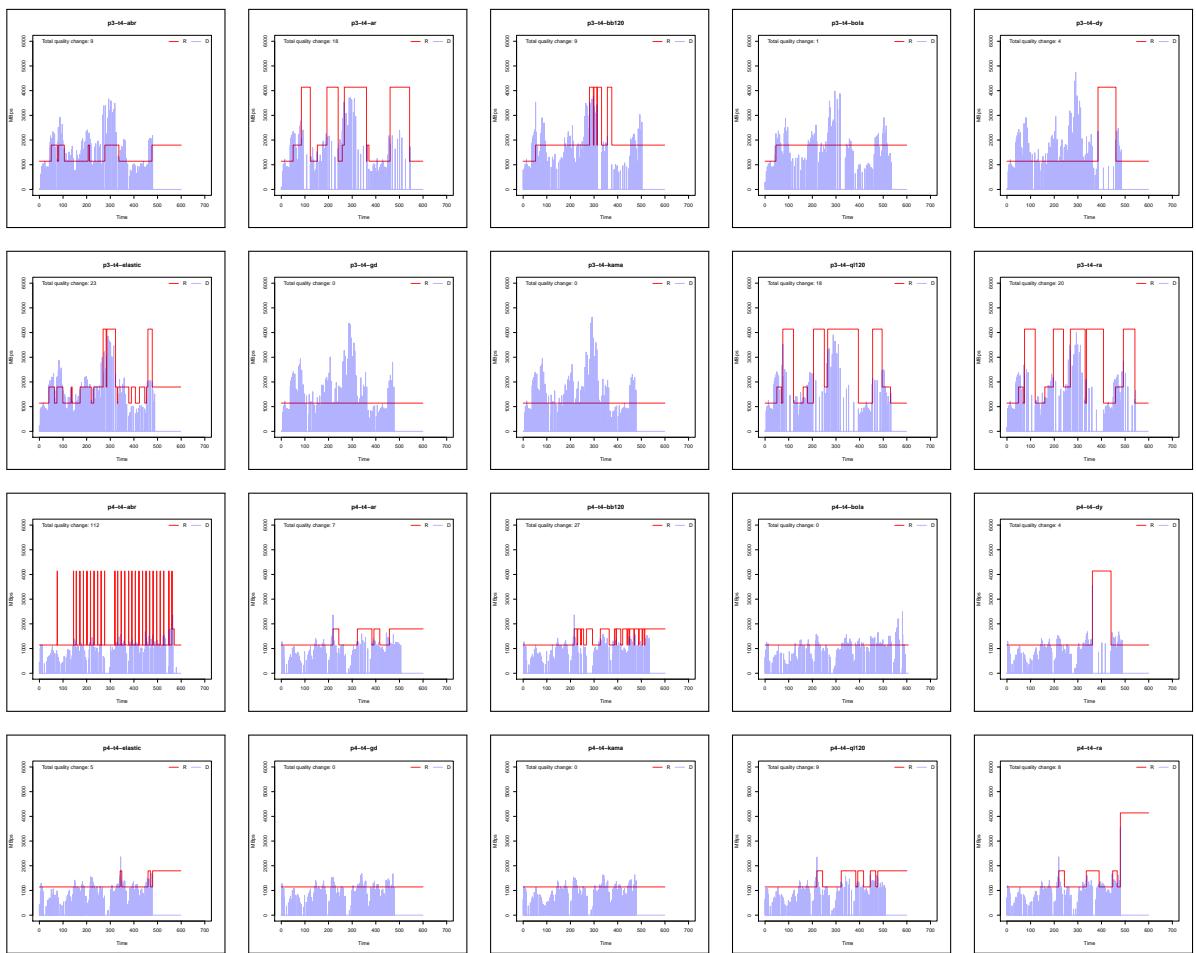
6 T3, Throughput



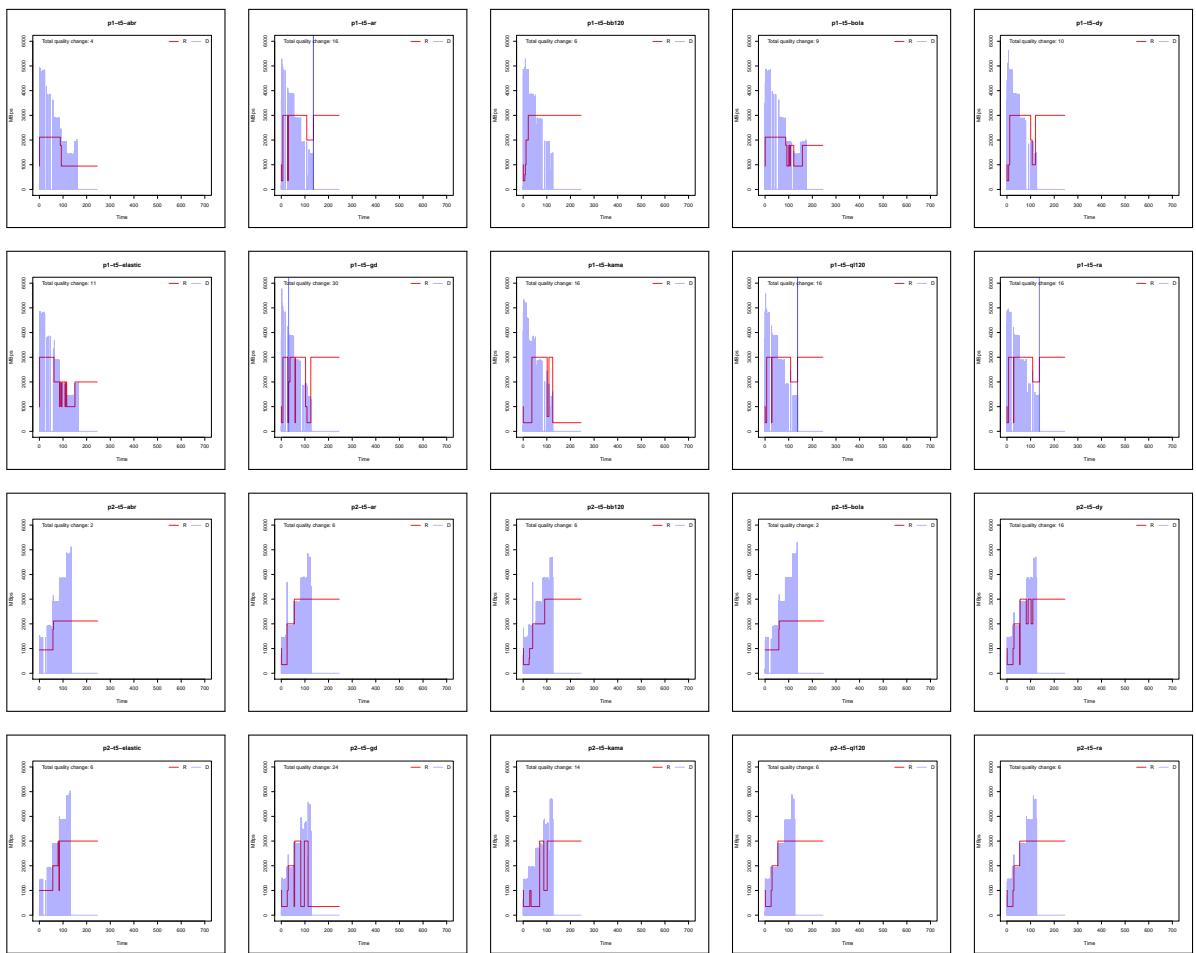
7 T4, Throughput



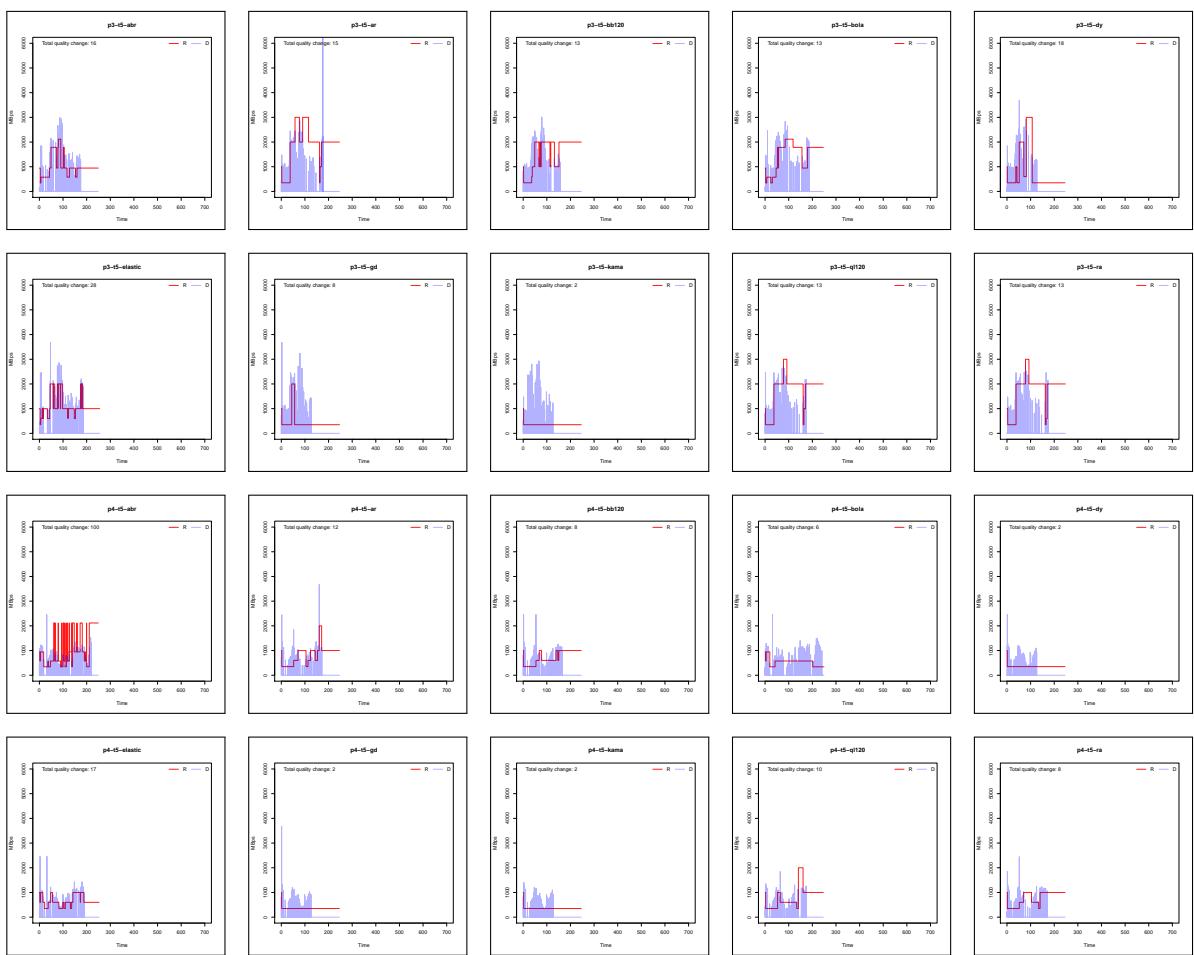
8 T4, Throughput



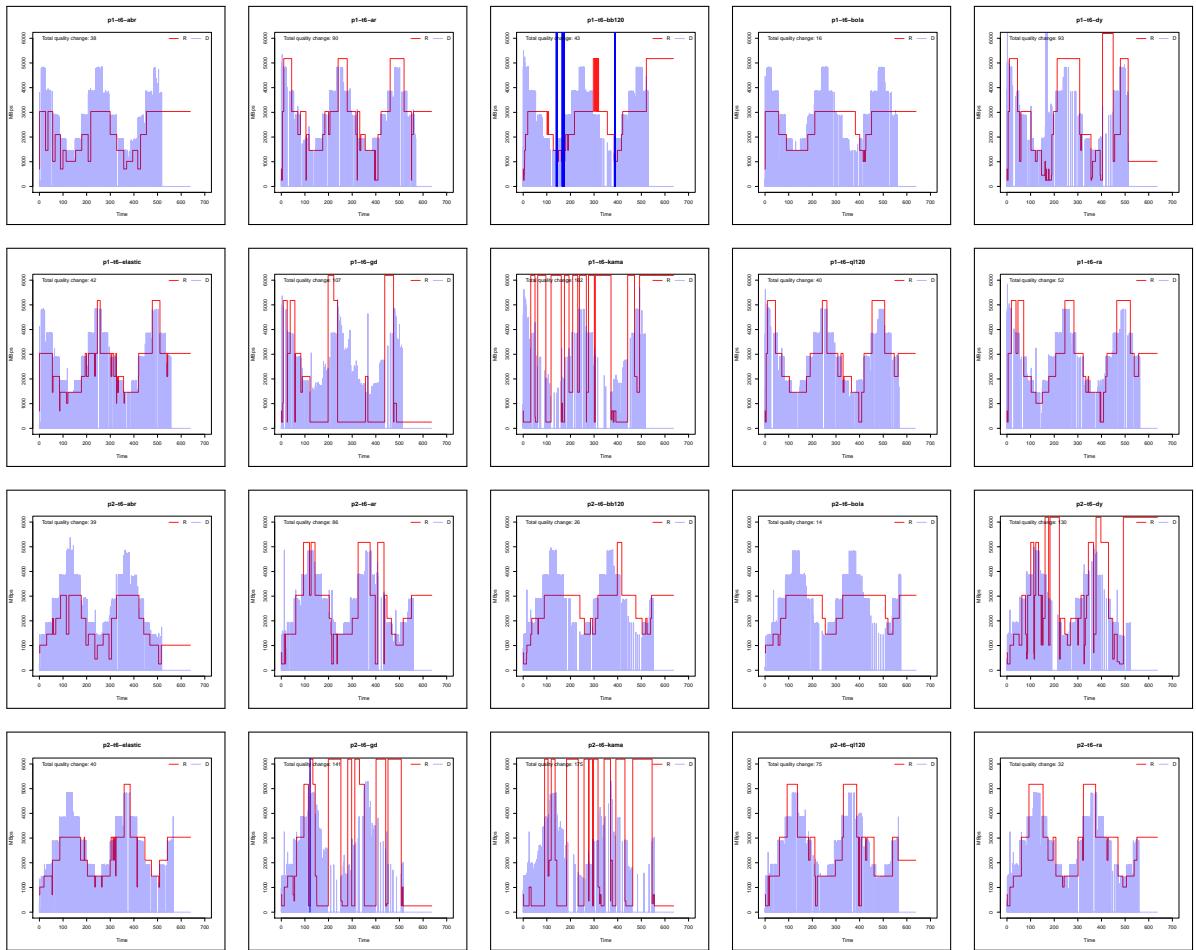
9 T5, Throughput



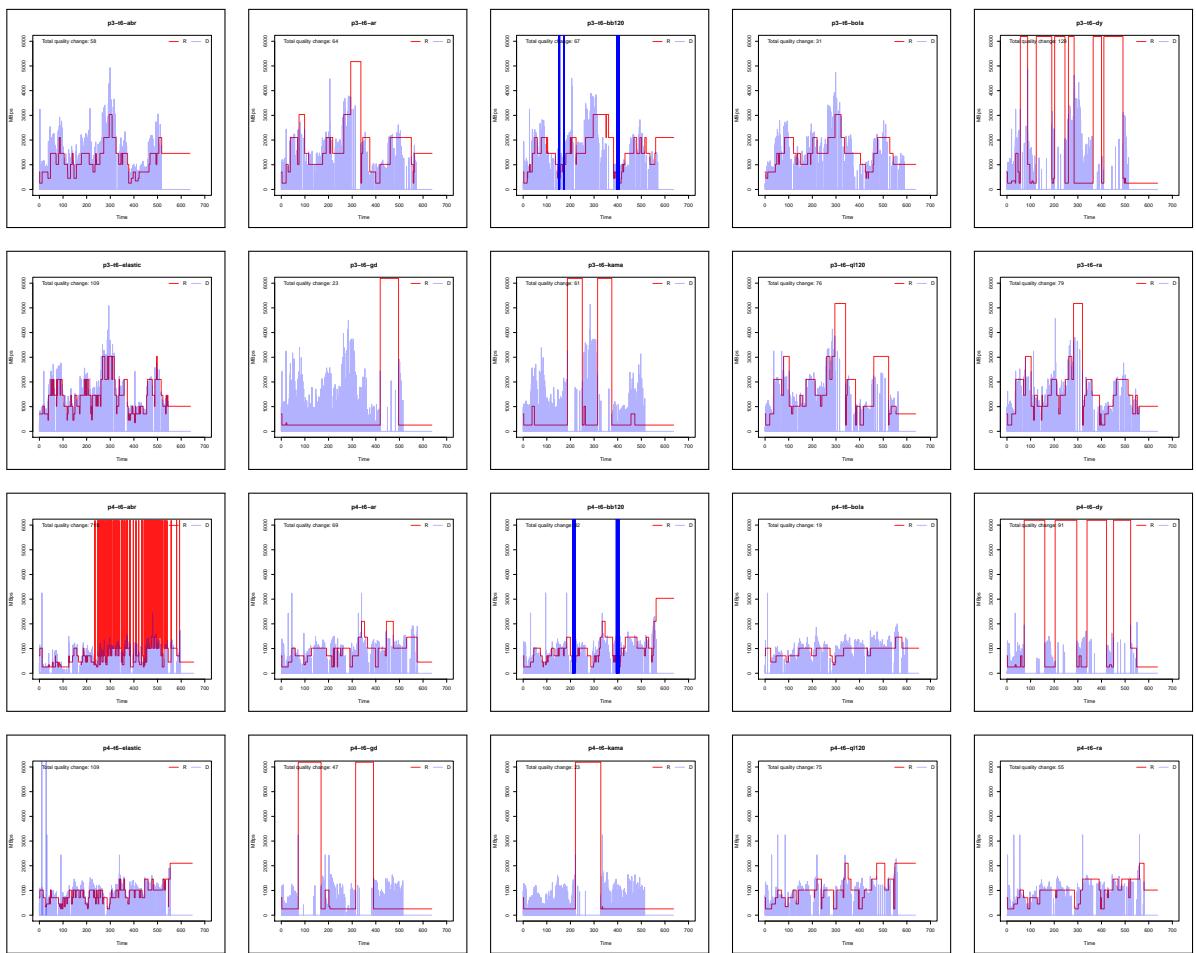
10 T5, Throughput



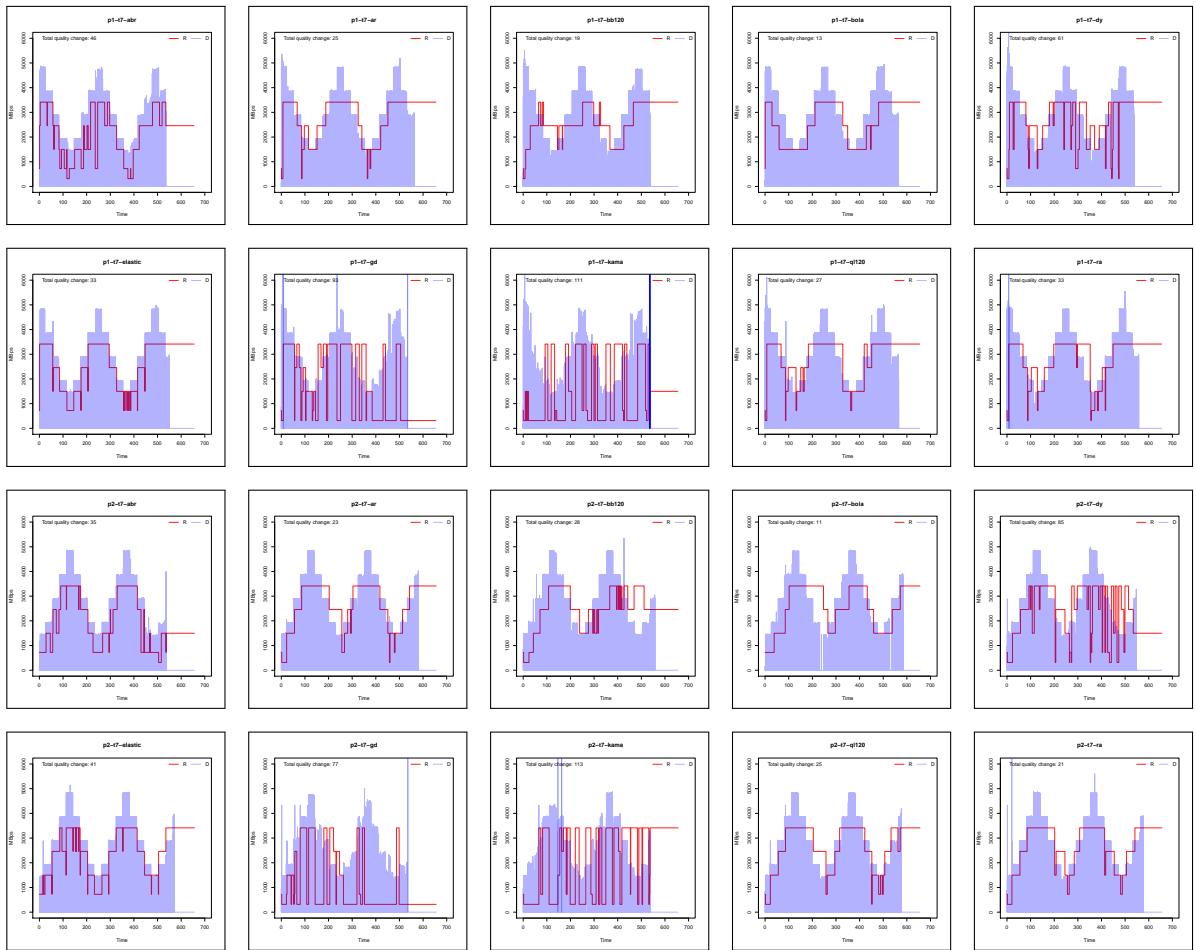
11 T6, Throughput



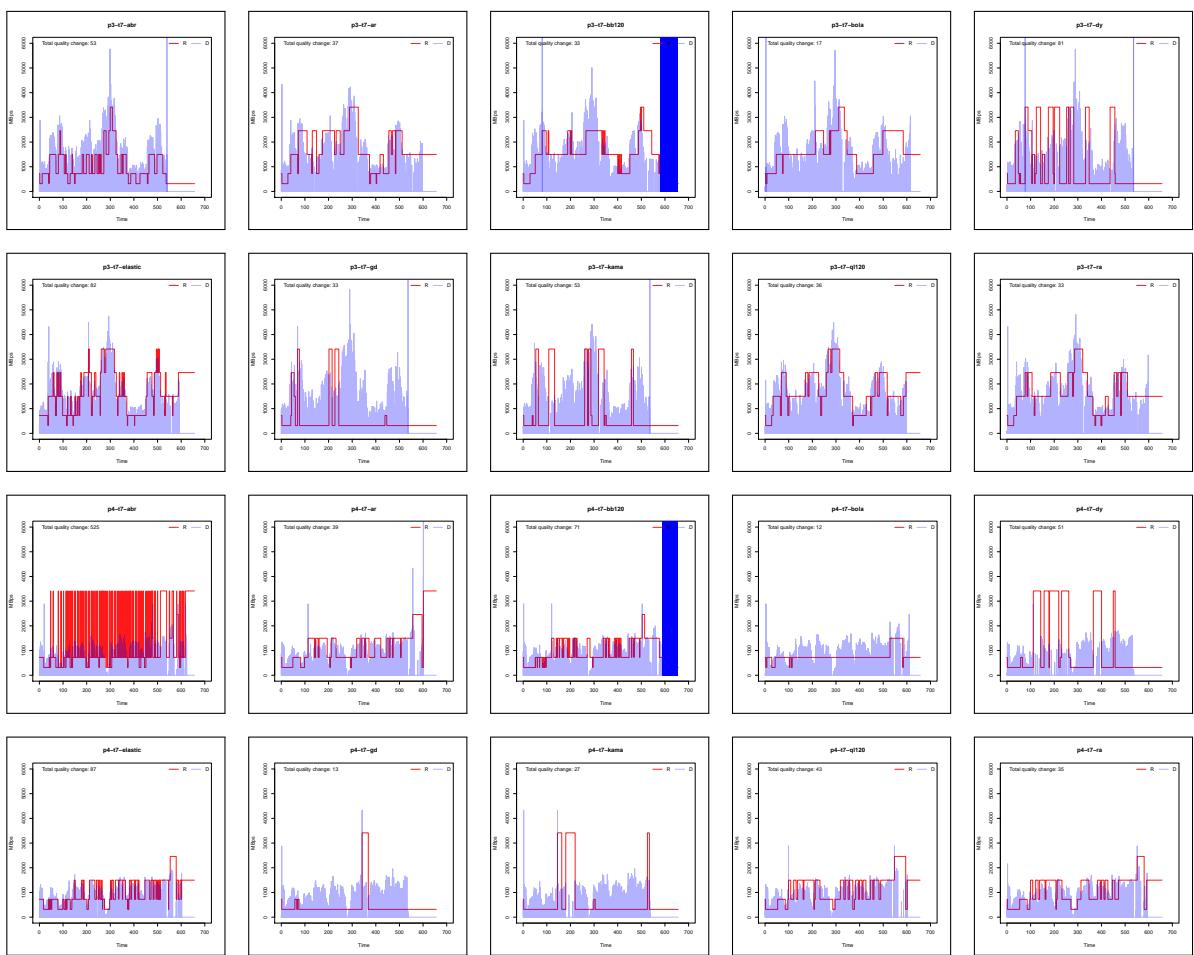
12 T6, Throughput



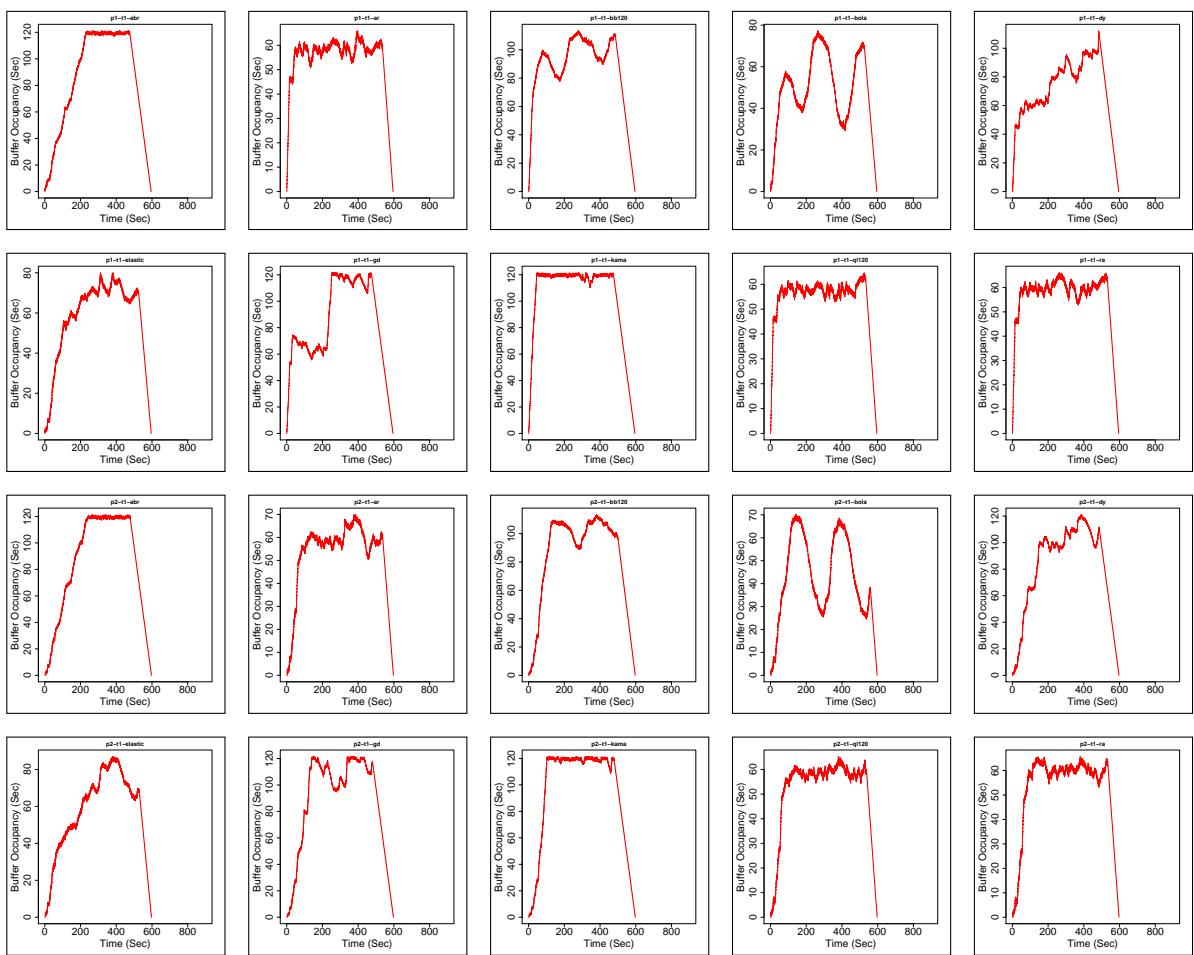
13 T7, Throughput



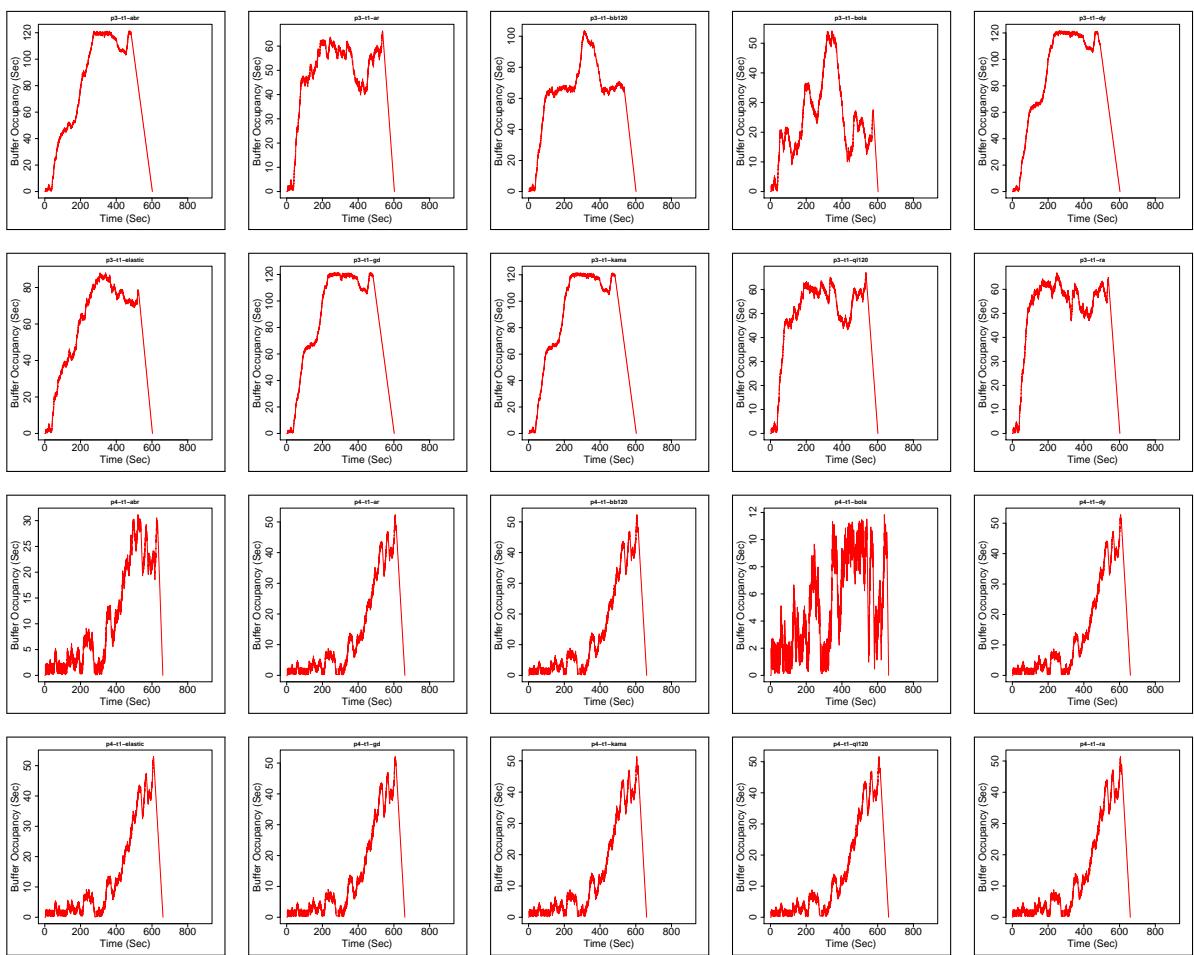
14 T7, Throughput



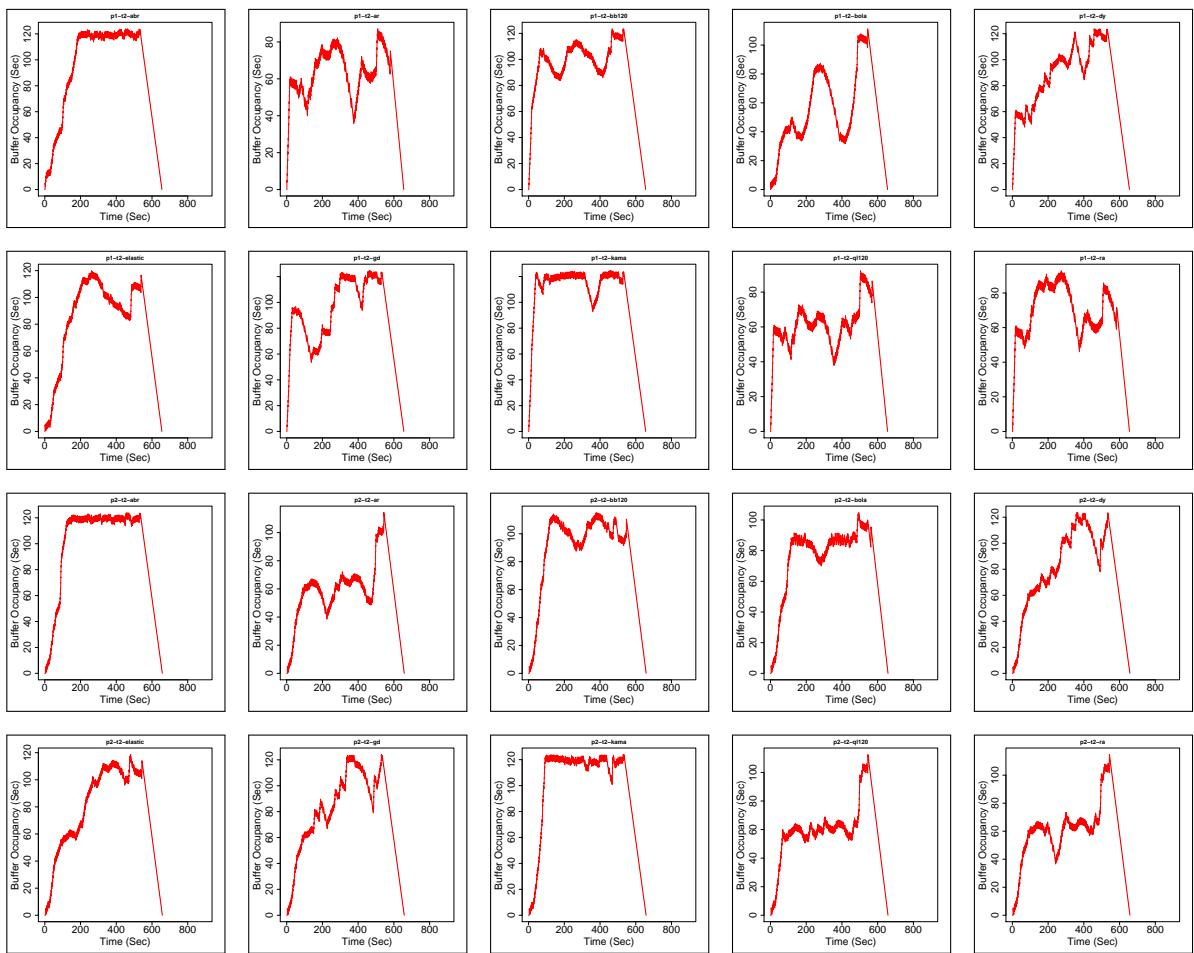
15 T1, Buffer Occupancy



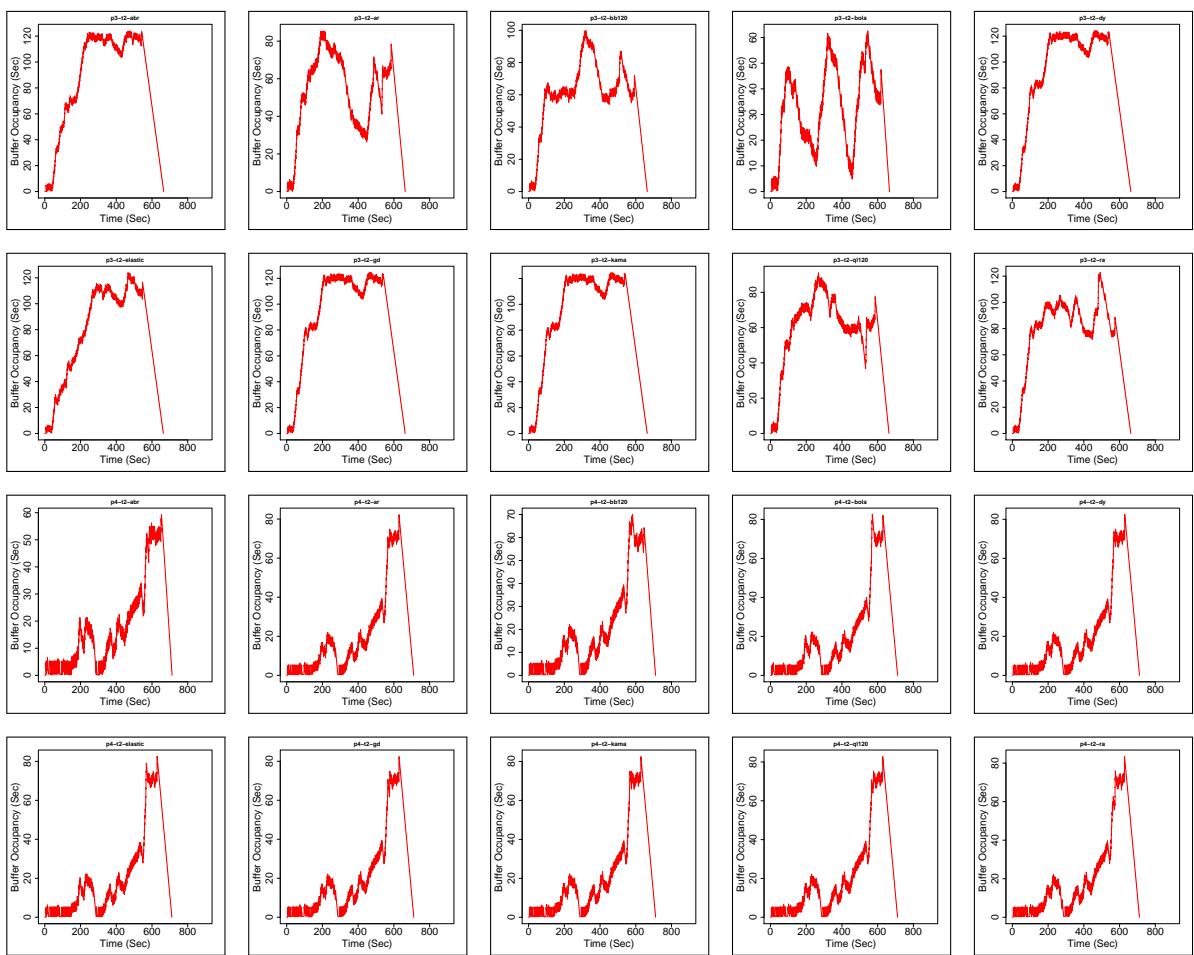
16 T1, Buffer Occupancy



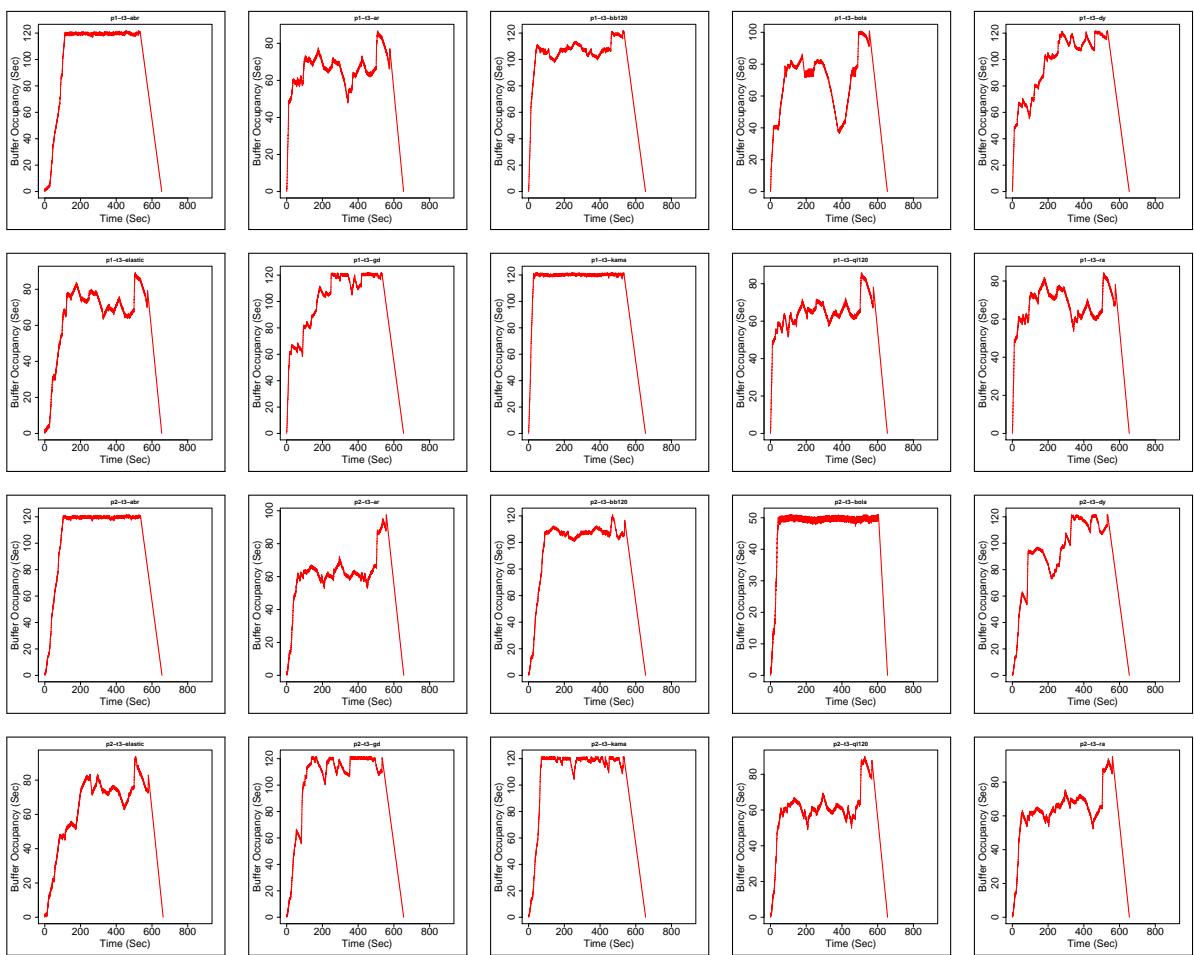
17 T2, Buffer Occupancy



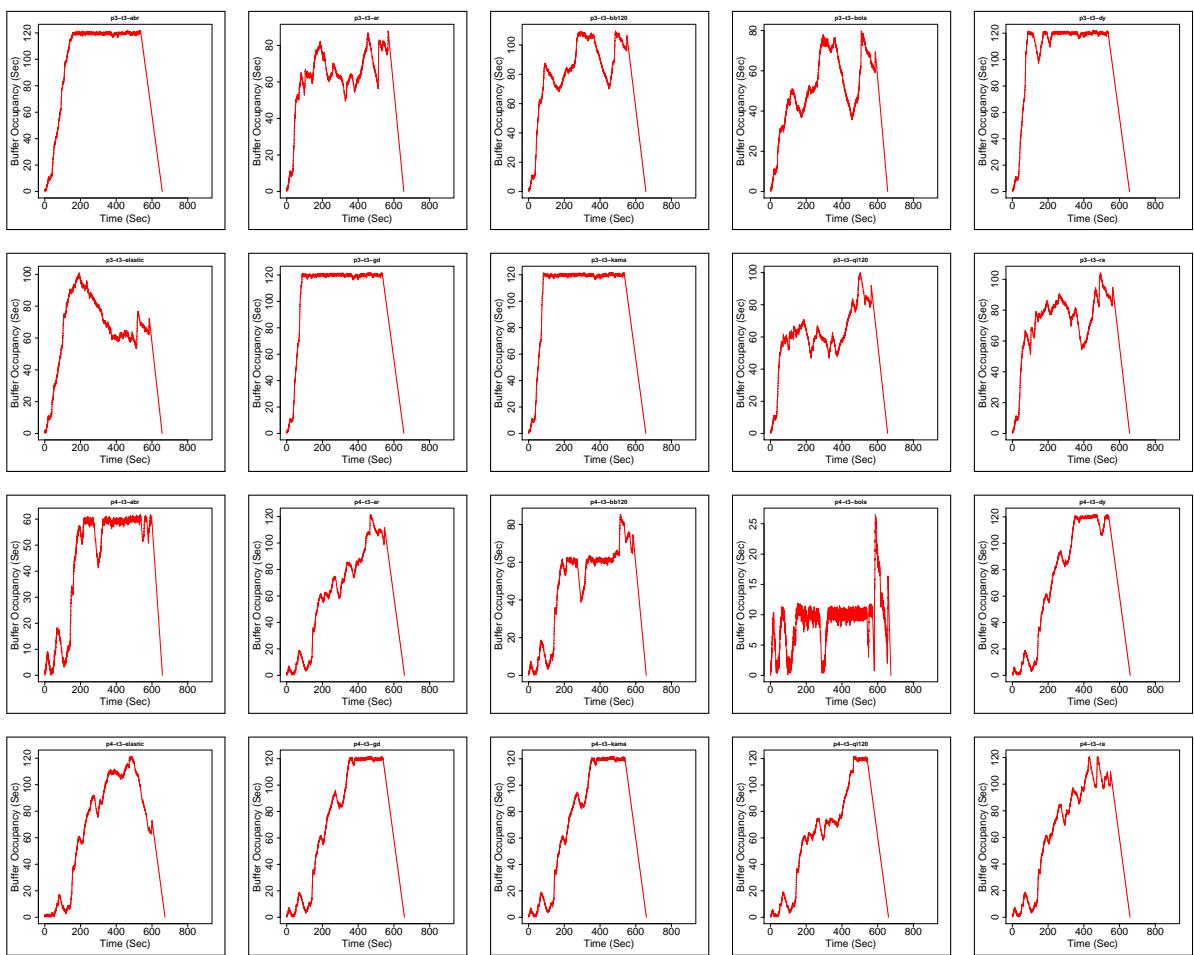
18 T2, Buffer Occupancy



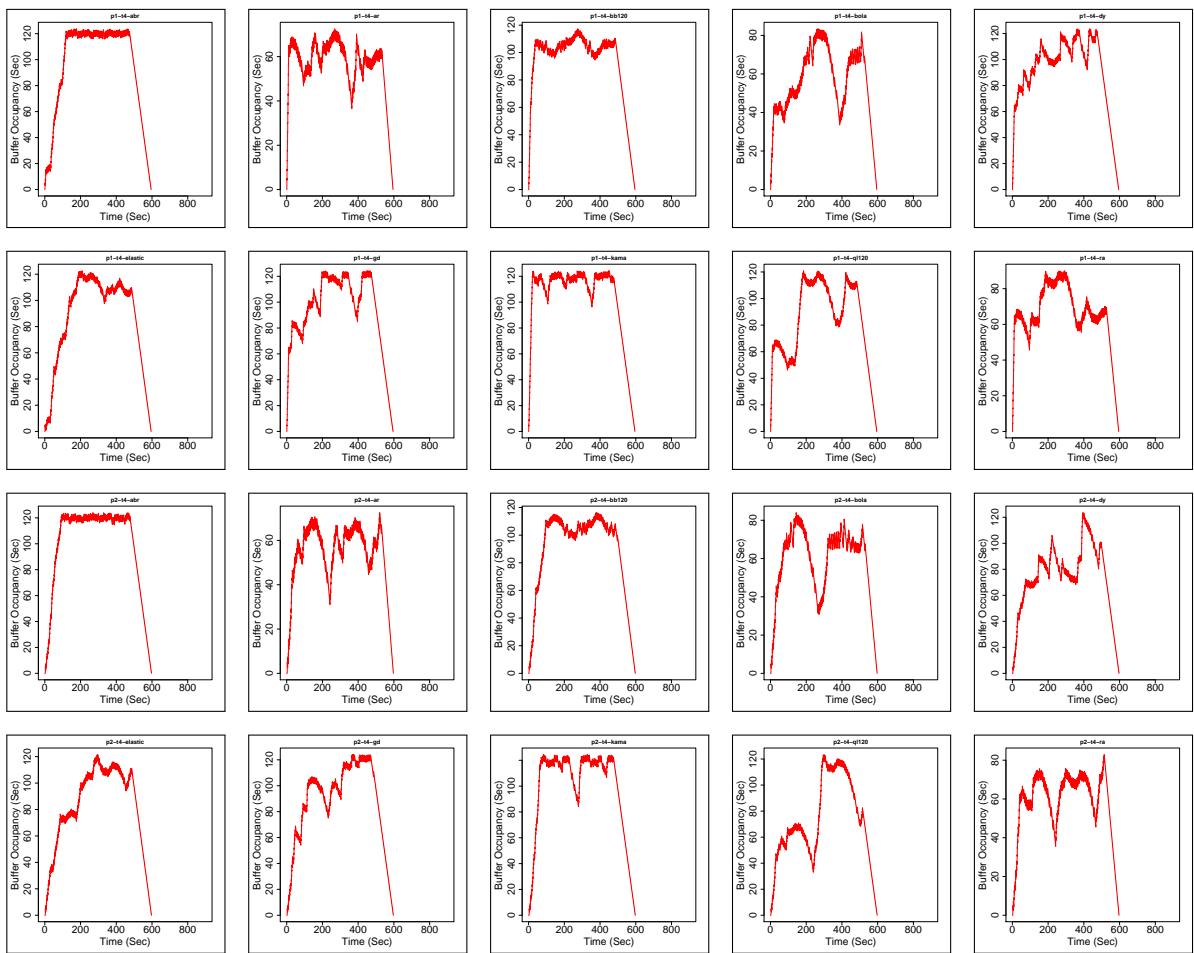
19 T3, Buffer Occupancy



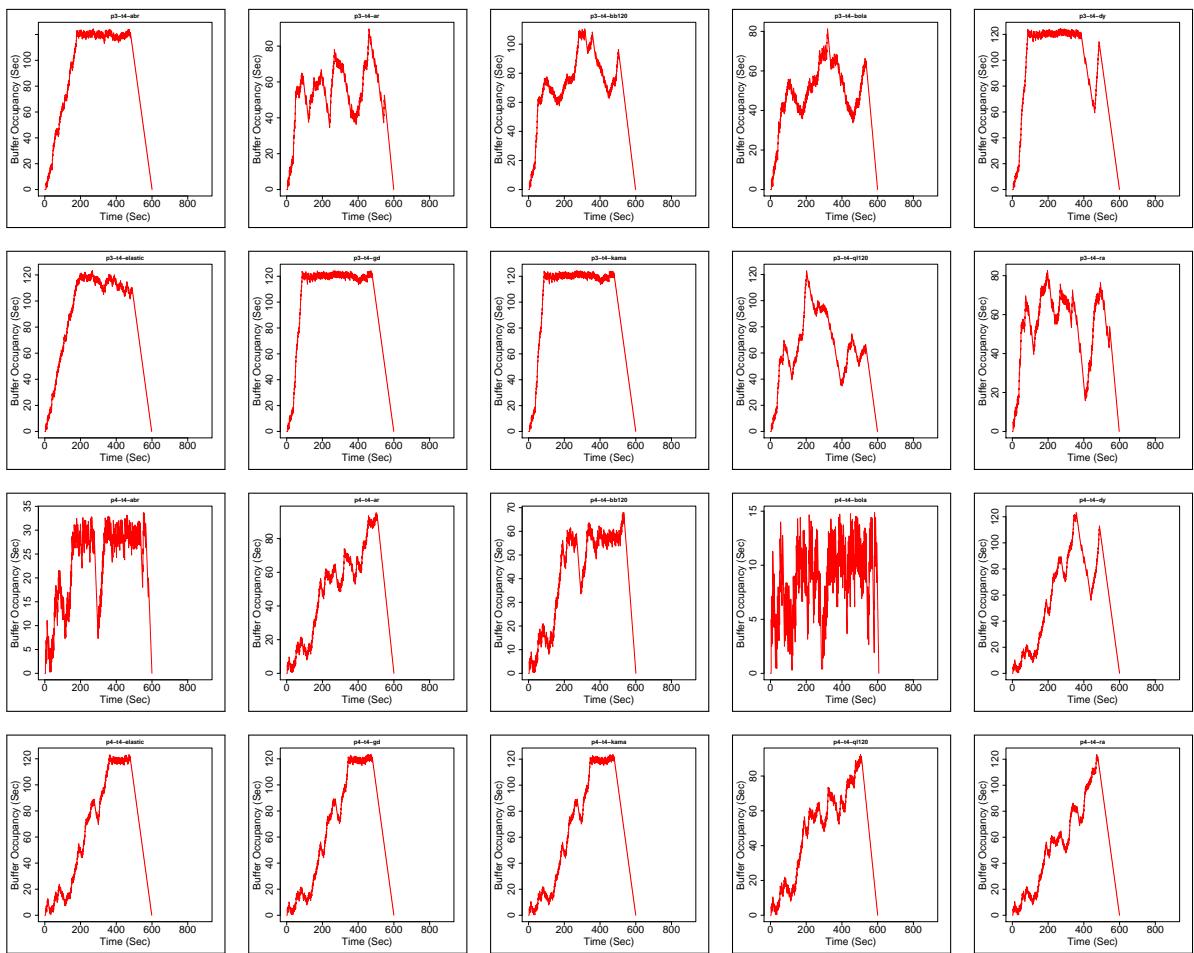
20 T3, Buffer Occupancy



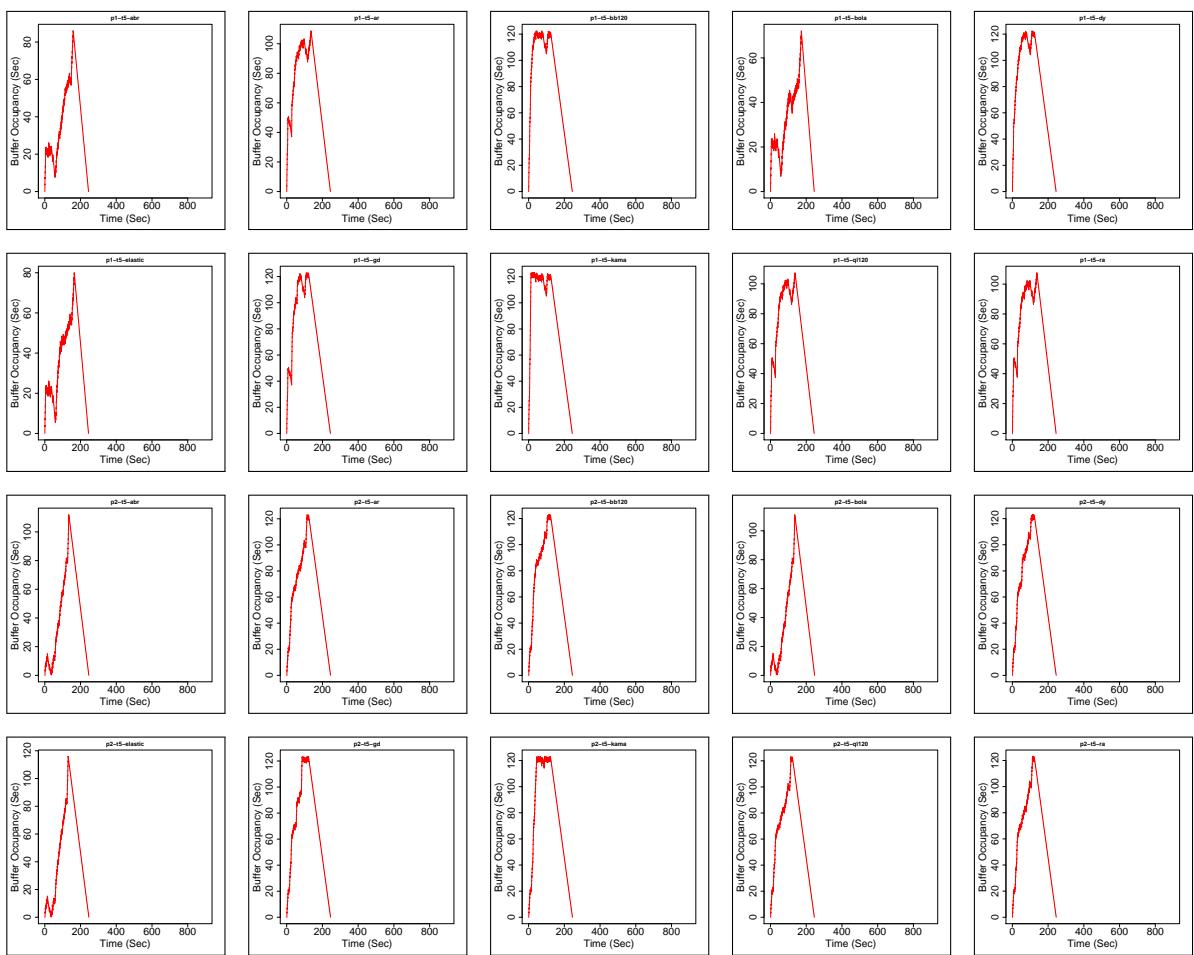
21 T4, Buffer Occupancy



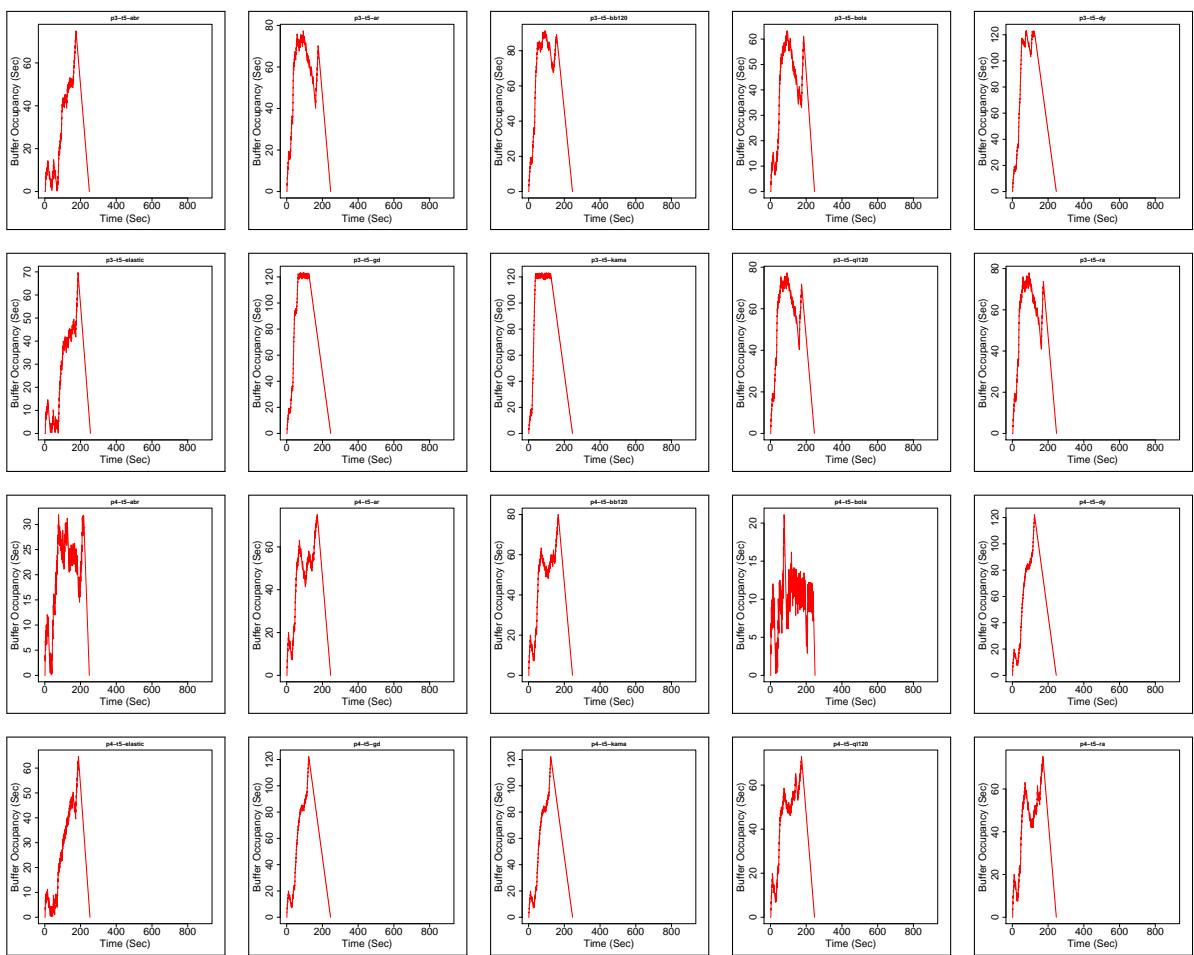
22 T4, Buffer Occupancy



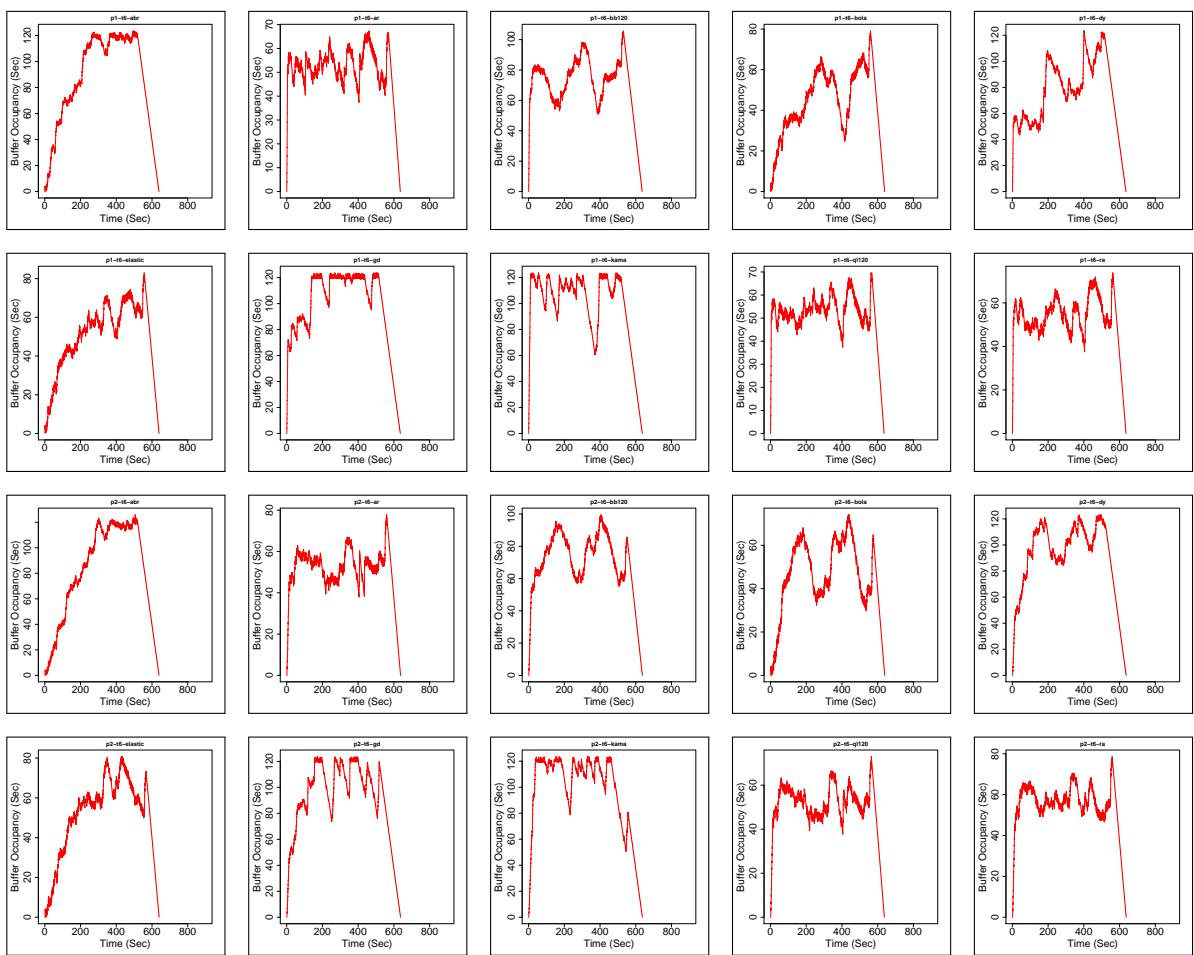
23 T5, Buffer Occupancy



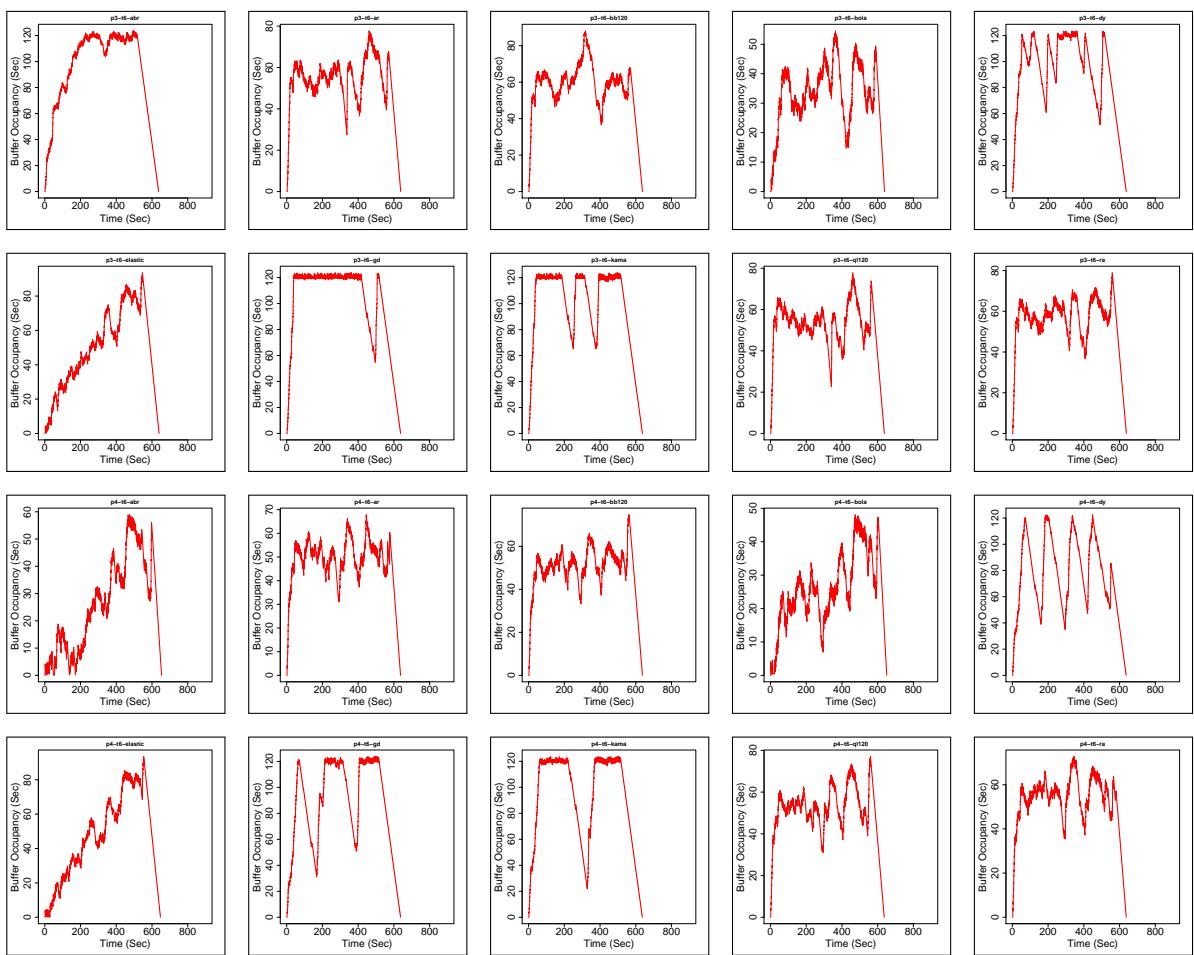
24 T5, Buffer Occupancy



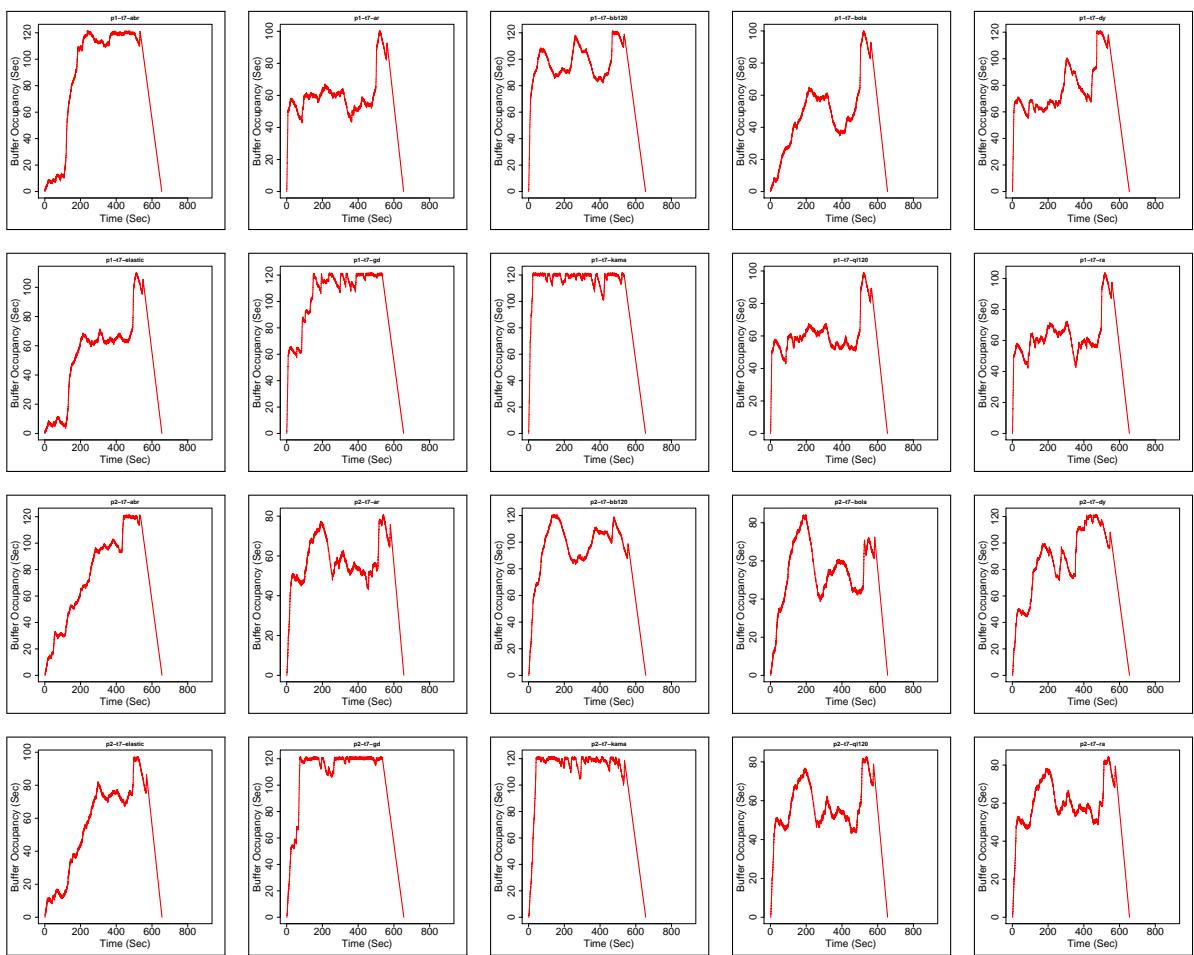
25 T6, Buffer Occupancy



26 T6, Buffer Occupancy



27 T7, Buffer Occupancy



28 T7, Buffer Occupancy

