

Software simulation

ARMSim

Assembly language will be simulated using ARMSim Simulator. LEGv8 and ARMSim (RMv7) are almost similar with little distinction.

LEGv8 VS ARMv7 (ARMSim) Registers

LEGv8 Registers

X0
X1
X2
X3
X4
X5
X6
X7
.
.
.
.
.
X31

ARMv7 Registers (ARMSim)

[illegible]

LEGv8 vs ARMSim Instructions

LEGv8	ARMSim
ADDI	ADD
SUBI	SUB
MOVI	MOV
LDUR	LDR
LDURB	LDRB
STUR	STR
CMPI	CMP

Program: A C++ program adds two given numbers a=10 and b=20 and prints resulting number. Convert the following C++ code to assembly code and simulate using ARMSim Version 2.1.

C++ Code:

```
#include<iostream>
using namespace std;
int main()
{
    int a=10, b=20, c;
    c= a + b;
    cout<<c;
    return 0;
}
```

LEGv8 Instructions:

```
MOVI X2, #10      @ X2 =10
MOVI X3, #20      @ X3 = 20
ADD X1, X2, X3    @ X1= X2+X3 =30
```

ARMSim Instructions:

```
MOV R2, #10      @ R2 =10
MOV R3, #20      @ R3 = 20
ADD R1, R2, R3   @ R1= R2+R3 =30
```

Program: A C++ program to find the factorial of 3. Convert the following C++ code to assembly code and simulate using ARMSim version 2.1.

C++ Code:

```
#include <iostream>
using namespace std;

int main()
{
    int i, j=1, n=3;
    for (i = 1; i <= n; i++)
    {
        j = j * i;
    }
    cout<<j;
}
```

ARMSim Instructions:

```
MOV R2, #3          @ n=3
MOV R5, #1          @ j=1
MOV R6, #1          @ i=1
loop:  CMP R6, R2     @ compare i and j
      BGT exit       @ go to exit
      MUL R5, R5, R6  @ j=j*i
      ADD R6, R6, #1  @ i=i+1 or i++
      B loop
exit:
```