**Problem:** If computer A runs a program in 10 seconds and computer B runs the same program in 15 seconds, how much faster is A than B?

$$=\frac{15}{10}$$

· performance A = 1.5 x penformanceB

**Problem:** Our favorite program runs in 10 seconds on computer A, which has a 2GHz clock. We are trying to help a computer designer build a computer, B, which will run this program in 6 seconds. The designer has determined that a substantial increase in the clock rate is possible, but this increase will affect the rest of the CPU design, causing computer B to require 1.2 times as many clock cycles as computer A for this program. What clock rate should we tell the designer to target?

$$\Rightarrow b = a + 3$$

$$e = a + b$$

## **Instruction Count and CPI**

<u>Instruction Count:</u> The number of instructions executed by the program.

<u>CPI (Clock cycles per instruction)</u>: Average number of clock cycles per instruction for a program.

The number of clock cycles required

for a program can be Written as

CPU clock cycles = Instruction count XCPI

CPU Time = CPU clock cycles Xclock cycletime

CPU TIME = CPU clock cycles

CPU TIME = CPU clock cycles

Clock Rate.

CPU TIME = Instruction Count X CPI

Clock Rate

CPU Time = Instruction count X CPIX

Clock cycle Time.

## Number of instructions = I.

**Problem:** Suppose we have two implementations of the same instruction set architecture. Computer A has a clock cycle time of 250ps and a CPI of 2.0 for some program, and computer B has a clock cycle time of 500ps and a CPI of 1.2 for the same program. Which computer is faster for this program and by how much?

## **CPI** in More Detail

**Problem:** A compiler designer is trying to decide between two code sequences for a computer. For a particular high-level language, the compiler writer is considering two code sequences that requires the following instruction counts. Which code sequence executes the most instructions? Which will be faster? What is the CPI for each sequence?

	Class	A	В	C ,
,	CPI	1	2	3
buidnimi *	IC in code sequence 1	2	1,	2.
D NOO NAM T X	IC in code sequence 2	4	1	1

2 executes the most instruction SERVENIE

epu clock cycles\_ = &2 x1 + 1x2+ 2x3 = 2+2+6 = 10 CPU Clock cycle 20 = 4 x1 + 1x2 +1x3 = 4+2+3=9,

Code Sequence 2 Will be fasten.

CLA Clock CACIET CPILA Instruction count

 $=\frac{10}{5}=(2)$ 

Instruction Counts CPIZ

 $\frac{9}{5} = (1.5)$