

# Resistance of Threshold Implementations against Statistical Ineffective Fault Attacks

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Journée de la Recherche

joint work with Vincent Grosso and Pierre-Louis Cayrel



UMR • CNRS • 5516 • Saint-Étienne



PROPHY ANR-22-CE39-0008-01

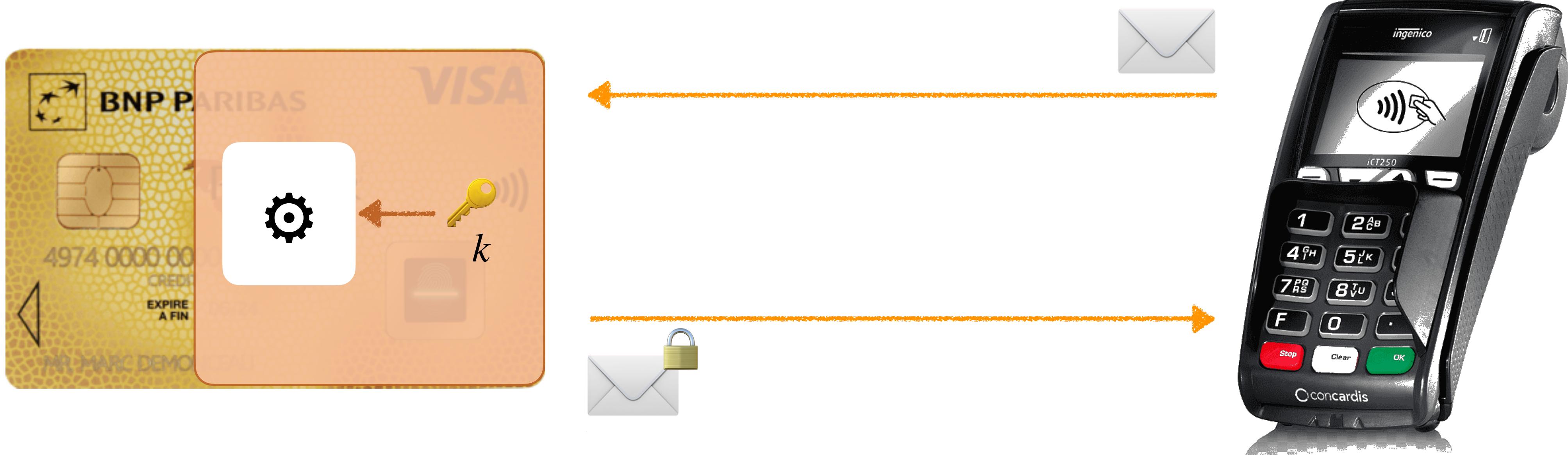


SIS  
SCIENCES  
INGÉNIERIE SANTÉ  
UNIVERSITÉ DE LYON

⚠ BIG CHALLENGE ⚠

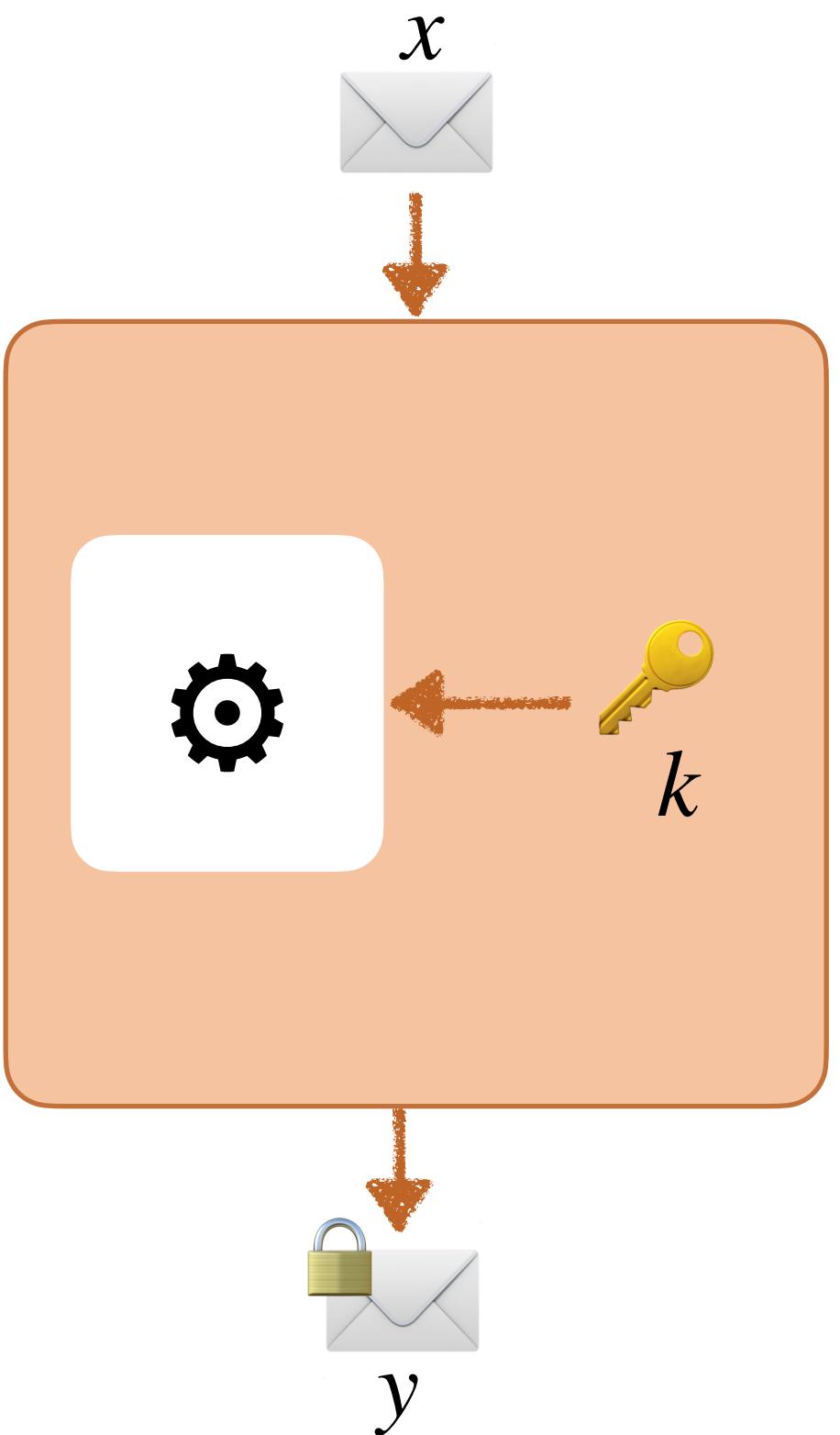
# Security of Data Transmission

# Payment Example



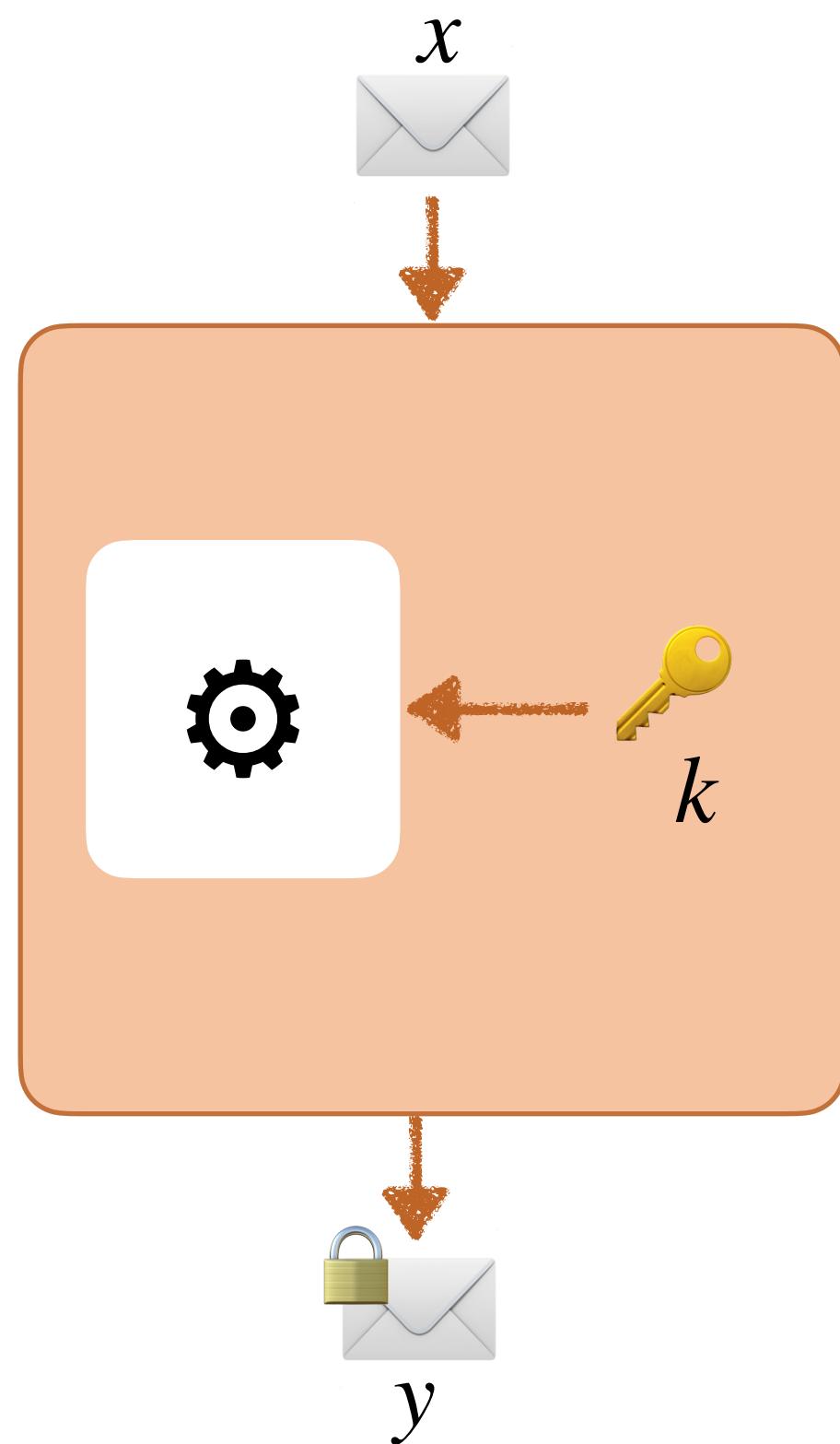
# Cryptography

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# Battle... here it comes!

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Attacker



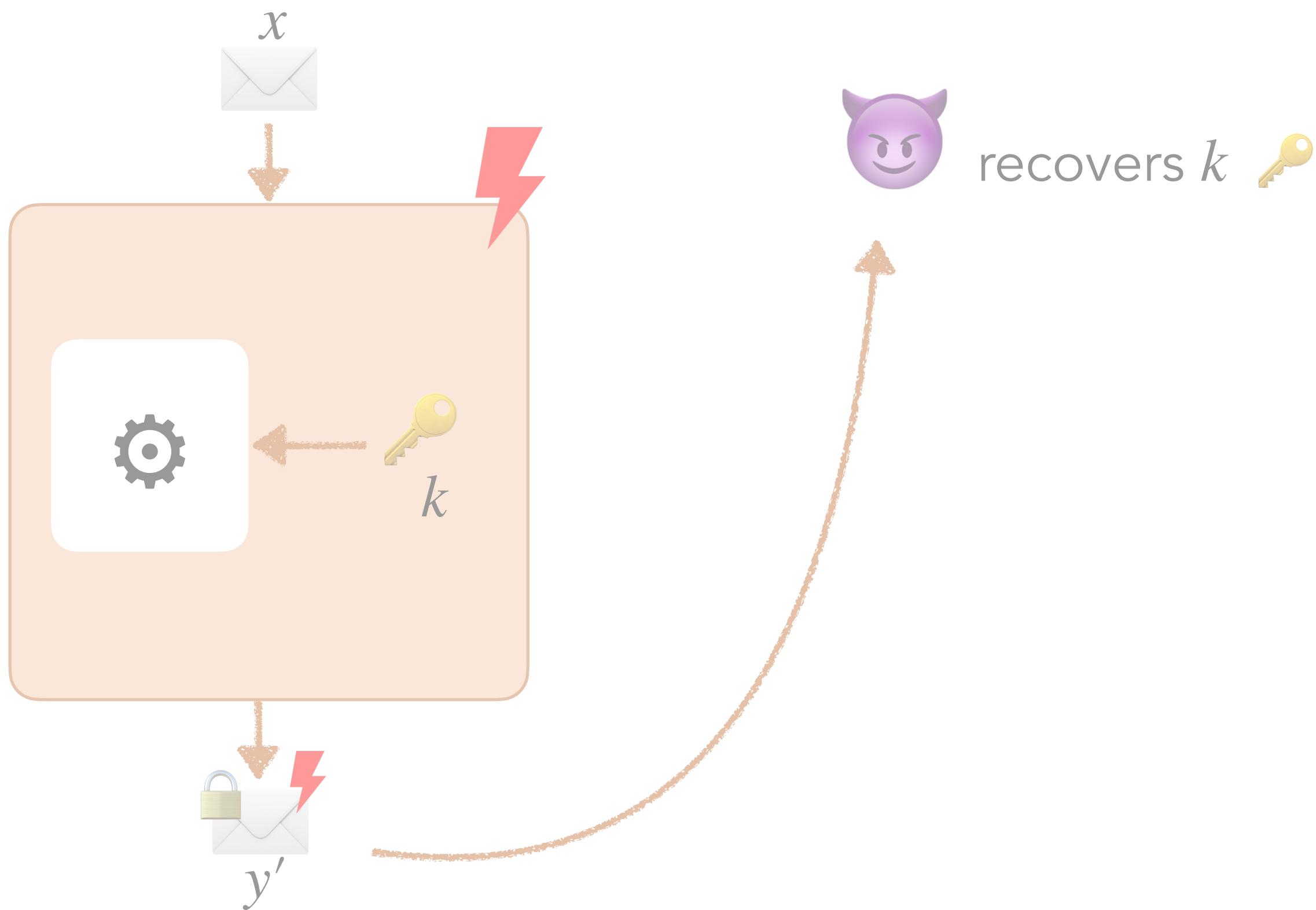
Designer

"I steal the key

"I protect the key



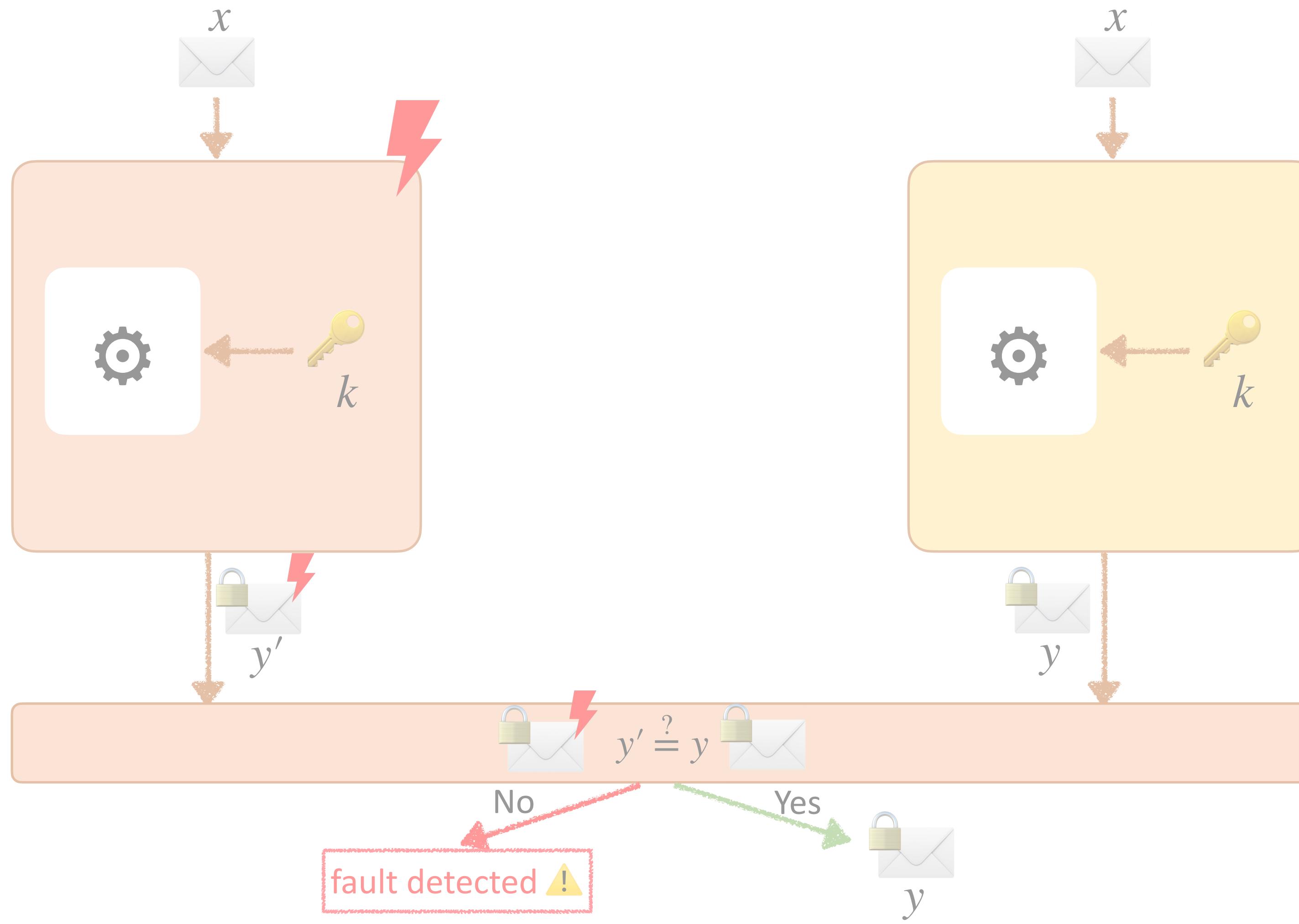
"I have fault attacks"





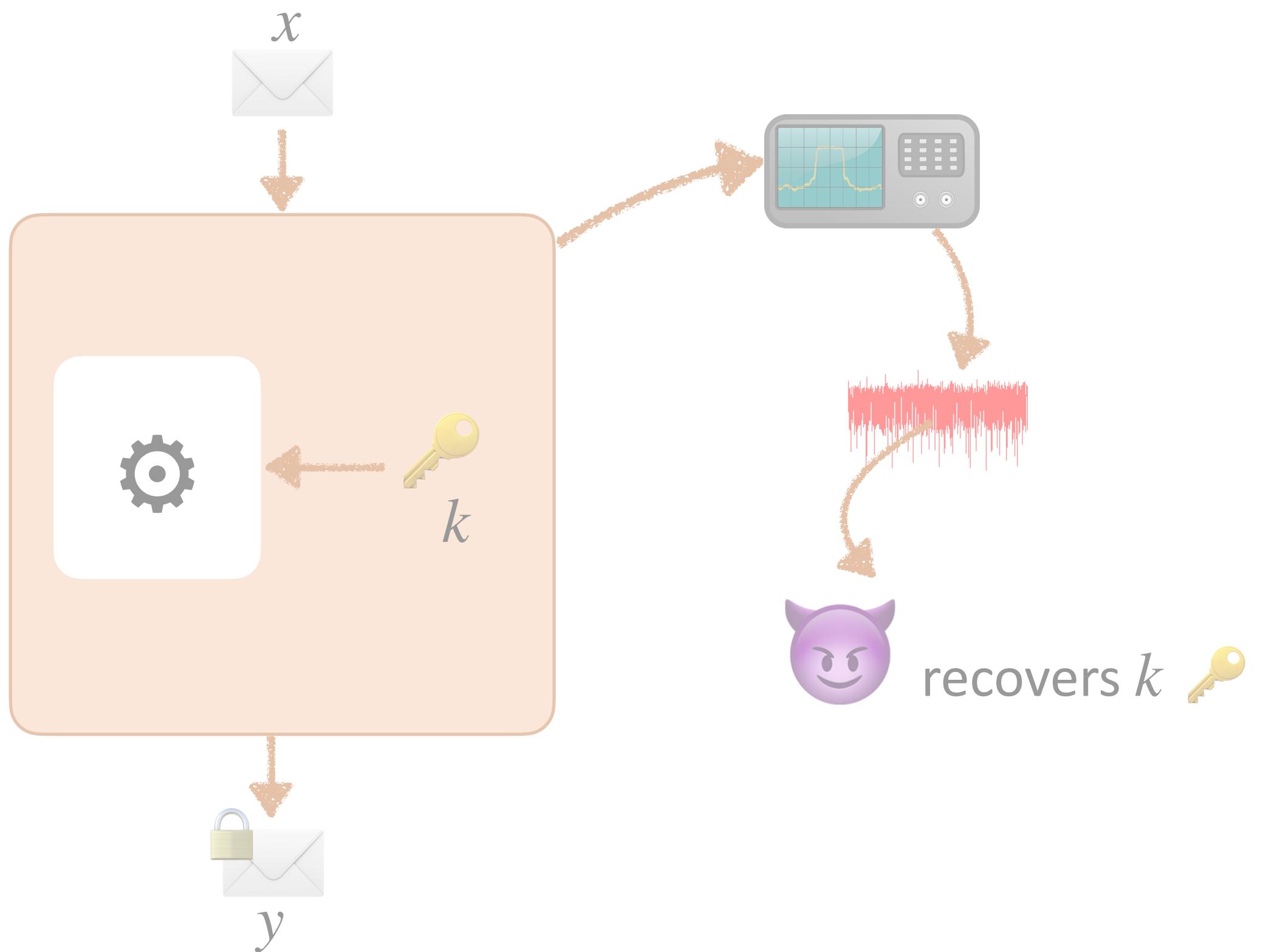
...

"I have countermeasure"





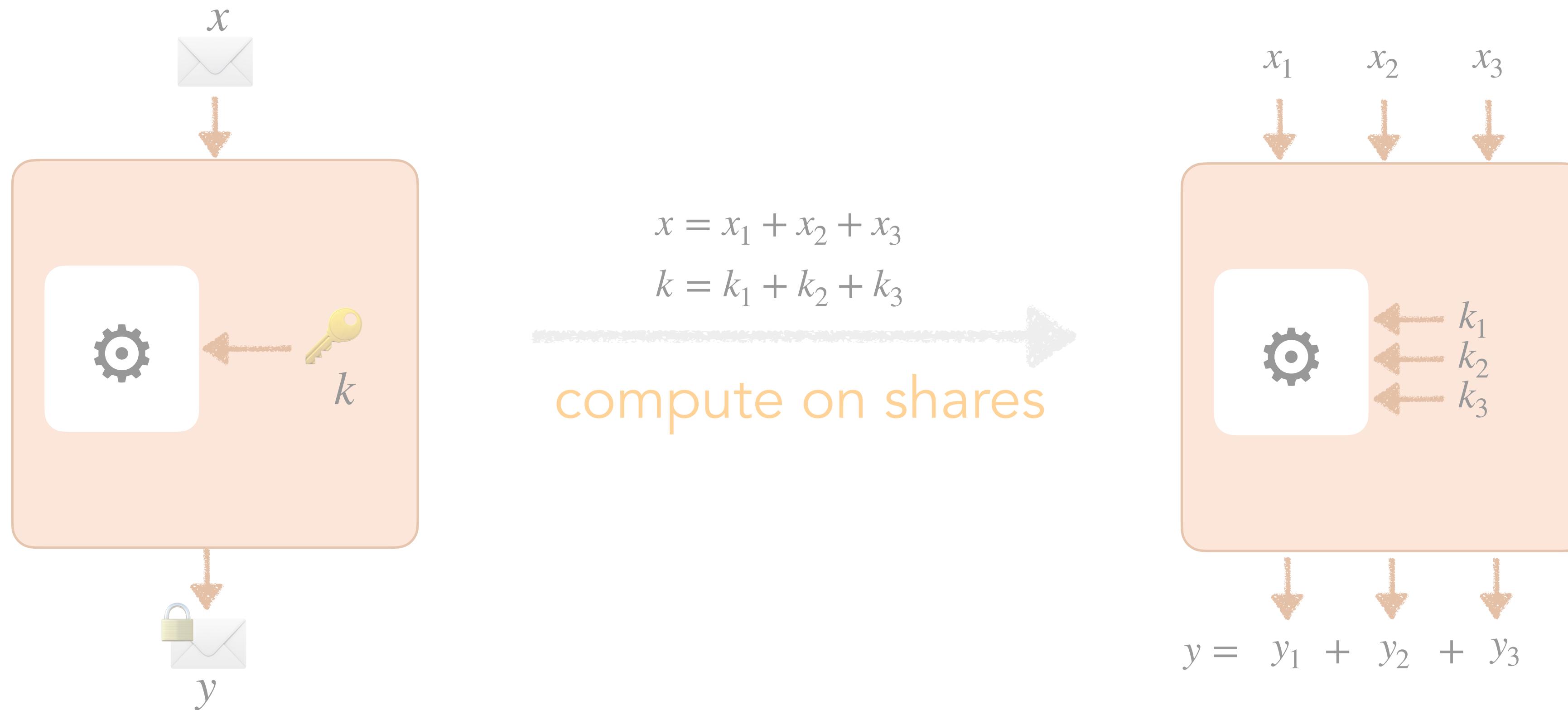
"I have side-channel attacks"





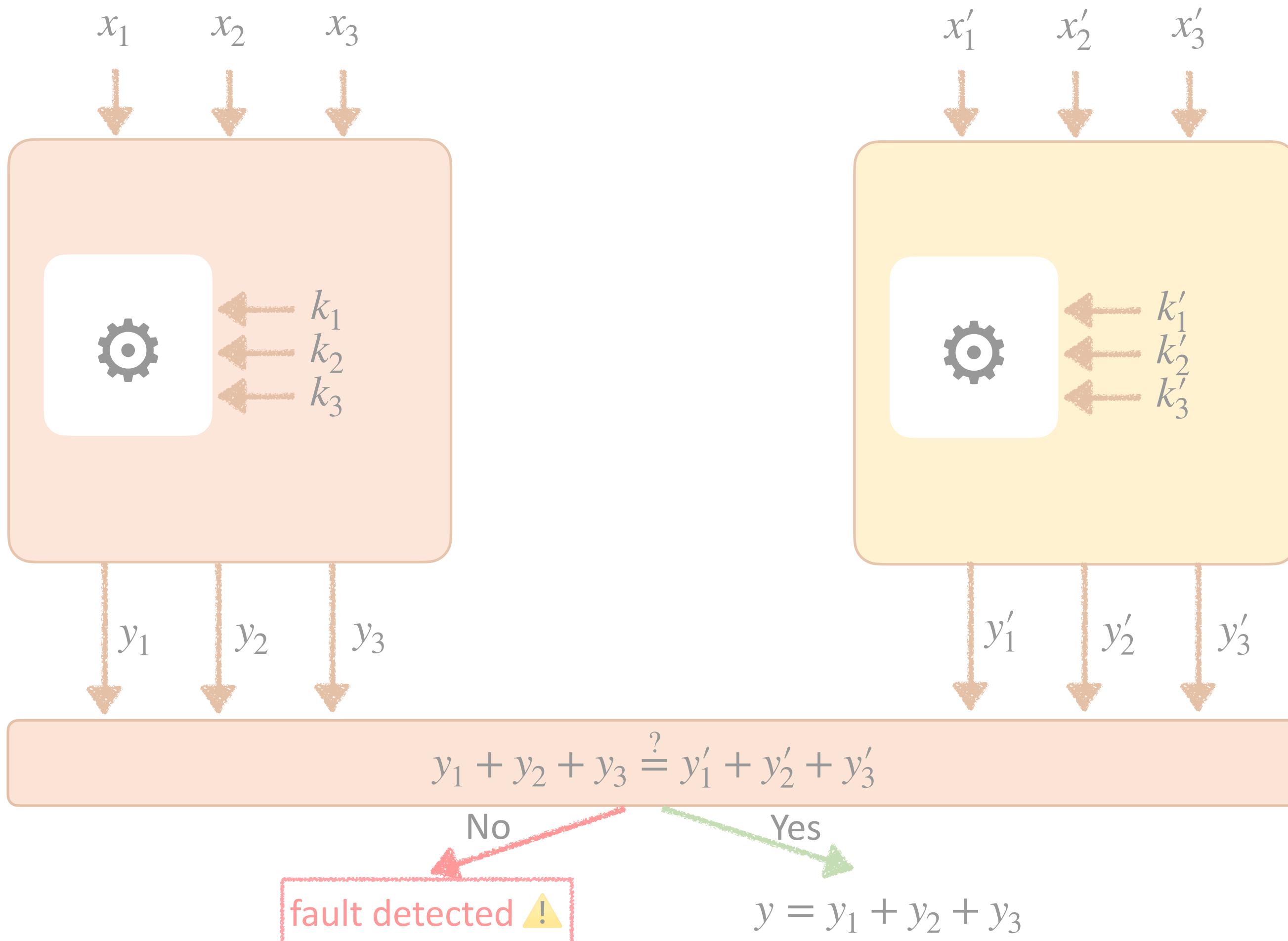
...

"I have countermeasure"





"This is combined countermeasure"



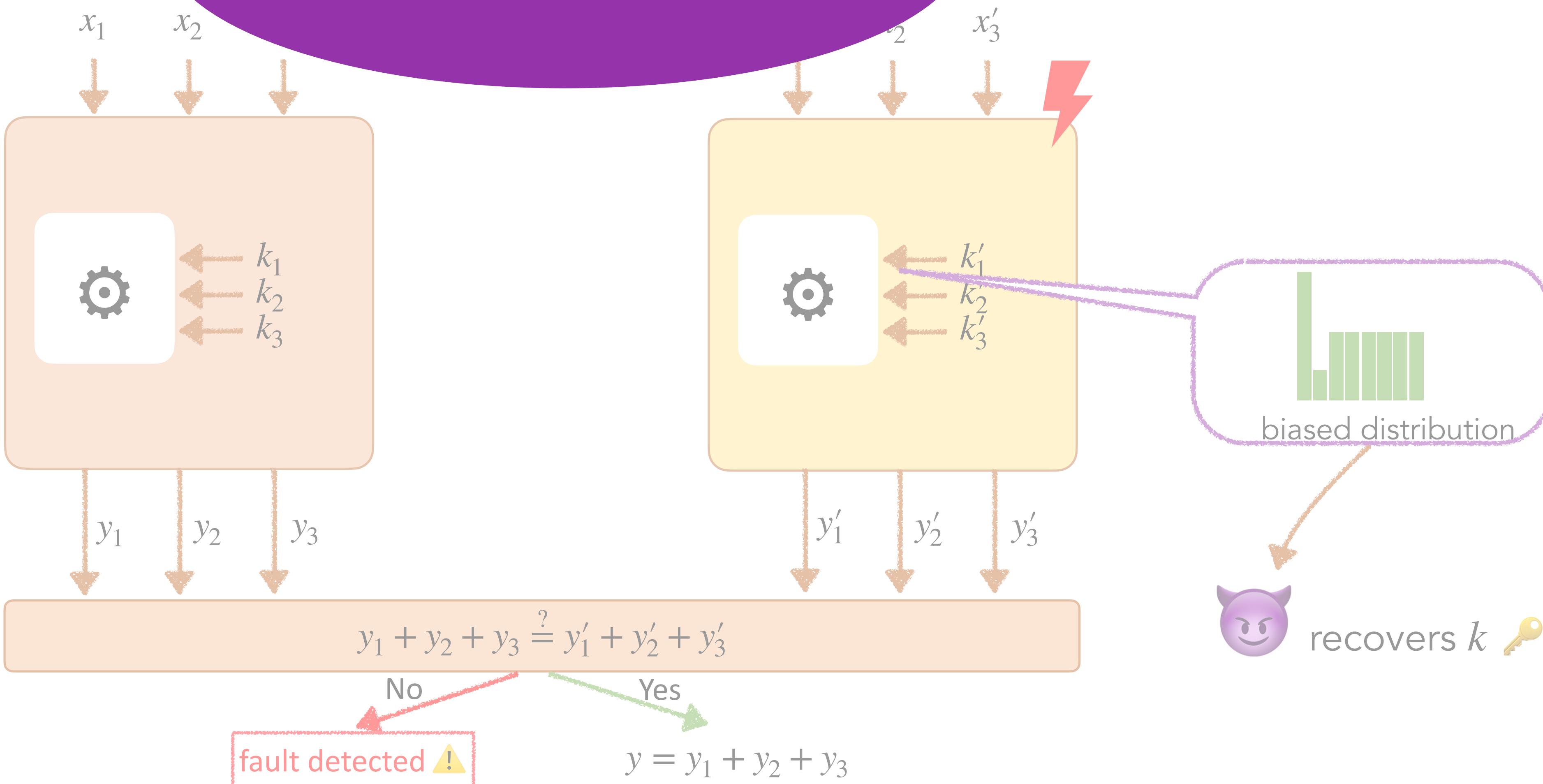


"I have stronger attack"



"Good countermeasure"

"This is state-of-the-art. Haha"





"I have stro

"I know the reason"

"This is state-of-the-art. Haha"

$x_1$

$x_2$

$\downarrow$



$x_2$

$x'_3$



"It's an implementation mistake"

$$y_1 + y_2 + y_3 \stackrel{?}{=} y'_1 + y'_2 + y'_3$$

No

Yes

fault detected !

$y = y_1 + y_2 + y_3$



recovers  $k$

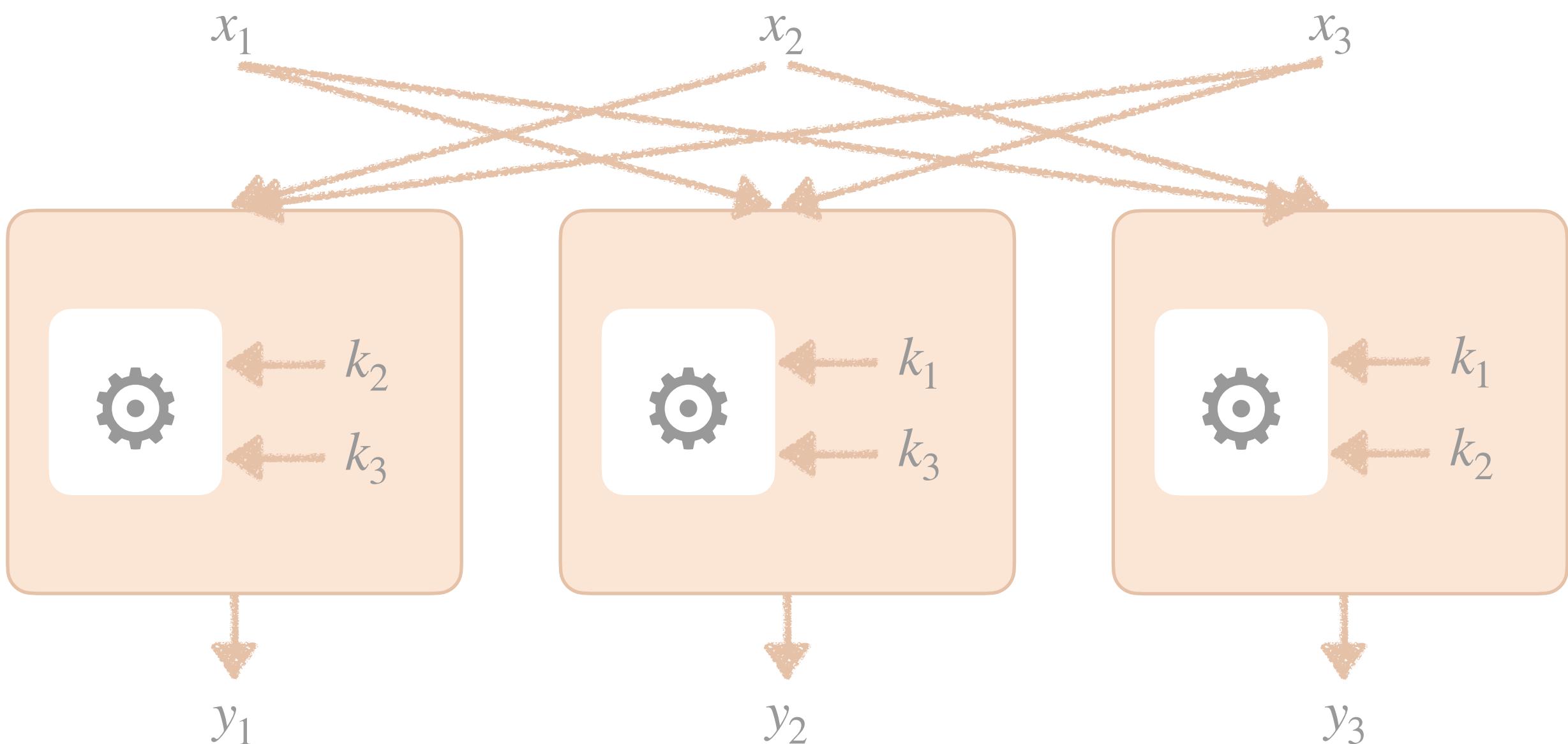


"I have stro

"I know the reason"

"This is state-of-

"I have countermeasure.  
Let's implement it properly!"





"I have stro

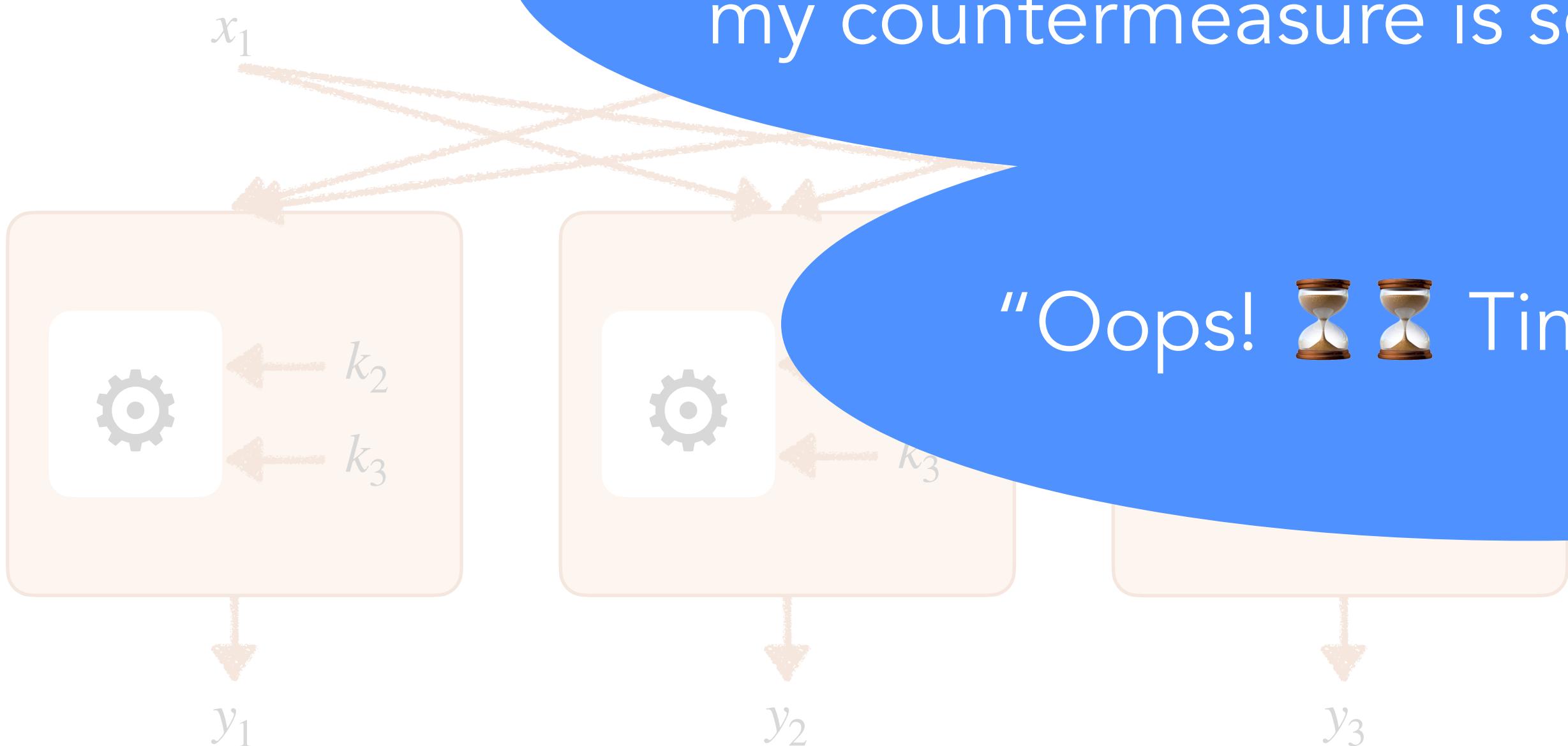
"This is state-o

"I know the reason"

"I have countermeasure.  
Let's implement it properly!"

"I can prove that  
my countermeasure is secure"

"Oops! ⏳ ⏳ Time's up! ⏴ ⏴"



"You want to know more?  
contact me 😊"



THANK YOU!

# **Natural Resistance of Threshold Implementations against Statistical (Ineffective) Fault Attacks**

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