

The 10th IEEE Non-Volatile Memory Systems and Applications Symposium

IEEE NVMSA 2021

CALL FOR PAPERS

August 18-20, 2021, Virtual Conference



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NVMSA is a premier conference in the area of non-volatile memory systems and emerging memory technologies. It provides a fantastic opportunity for global non-volatile memory researchers from different communities to discuss and exchange knowledge, ideas and insights, and to facilitate the establishment of potential collaborations that can speed up the progress in the design and application of NVMs. An expanded technical program will be offered in NVMSA 2021 for the audience from academia and industry.

Scope of Conference

Device/Circuit Design of NVM Systems: NVM Architecture and Systems:

- Emerging NVM Circuit Design
- NVM Device Design
- Error Correction for NVMs
- Non-volatile Logic Circuit
- Low Power NVM Circuit
- Non-volatile Registers
- Non-volatile Memory Architectures
- Non-volatile Cache Design
- NVM Neuromorphic Architectures
- NVDIMM

NVM Storage:

- Operating System Support for NVM
- Compiler Optimization for NVM
- NVM-based Storage Software
- NVM-based Databases
- NVM Controller Design

NVM Applications:

- In-memory Computing
- NVM for Big Data Analytics
- NVM in Mobile Healthcare Applications
- NVM in Wearable Applications
- NVM in the Internet of Things

Paper Submission

Both research and industry track papers are solicited. All papers must be submitted electronically in PDF format. The submitted manuscripts must describe original work not previously published and not concurrently submitted elsewhere. Submissions must be no more than 6 pages in the IEEE conference proceedings format (two-column, single-space). Each paper is peer-reviewed by the program committee members, and the authors are notified of either "accepted for a long presentation", "accepted for a short presentation", or "reject". A long presentation will be given a 30-mins slot (including Q&As) and a short presentation will be given a 20-mins slot (including Q&As) at the conference. Note that authors of a short presentation do not need to shorten the paper itself. Accepted papers will be included in the IEEE Xplore and will be EI indexed. Best papers will be fast tracked to ACM Transactions on Storage for possible publication. Authors of non-accepted papers are encouraged to submit a poster. The authors should submit their papers through the submission web page at:

<https://easychair.org/my/conference?conf=nvmsa2021>

Important Dates

Abstract Submission: ~~May 14, 2021 23:59 (AoE)~~ **May 21, 2021 23:59 (AoE)**

Paper Submission: ~~May 21, 2021 23:59 (AoE)~~ **May 28, 2021 23:59 (AoE)**

Acceptance Notification: **June 25, 2021**

Camera-ready Deadline: **August 6, 2021**

Conference Date: **August 18-20, 2021**

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