

# Report Generated by Test Manager

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**Title:** Baseline\_Test\_report

**Author:**

**Date:** 02-Apr-2023 22:11:23

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## Test Environment

Platform: MACI64

MATLAB: (R2022a)


## Summary

Name	Outcome	Duration (Seconds)
<a href="#">Results: 2023-Apr-02 21:55:33</a>		31.941
 <a href="#">New Test Case 1</a>		31.898

## Results: 2023-Apr-02 21:55:33

Result Type: Result Set  
Parent: None  
Start Time: 02-Apr-2023 21:55:38  
End Time: 02-Apr-2023 21:56:10  
Outcome: Total: 1, **Passed: 1**

### Aggregated Coverage Results

Analyzed Model	Sim Mode	Complexity	Decision	Execution
 <a href="#">Vehicle Speedometer Module</a>	Normal	10	100%	100%

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## New Test Case 1

### Test Result Information

Result Type: Test Case Result  
Parent: [Results: 2023-Apr-02 21:55:33](#)  
Start Time: 02-Apr-2023 21:55:38  
End Time: 02-Apr-2023 21:56:10  
Outcome: **Passed**


### Test Case Information


Name: New Test Case 1  
Type: Baseline Test  
Baseline Name: baseline1  
Baseline File: /Users/jamesbond/Desktop.xlsx

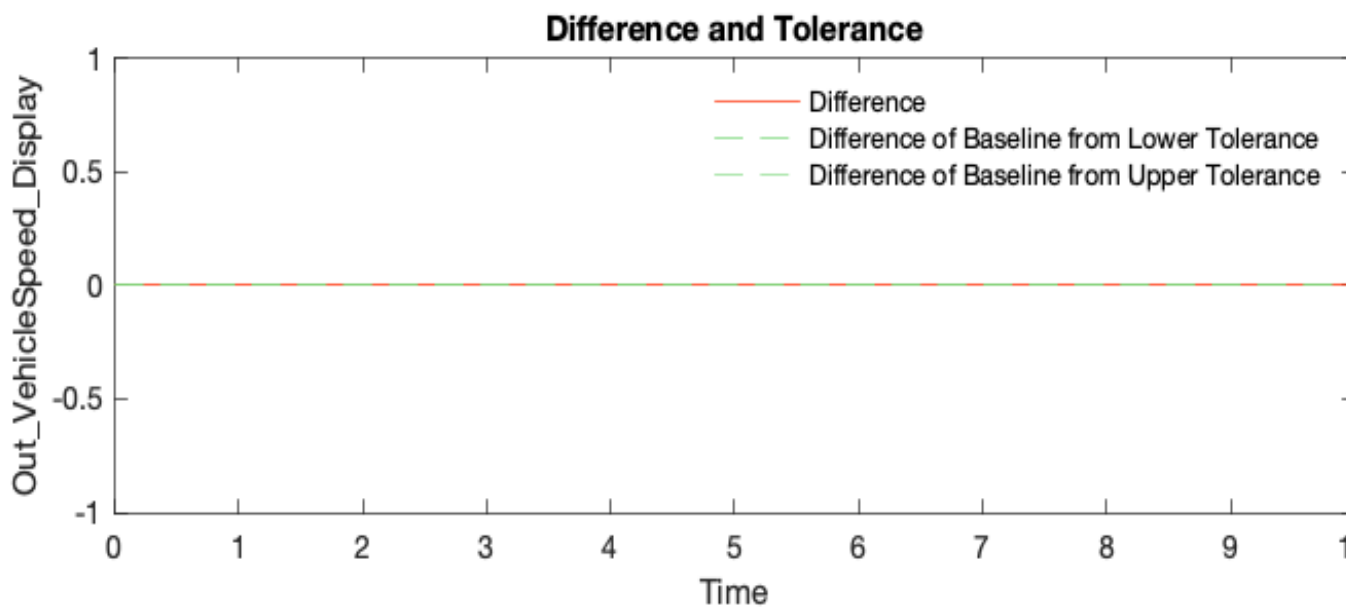
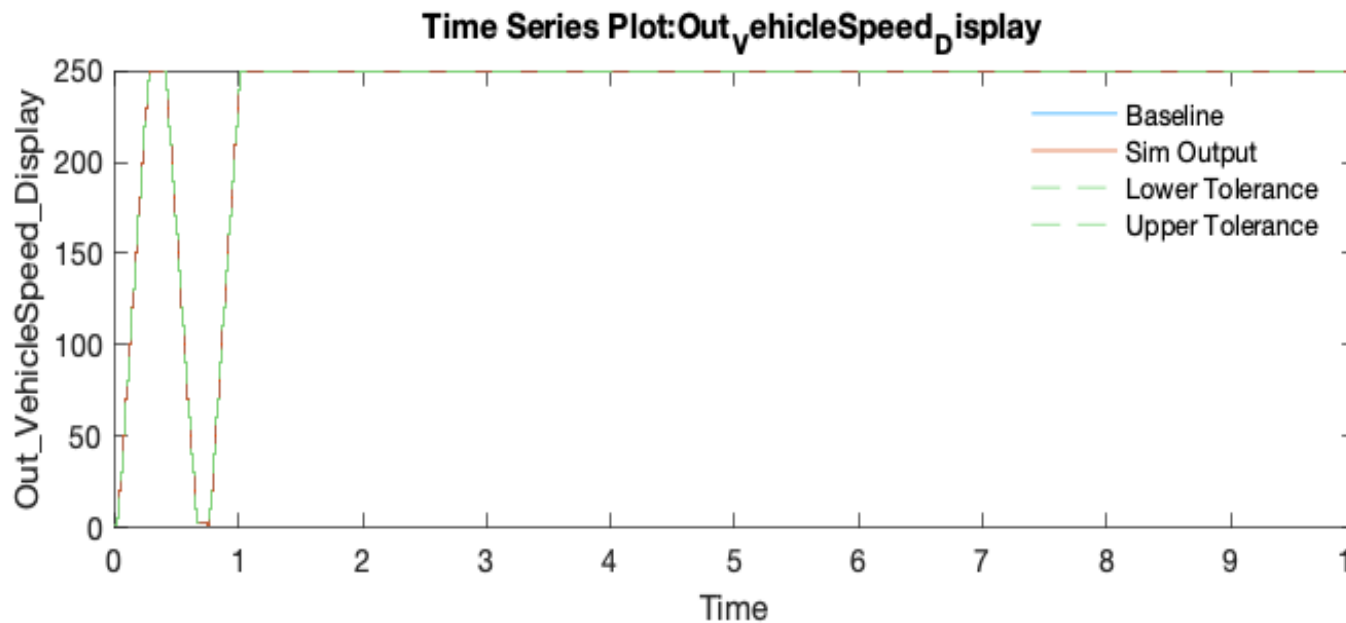
### Test Case Requirements

Description: TFT\_Cluster\_Speedometer\_Req\_1818.slreqx:TFT\_Cluster\_Speedometer\_Req\_1818  
Document: ../../Desktop/TFT\_Cluster\_Speedometer\_Req\_1818.slreqx

## Baseline Comparison

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync	Link to Plot
 Out_VehicleSpeed_Display	0	0	0	0	0	fxp_slope_bias_scaling			ufix16_Sp01		0.01	zoh	union	<a href="#">Link</a>

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
 Out_VehicleSpeed_Display	0	0	0	0	0	fxp_slope_bias_scaling			ufix16_Sp01		0.01	zoh	union



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## Simulation

### System Under Test Information

Model: Vehicle\_Speedometer\_Module  
Release: Current

Simulation Mode: normal  
Override SIL or PIL Mode: 0  
Configuration Set: Configuration  
External Input Name: input  
External Input File: /Users/jamesbond/Desktop/nv.xls  
Start Time: 0  
Stop Time: 10  
Checksum: 1959822732 1146748841 1462711833 2177022575  
Simulink Version: 10.5  
Model Version: 1.43  
Model Author: jamesbond  
Date: Sun Apr 02 13:49:32 2023  
User ID: jamesbond  
Model Path: /Users/jamesbond/Desktop/TFT Speedometer  
Module/Vehicle\_Speedometer\_Module.slx  
Solver Name: FixedStepDiscrete  
Solver Type: Fixed-Step  
Fixed Step Size: 0.01  
Simulation Start Time: 2023-04-02 21:55:39  
Simulation Stop Time: 2023-04-02 21:55:53  
Platform: MACI64

#### Simulation Logs:

Inconsistent numeric values for port 1 of '[Vehicle\\_Speedometer\\_Module/In\\_AuxFilt\\_SpeedValue](#)':  
Output value (uint8(0)) at major time step 0 is less than minimum (uint8(1)) from  
'[Vehicle\\_Speedometer\\_Module/In\\_AuxFilt\\_SpeedValue](#)'

Inconsistent numeric values for port 1 of '[Vehicle\\_Speedometer\\_Module/Avg\\_VehicleSpeed](#)': Output  
value (fi([],0,16,0.01,0,'int',uint16(29000))) at major time step 1.05 is greater than maximum  
(fi([],0,16,0.01,0,'int',uint16(28000))) from '[Vehicle\\_Speedometer\\_Module/Avg\\_VehicleSpeed](#)'

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