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Code Generation Report for 'Vehicle_Speedometer_Module'

Model Information

Author	jamesbond
Last Modified By	jamesbond
Model Version	1.35
Tasking Mode	SingleTasking

[Configuration settings at time of code generation](#)

Code Information

System Target File	ert.tlc
Hardware Device Type	Intel->x86-64 (Mac OS X)
Simulink Coder Version	9.7 (R2022a) 13-Nov-2021
Timestamp of Generated Source Code	Sun Mar 19 04:03:56 2023
Location of Generated Source Code	/Users/jamesbond/Desktop/Vehicle_Speedometer_Module_ert_rtw
Type of Build	Model
Memory Information	Global Memory: 18(bytes) Maximum Stack: 16(bytes)
Objectives Specified	Unspecified

Additional Information

Code Generation Advisor	Not run
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