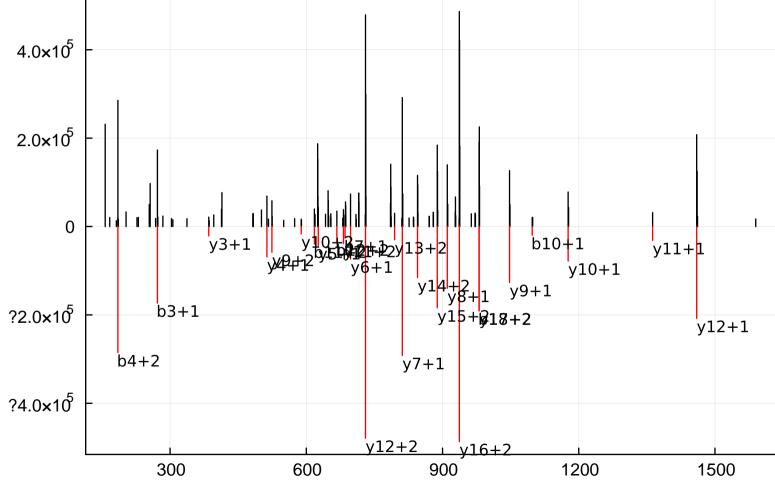
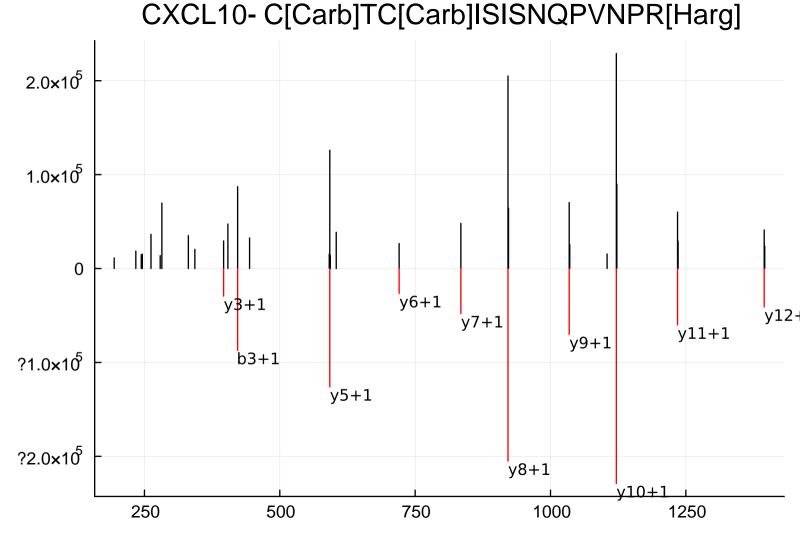
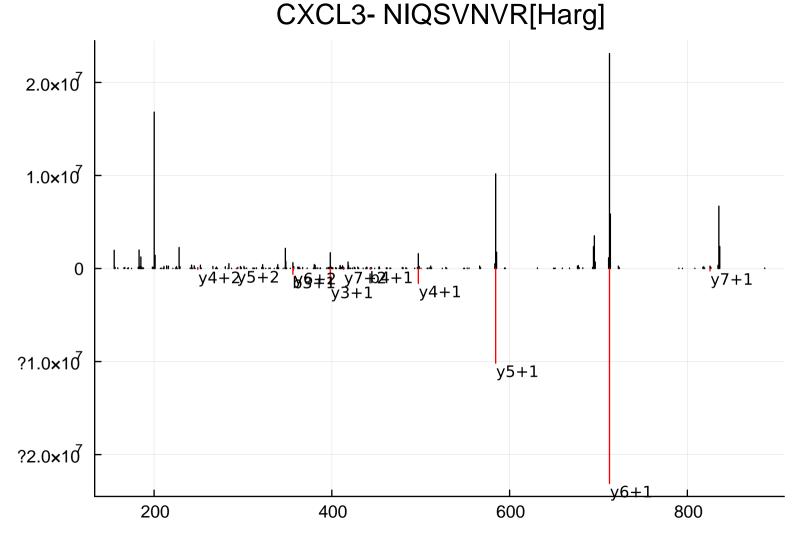
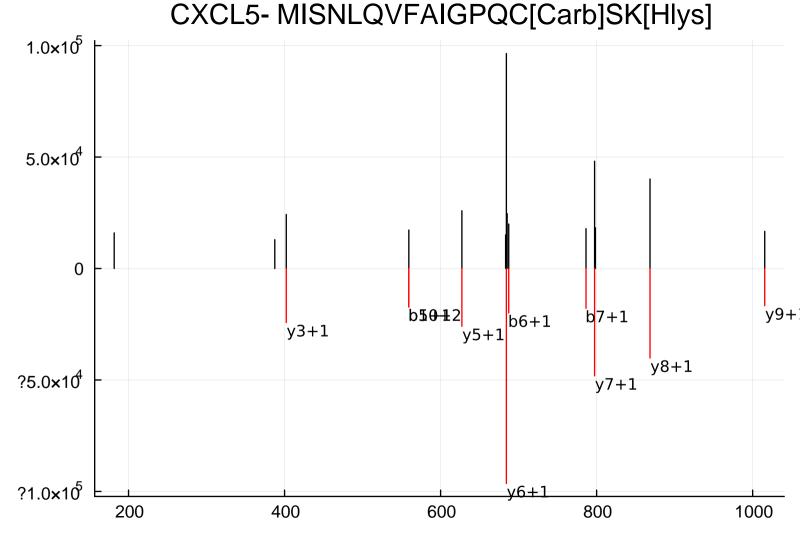


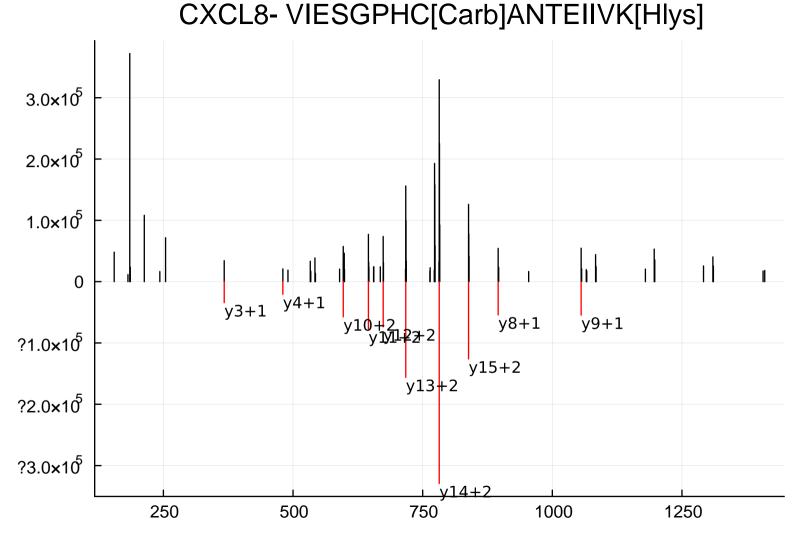
## CSF2- SPSPSTQPWEHVNAIQEAR[Harg]

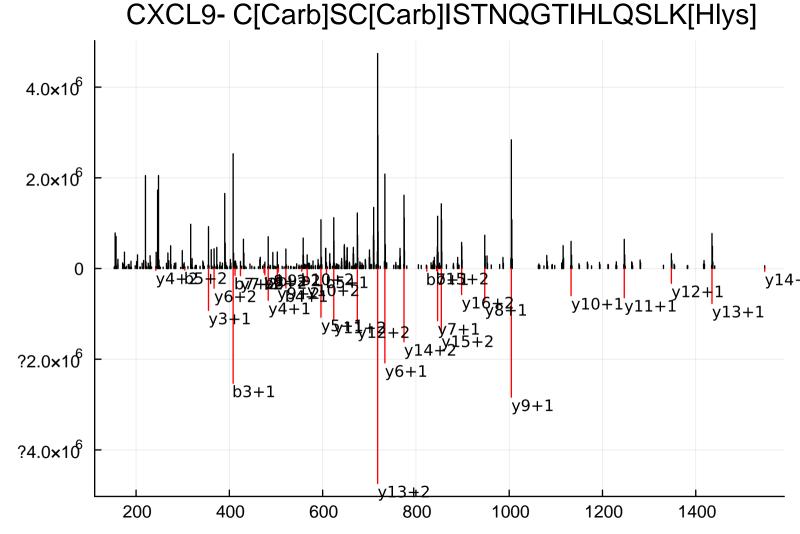




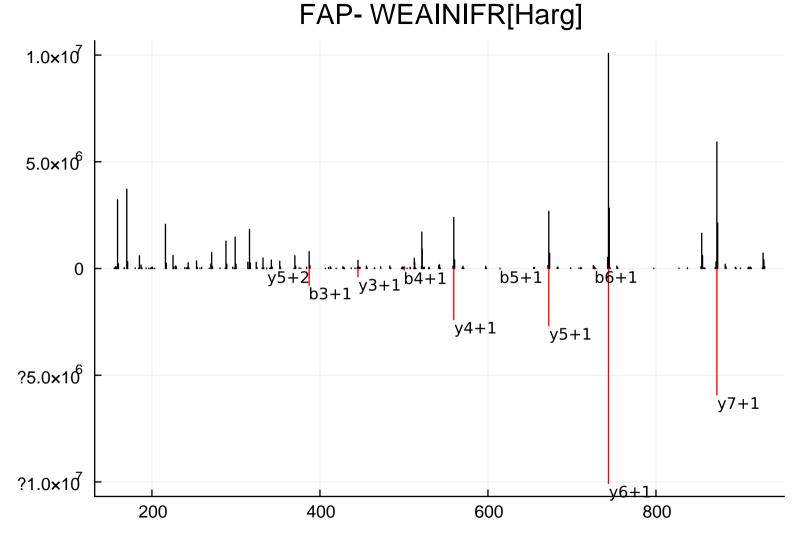


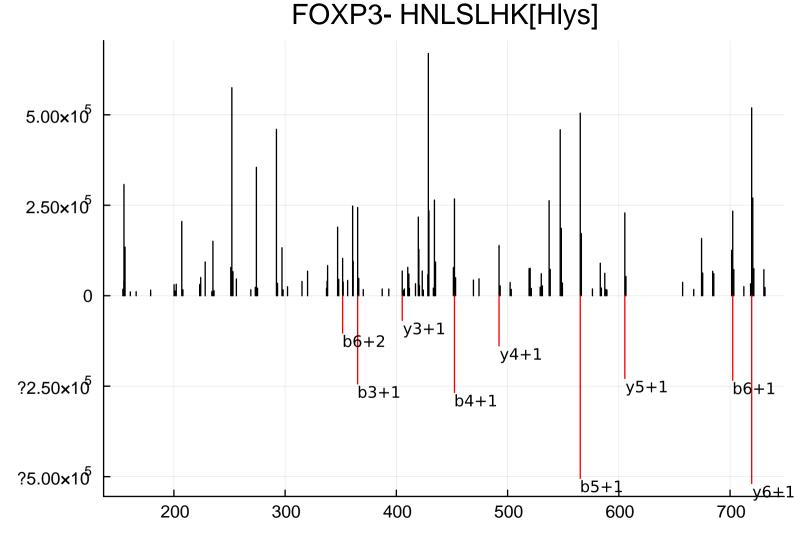


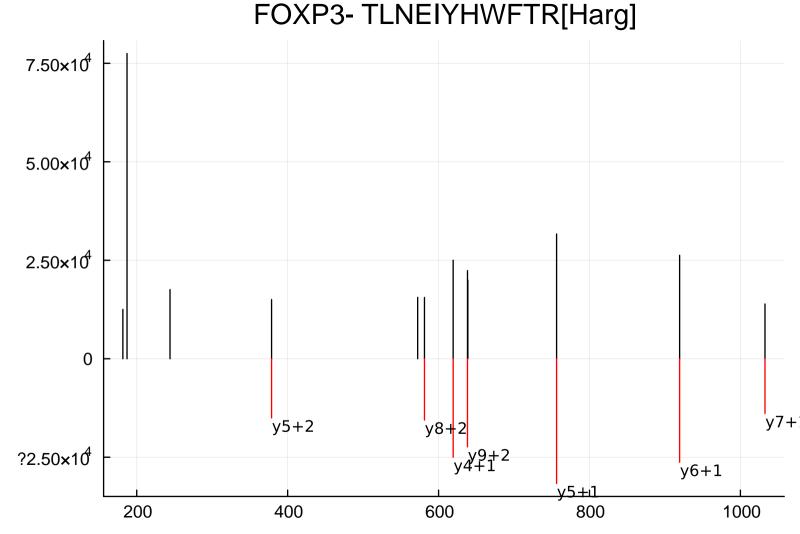


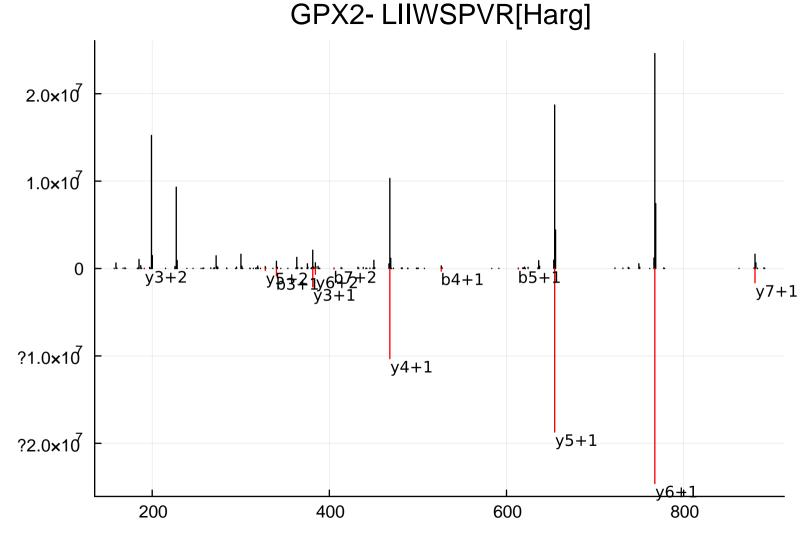


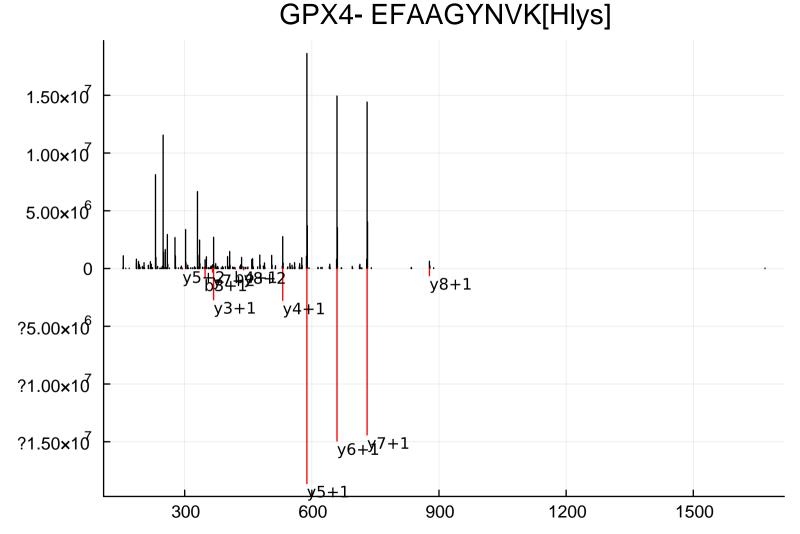
E7- LC[Carb]VQSTHVDIR[Harg] 1.50×10<sup>6</sup> 1.00×10<sup>6</sup> 5.00×10<sup>5</sup> 200 400 600 800 1000

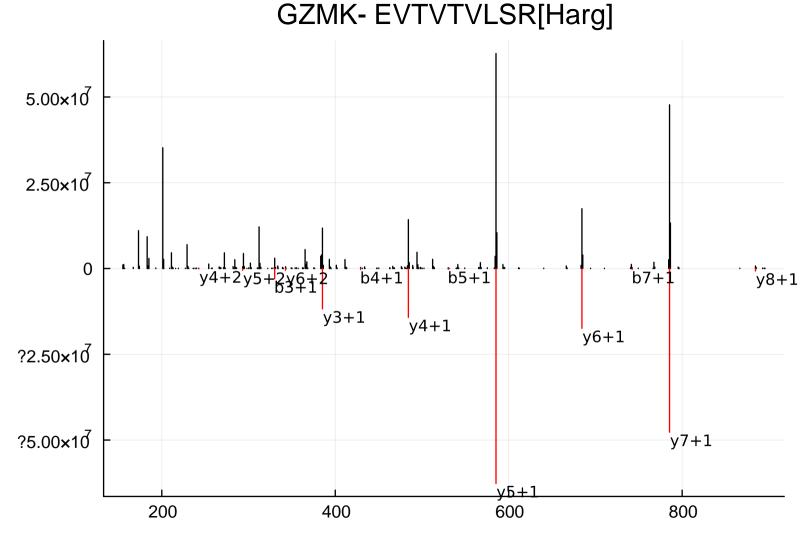


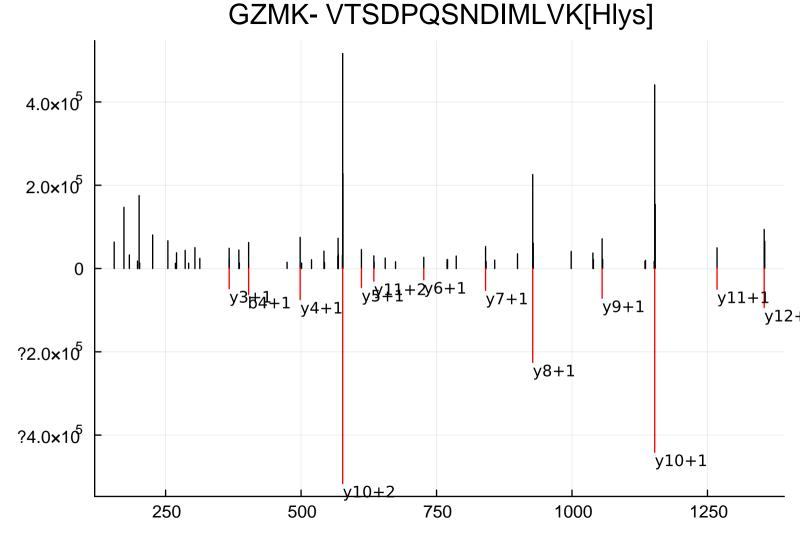


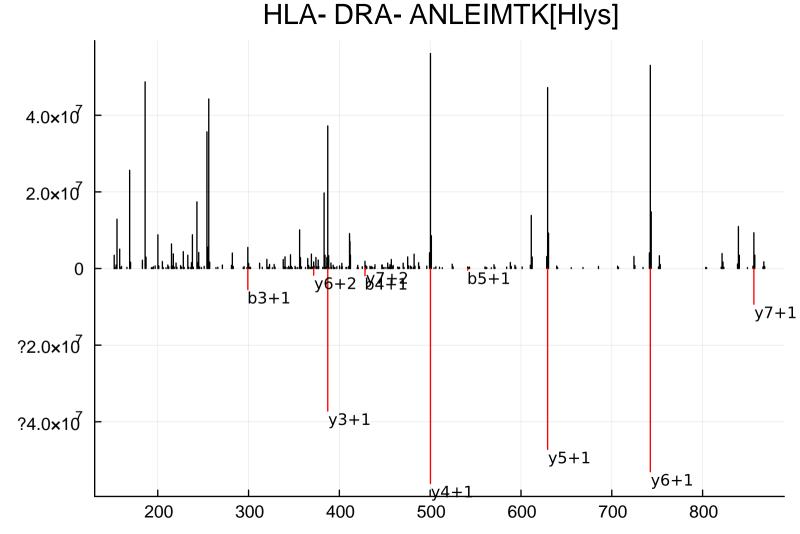


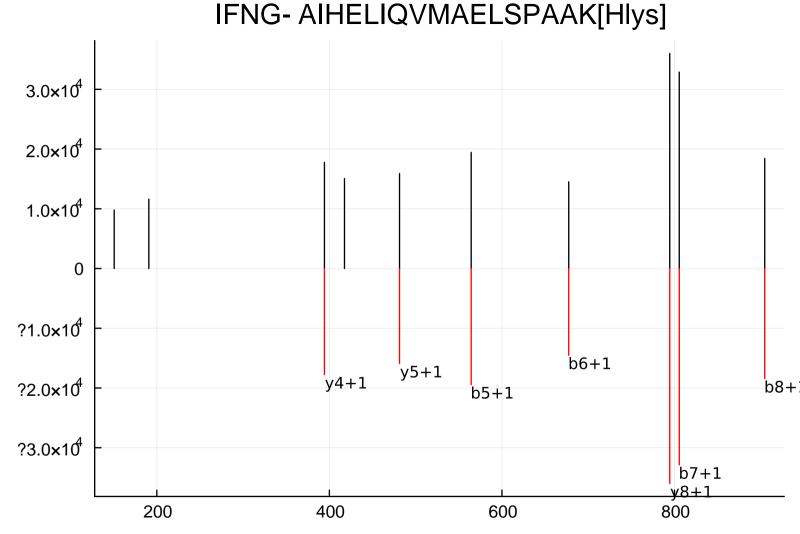


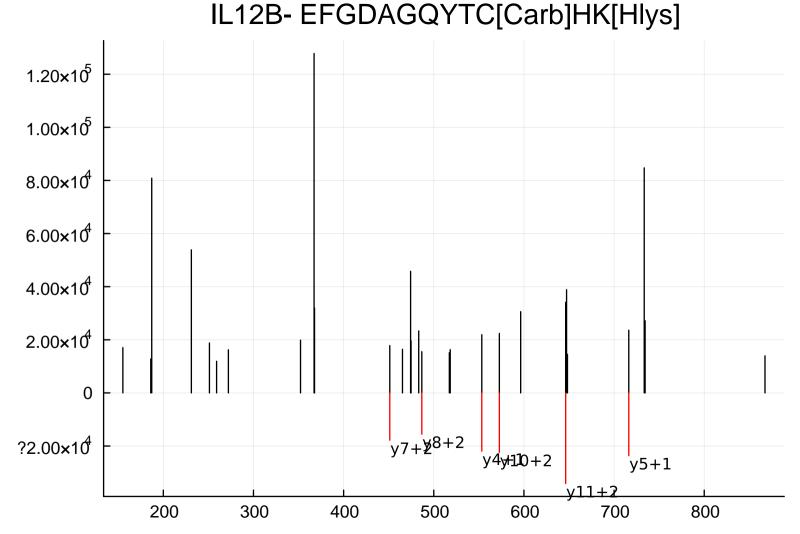


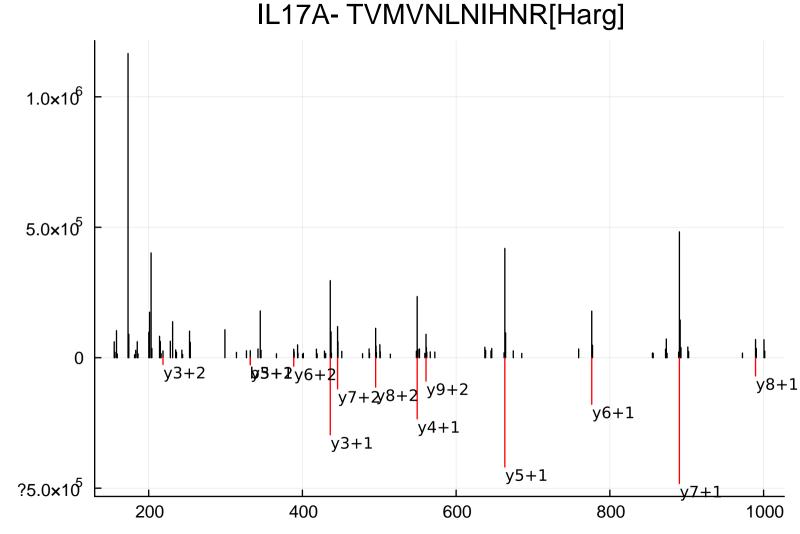


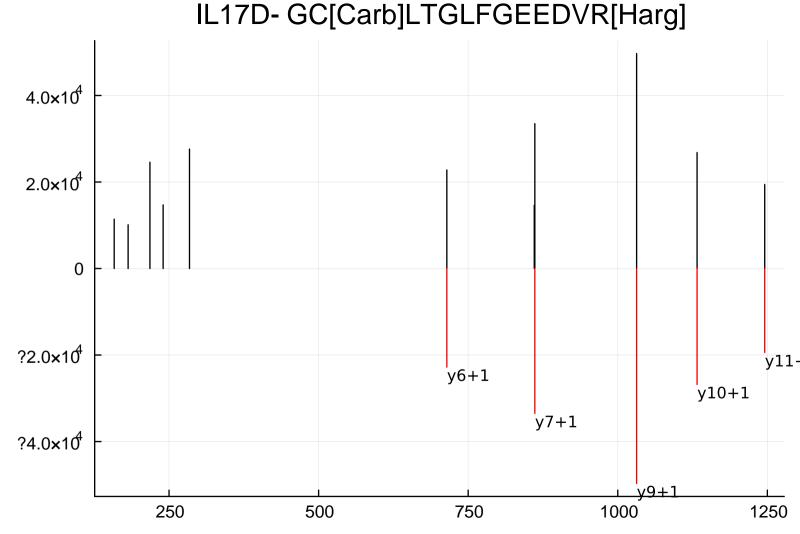


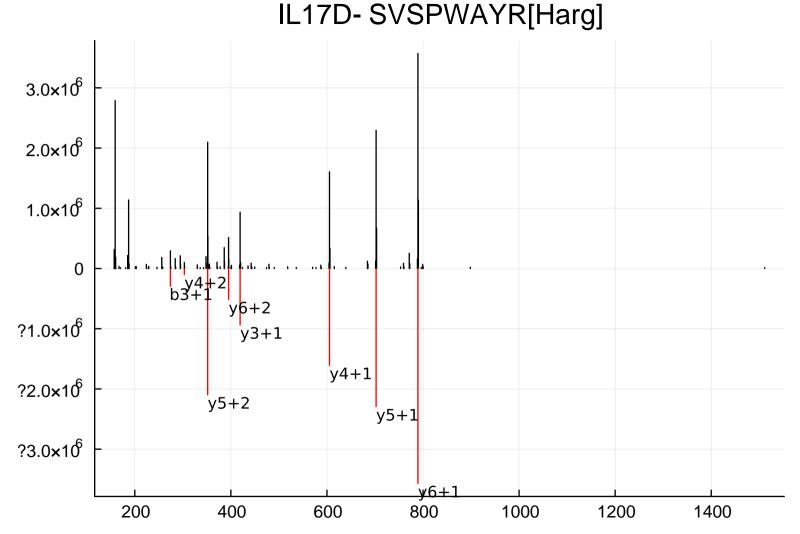


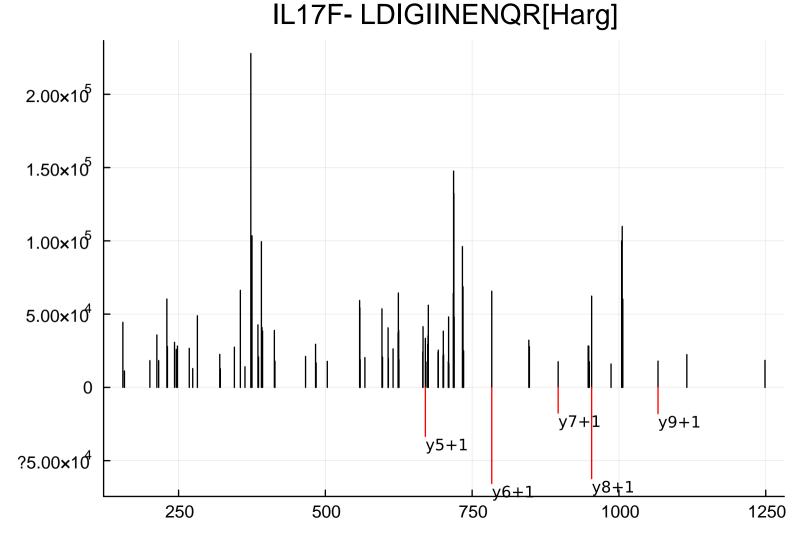


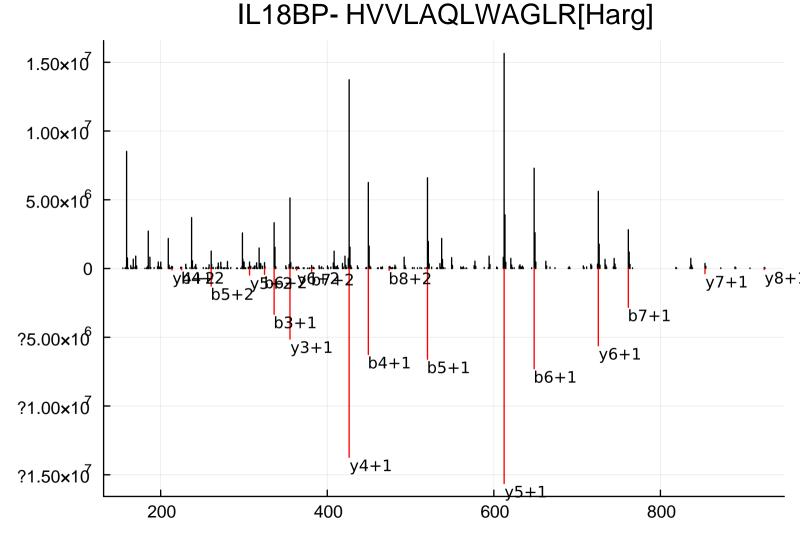


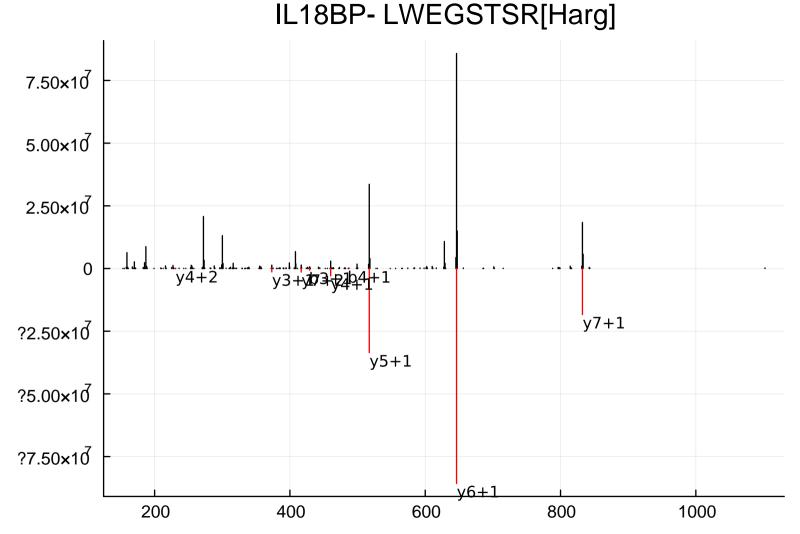


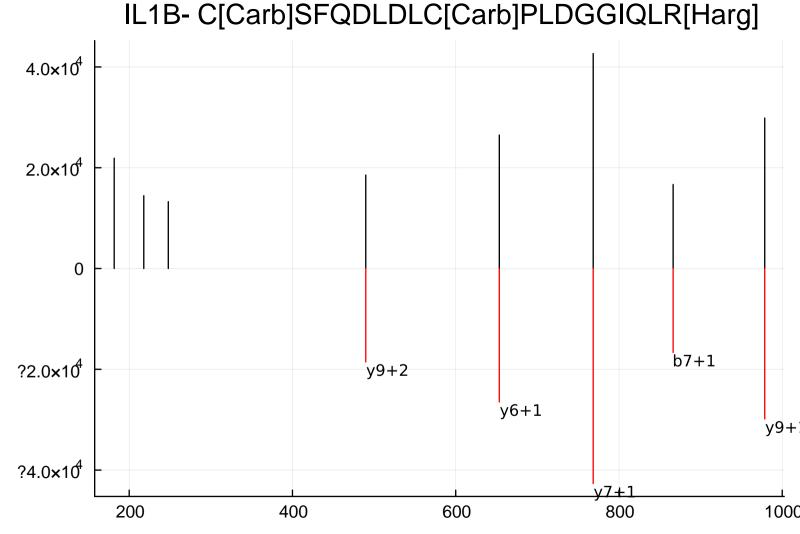


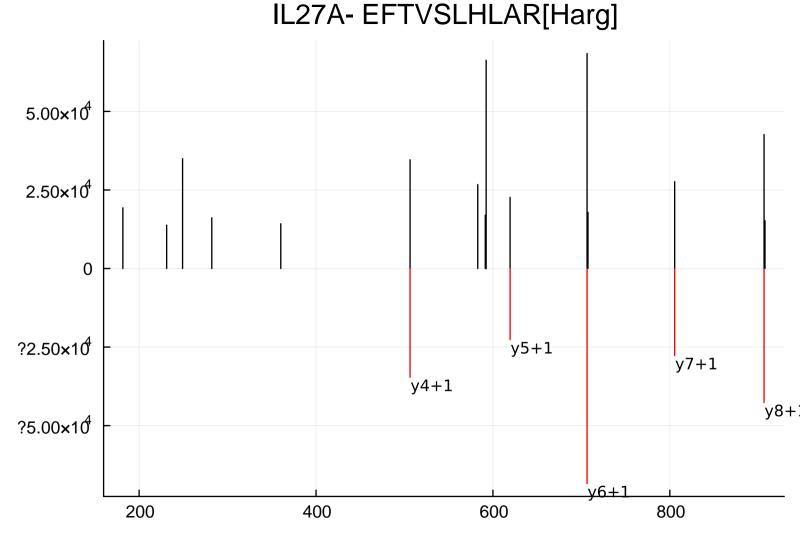


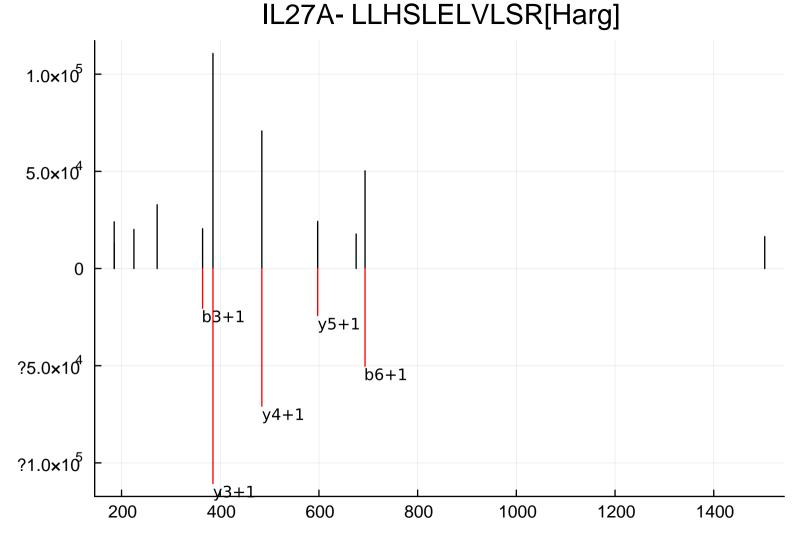




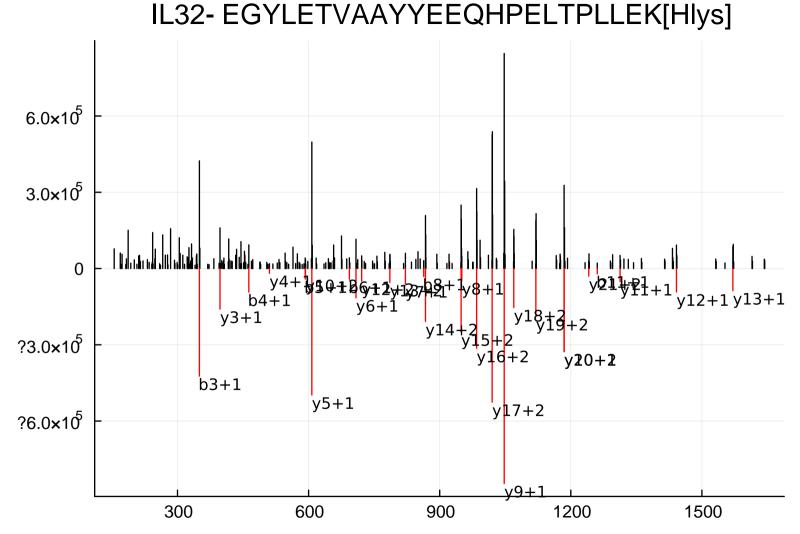




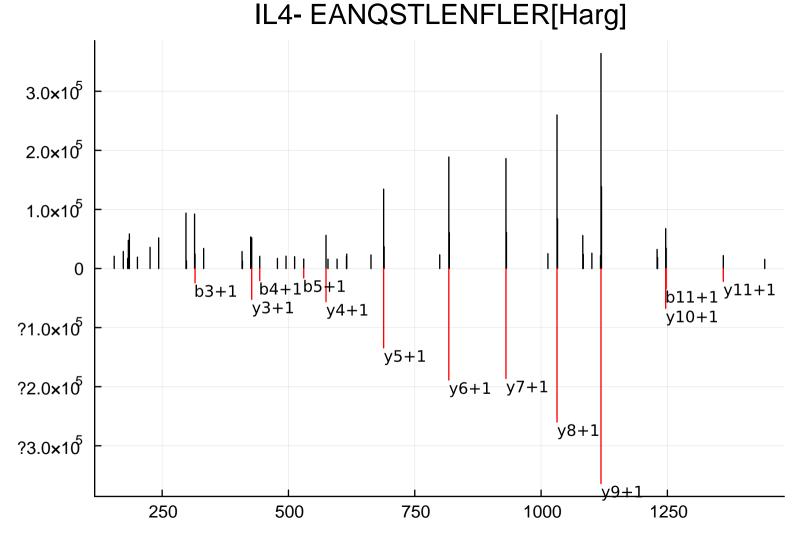


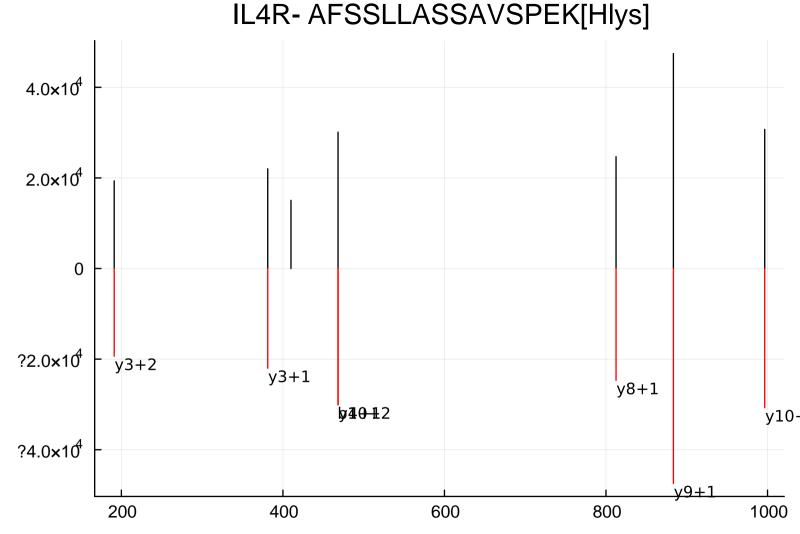


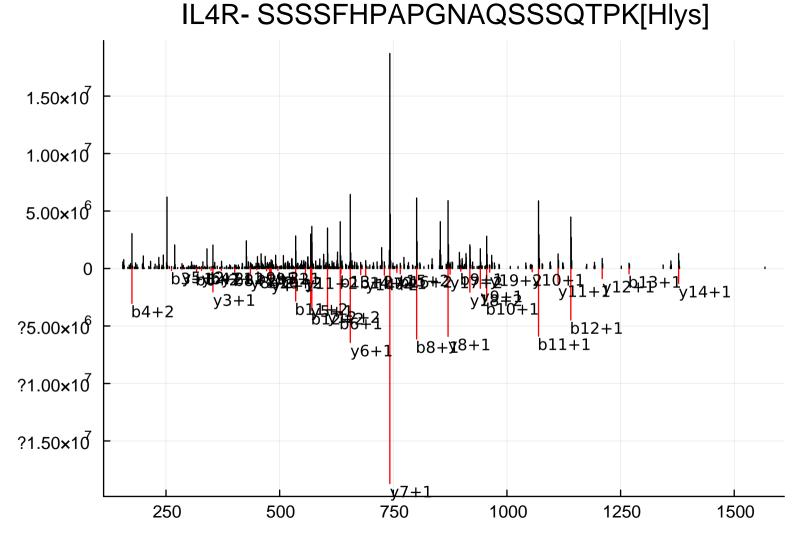
## IL2RG- NLEDLVTEYHGNFSAWSGVSK[Hlys] 4.0×10<sup>6</sup> 2.0×10<sup>6</sup> y12+1 ?2.0×10<sup>6</sup> y15 + 2v19+2 300 600 900 1200 1500

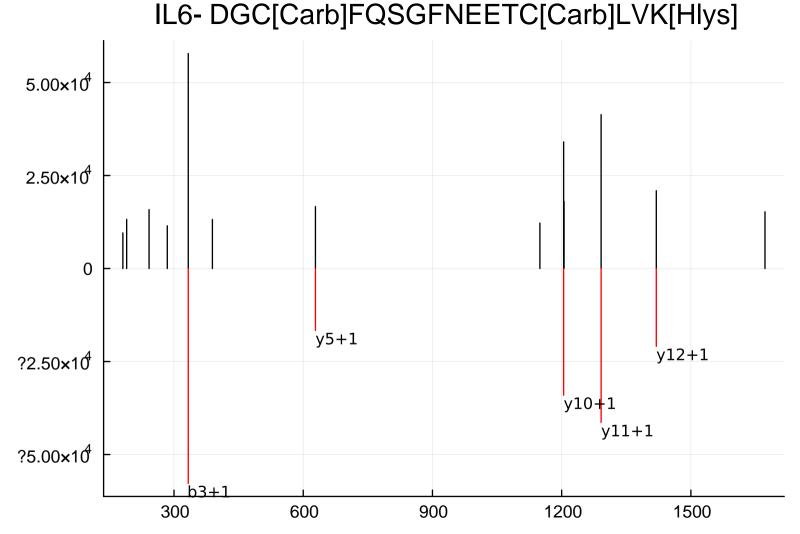


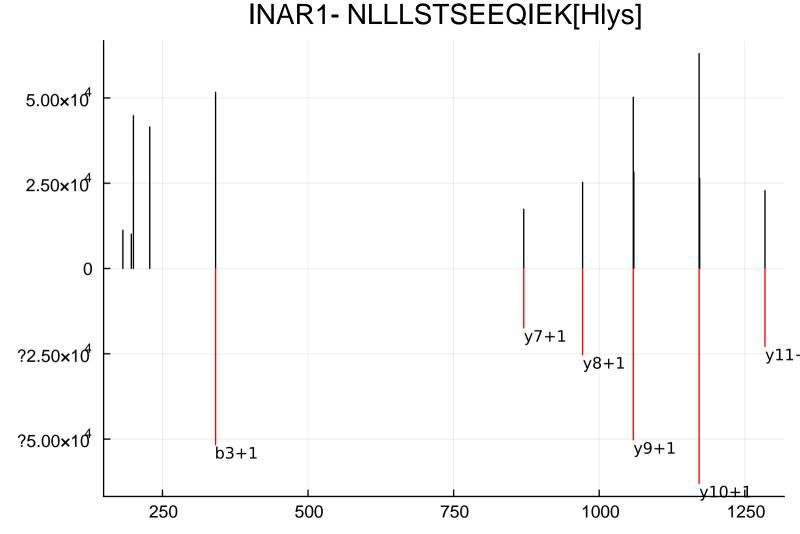
## IL32- GQVMSSLAELEDDFK[Hlys] $4.0 \times 10^{5}$ 3.0×10<sup>5</sup> 2.0×10<sup>5</sup> 1.0×10<sup>5</sup> 0 b4+1 y3+1 b12+1 y4+1 ?1.0×10<sup>5</sup> 99 + 1y10 + 1y8+1 y12+1 ?2.0×10<sup>5</sup> b3+1 200 400 600 800 1000 1200 1400

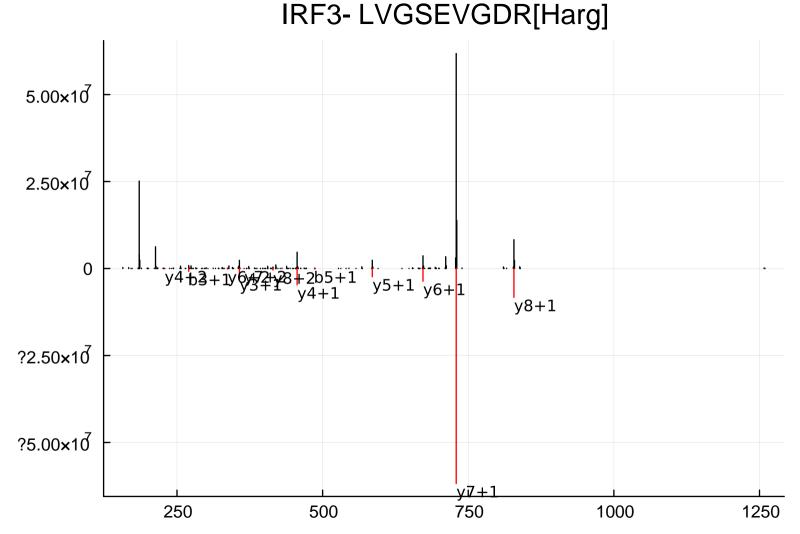


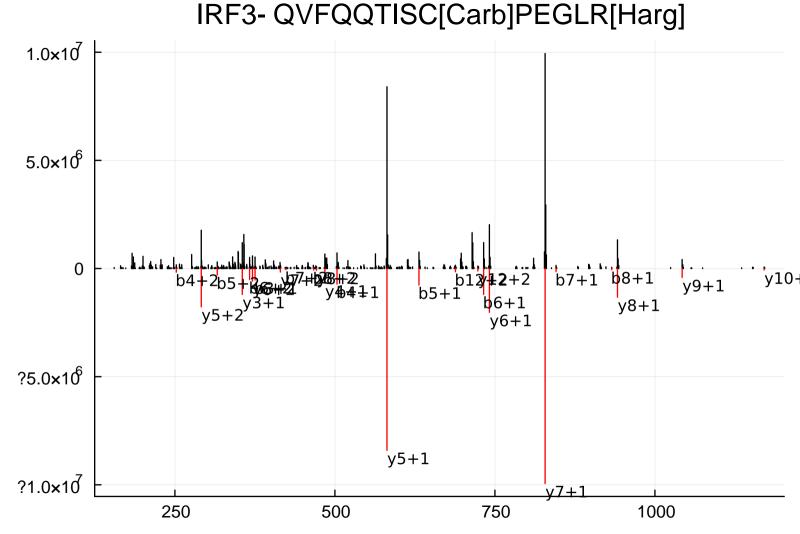


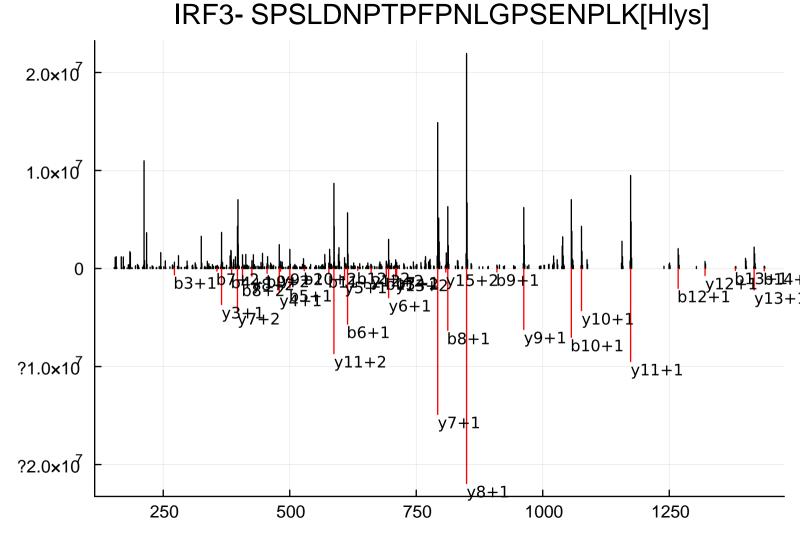


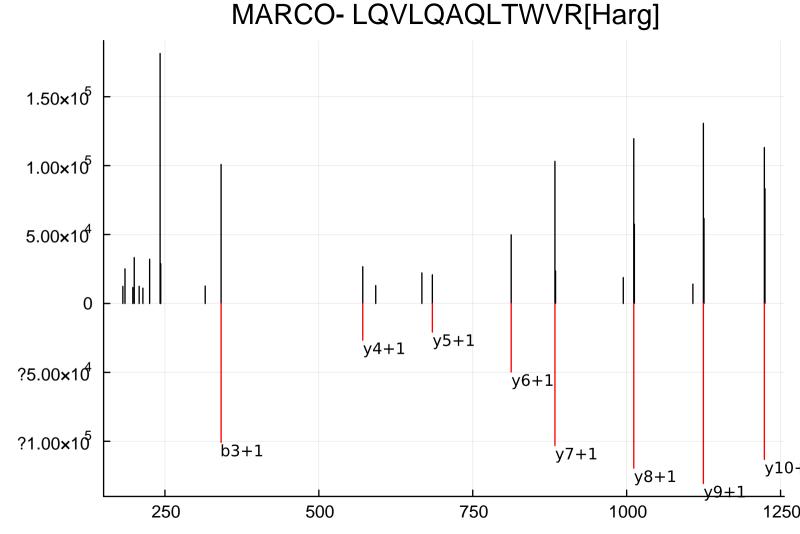


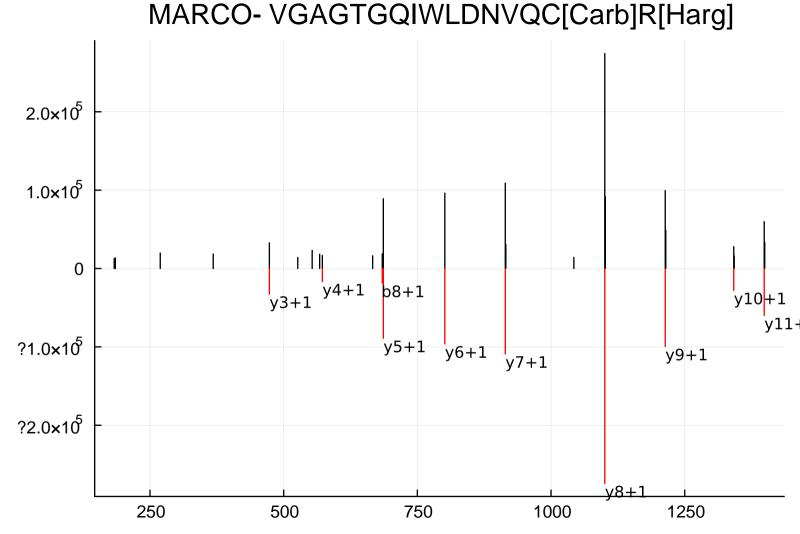


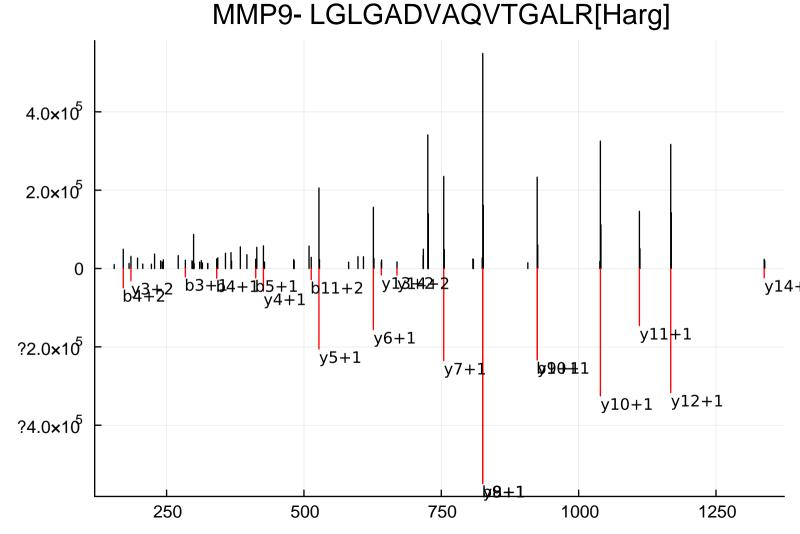


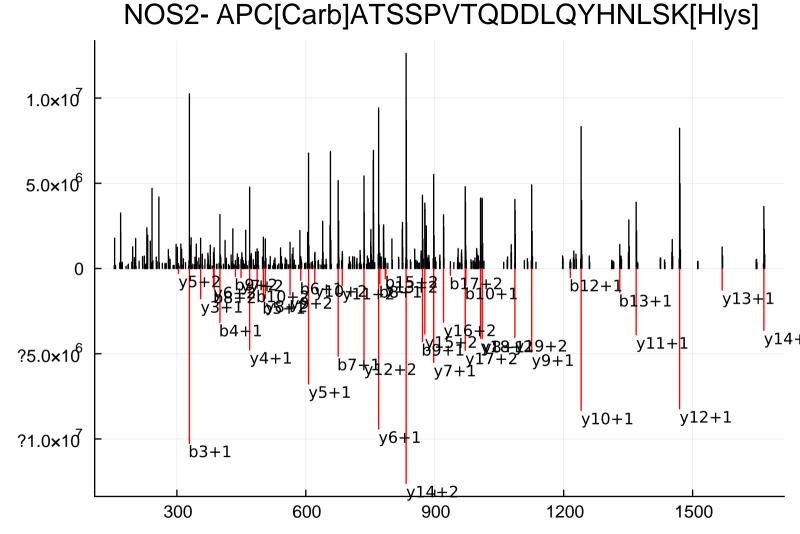


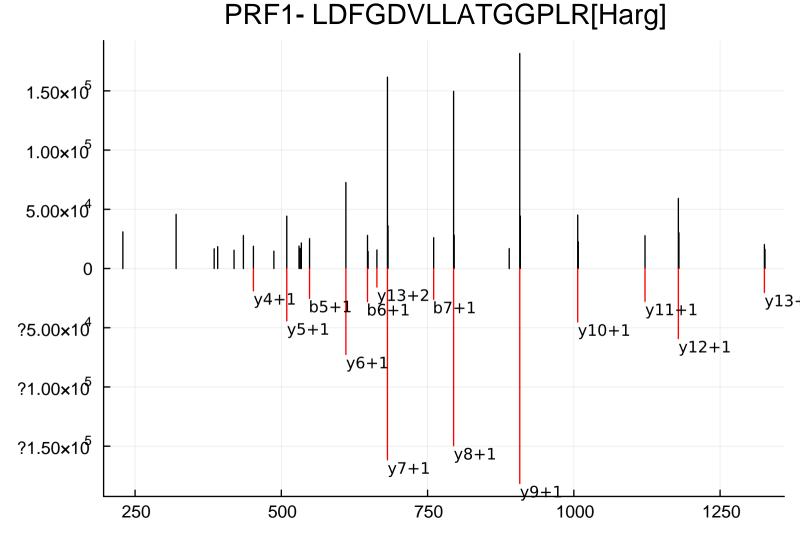




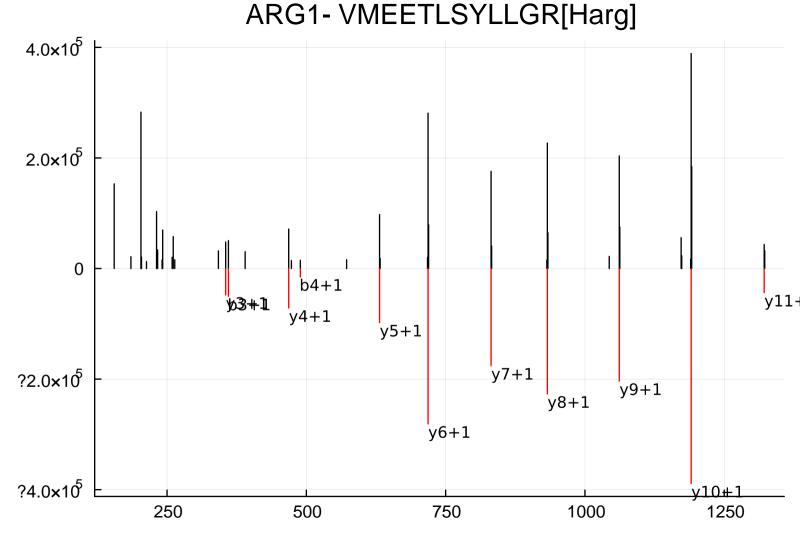


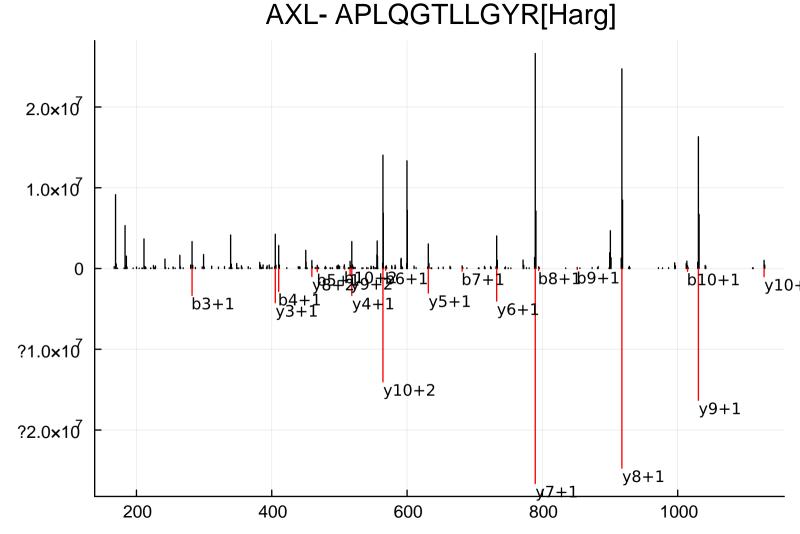


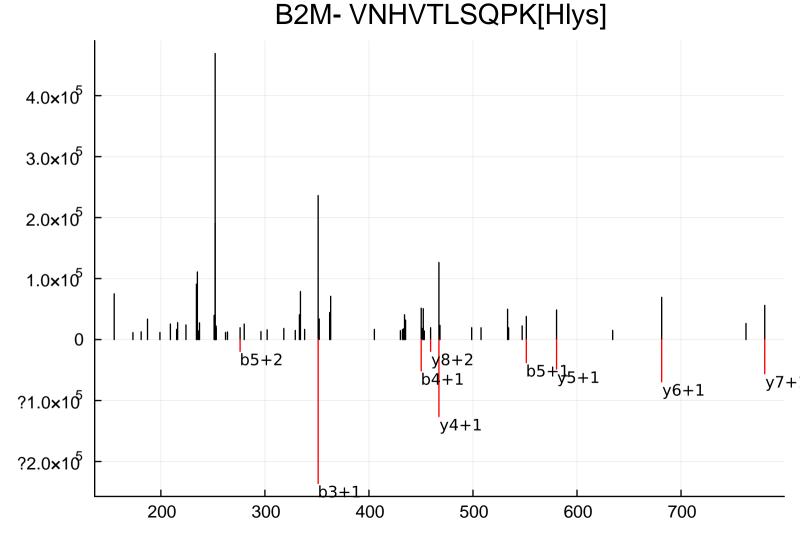


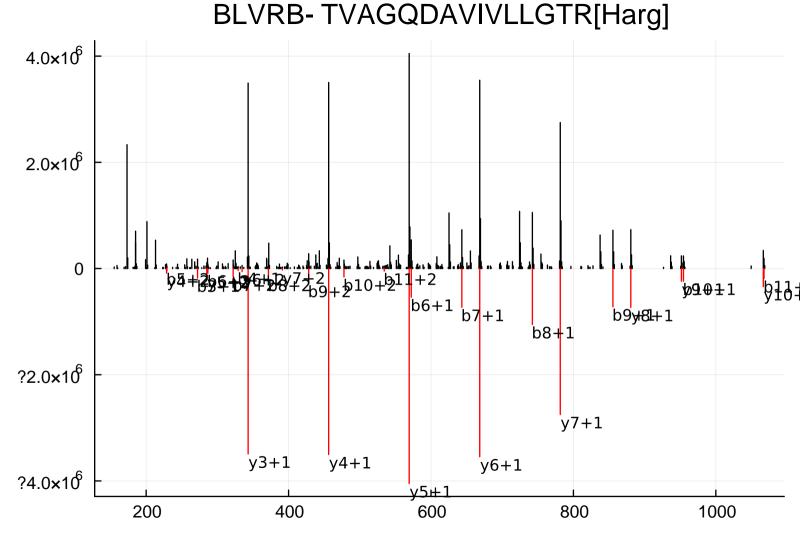


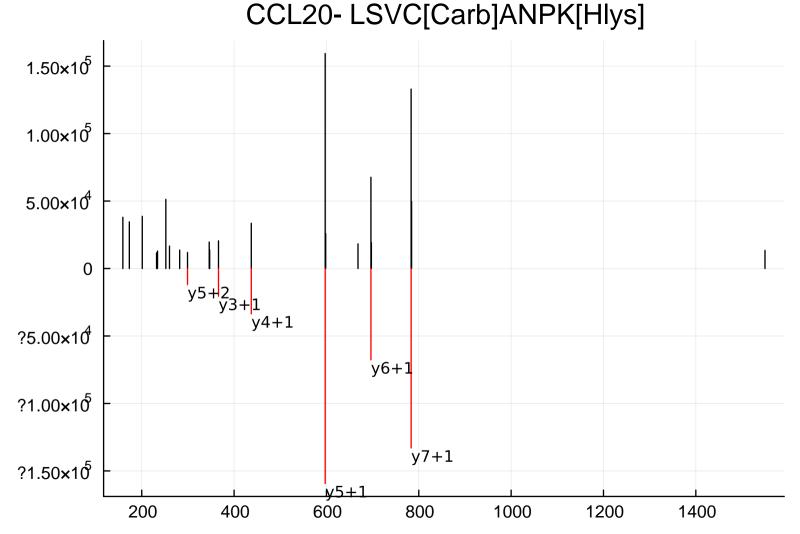
## SIGLEC1- LVASTLQGVGGPEGSSPR[Harg] 2.0×10<sup>7</sup> 1.0×10<sup>7</sup> 0 y11+1 ?1.0×10<sup>7</sup> y5+1 y7+2 ?2.0×10<sup>7</sup> 250 500 750 1000 1250

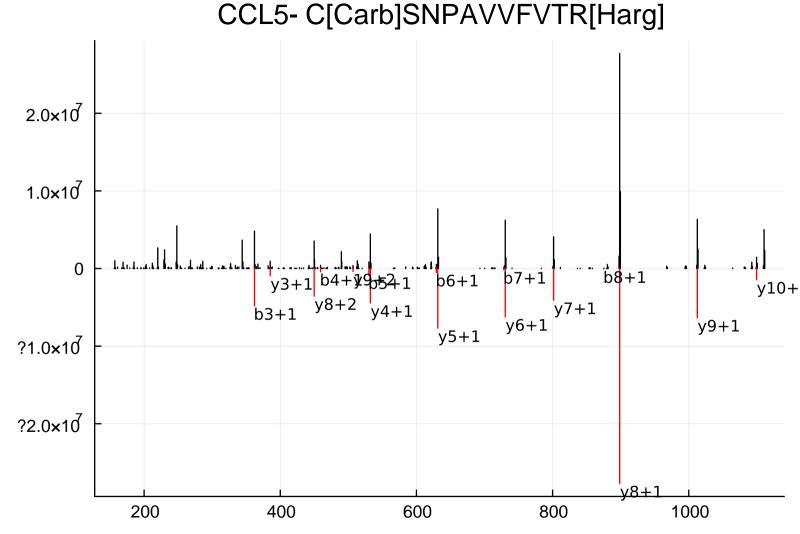


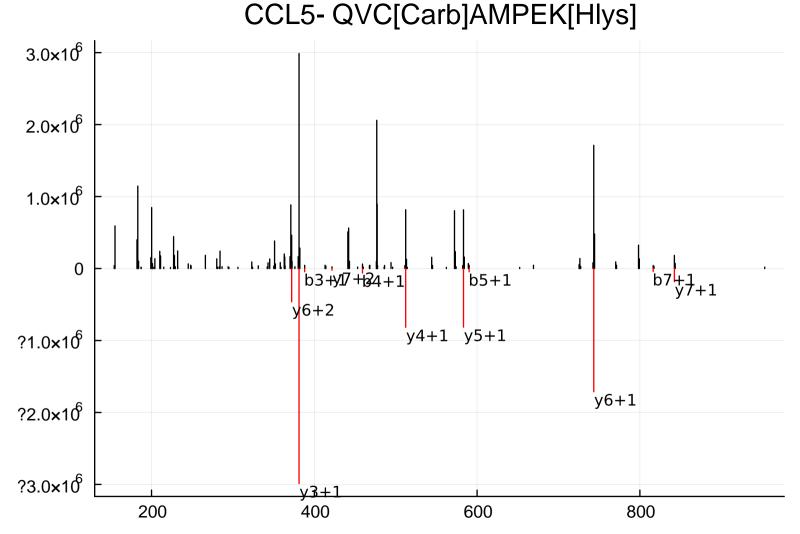


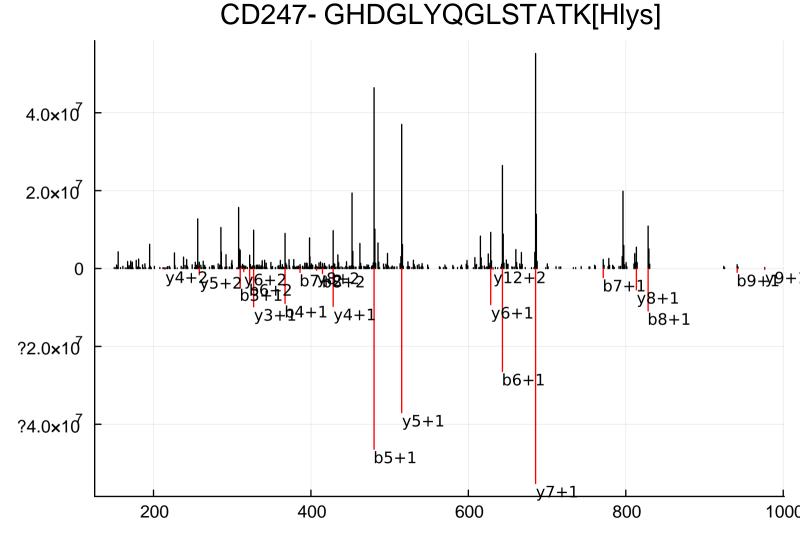


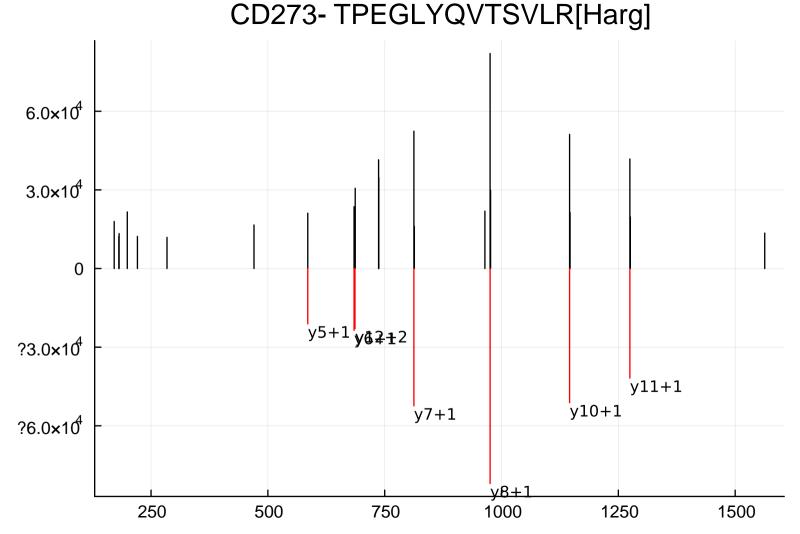


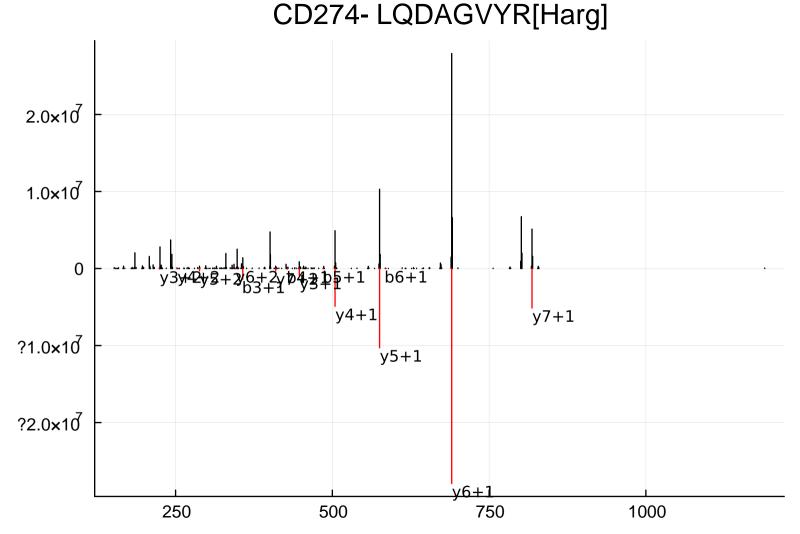


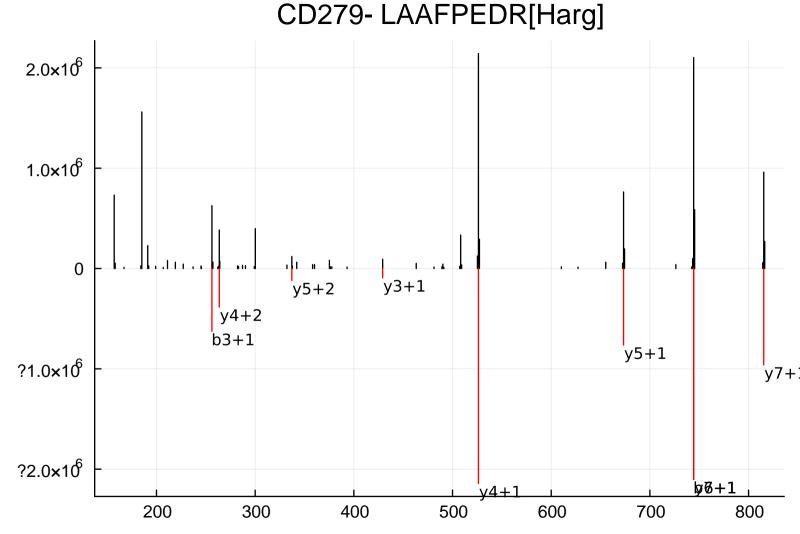


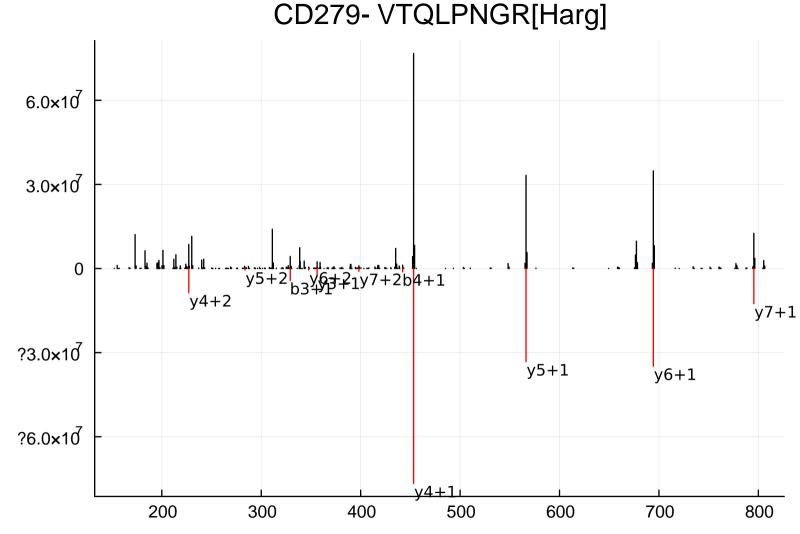


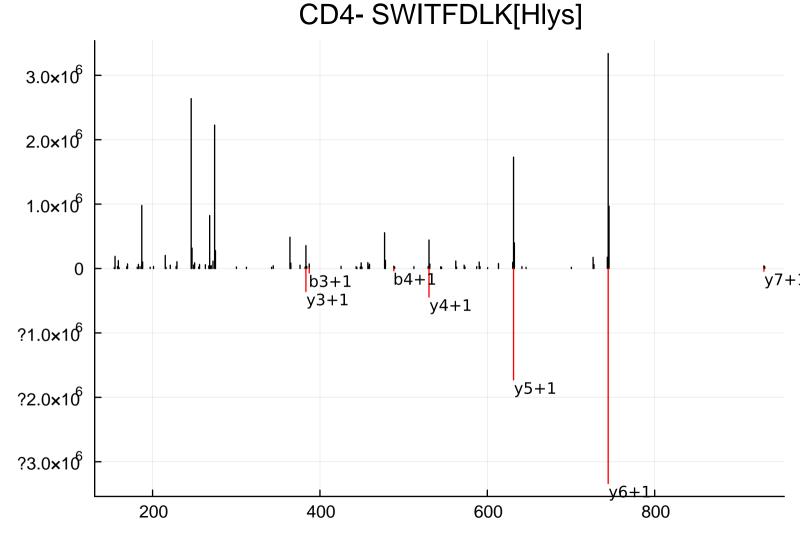


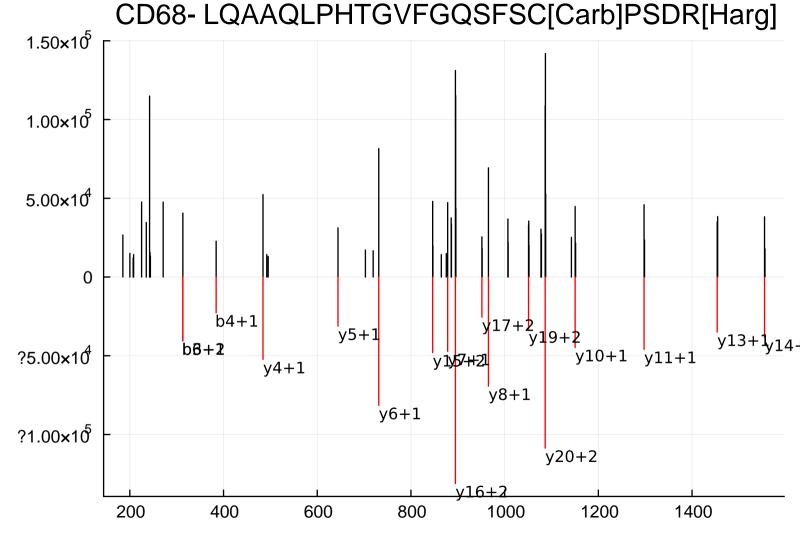


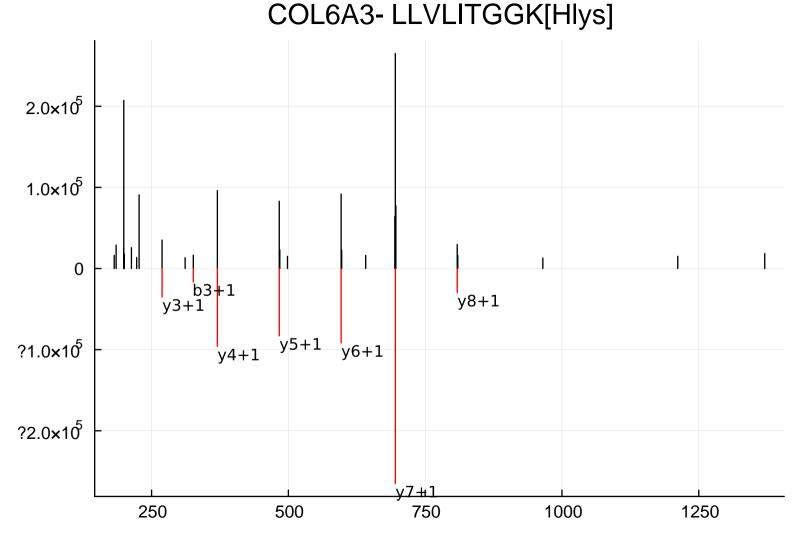


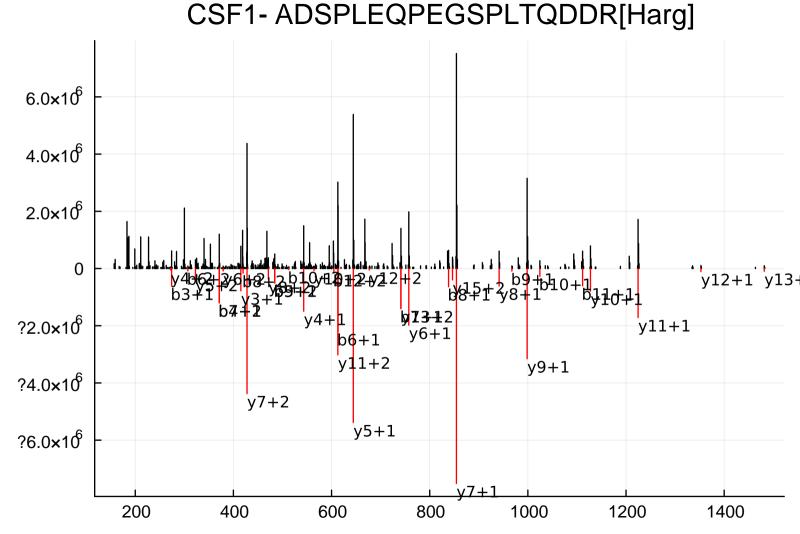




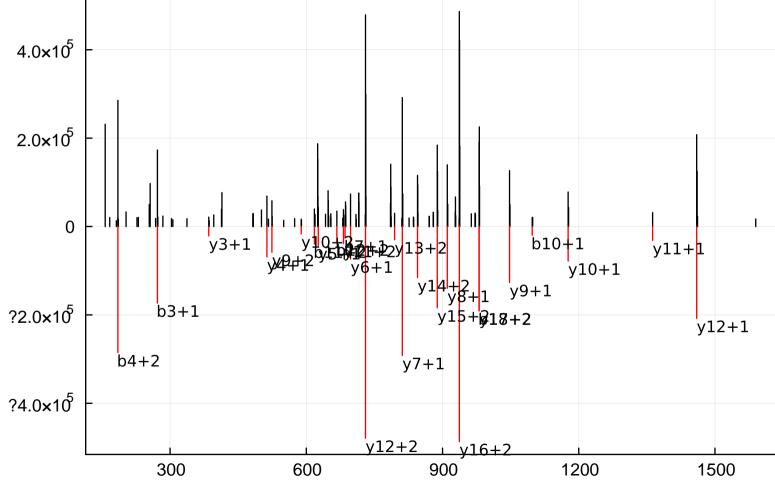


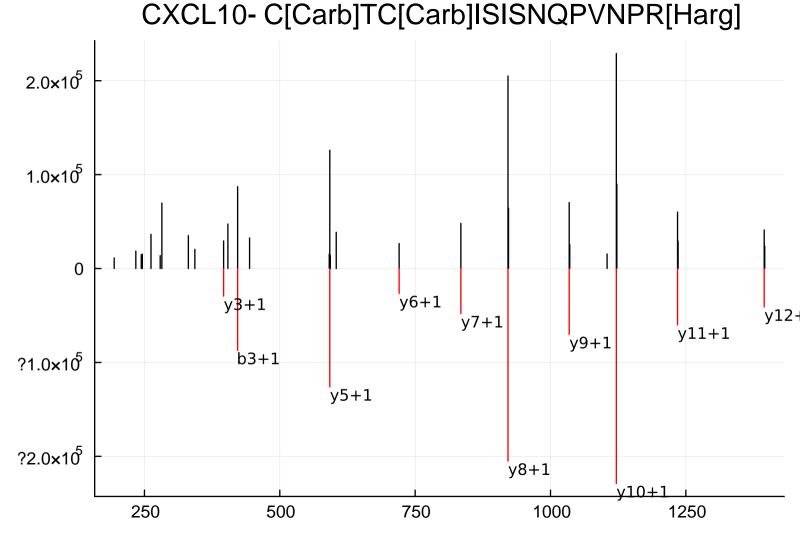


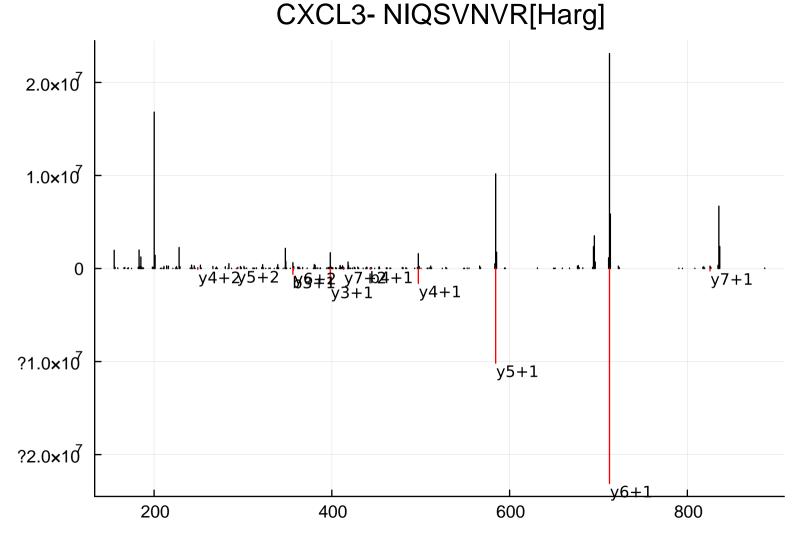


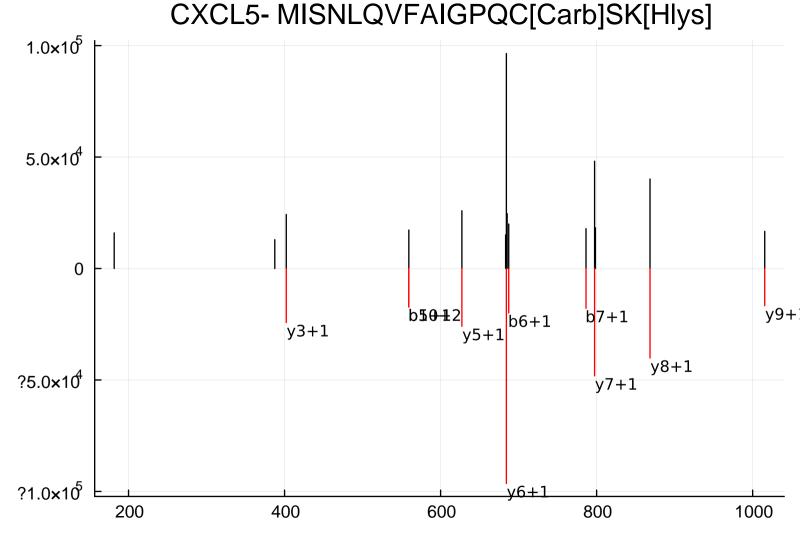


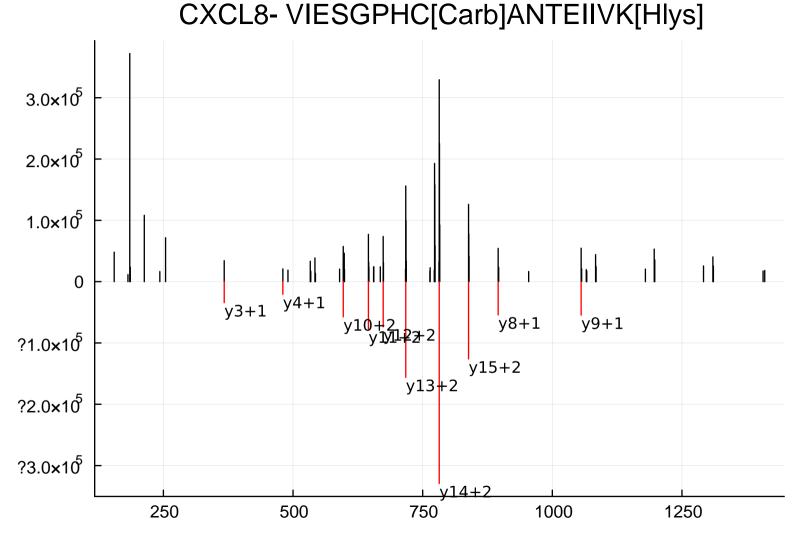
## CSF2- SPSPSTQPWEHVNAIQEAR[Harg]

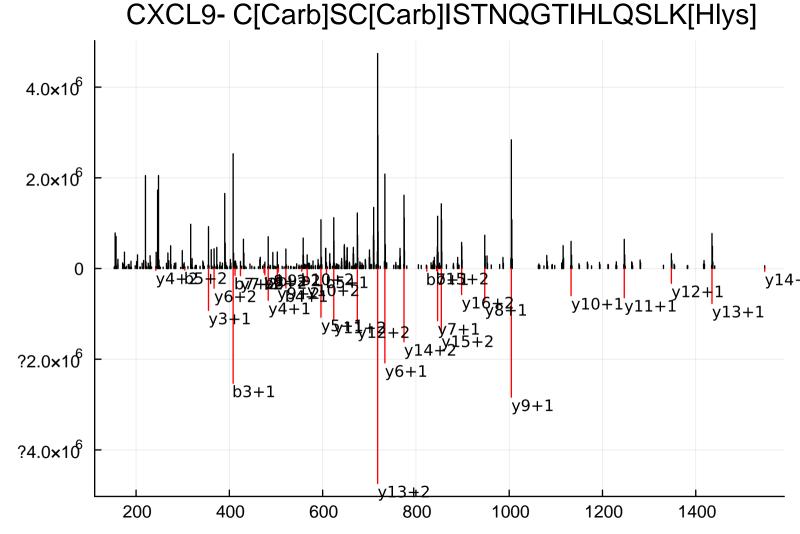




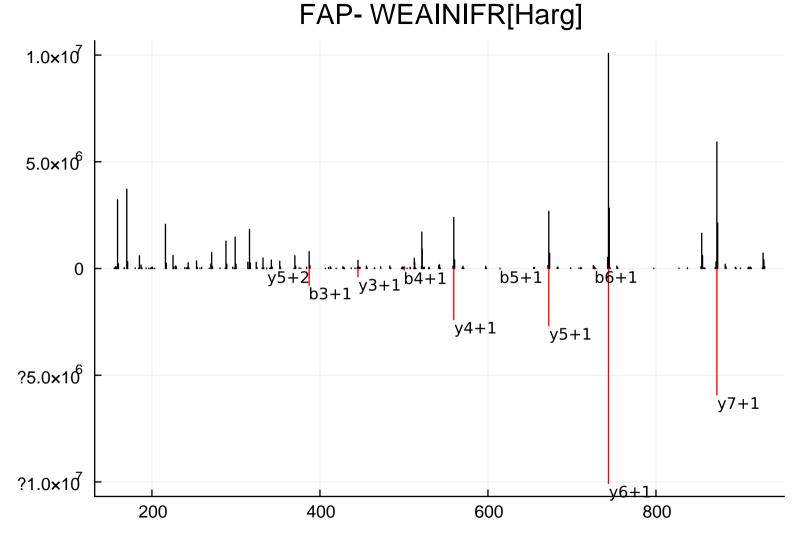


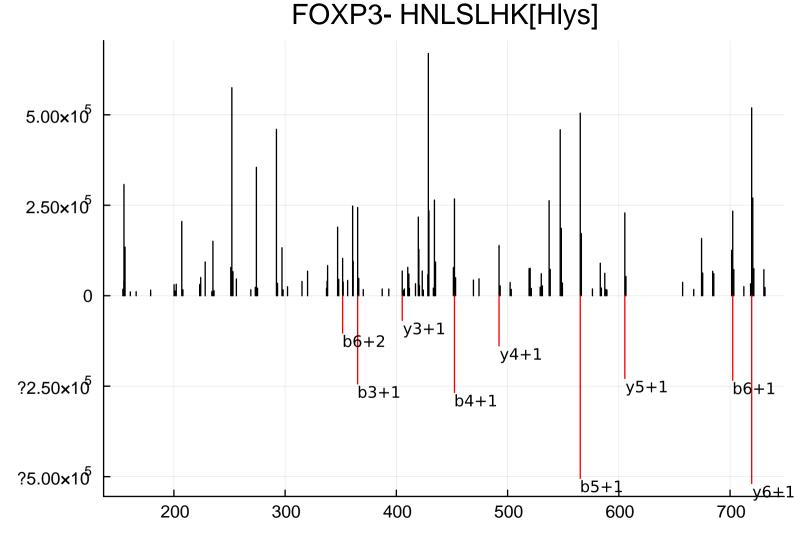


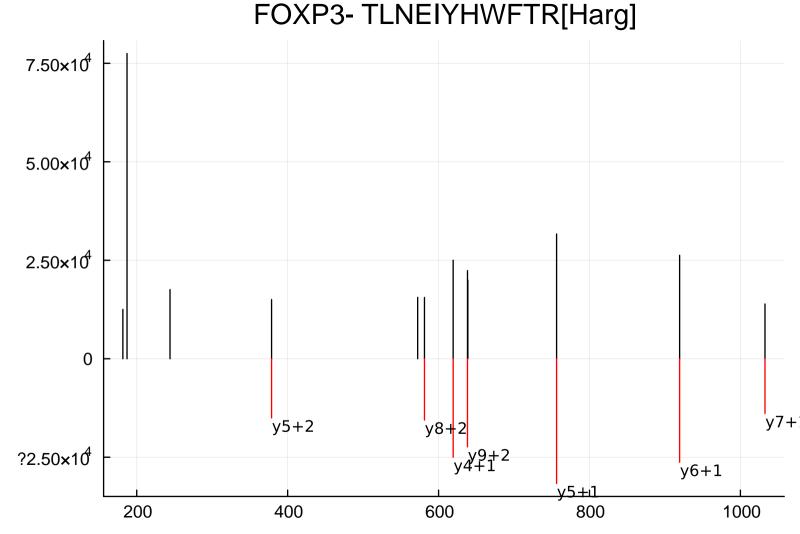


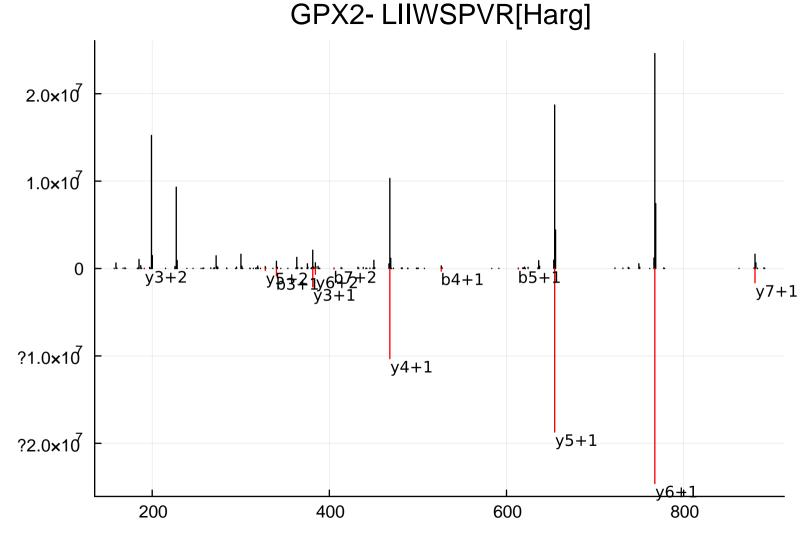


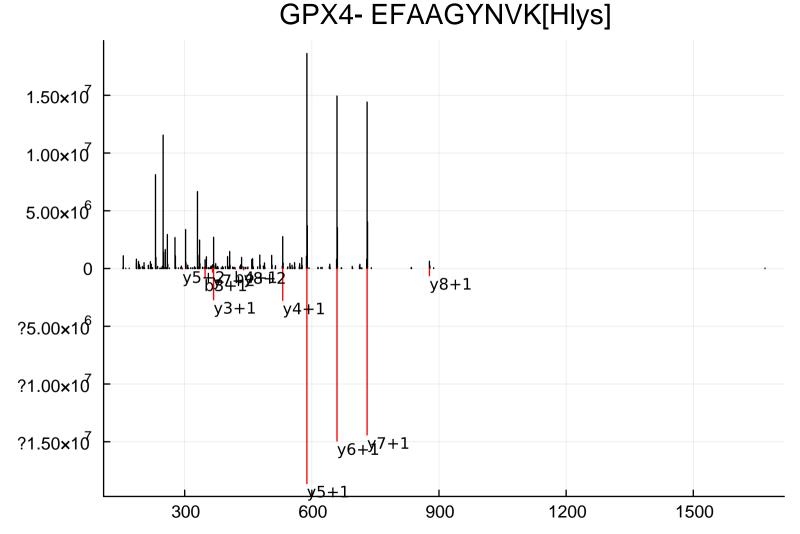
E7- LC[Carb]VQSTHVDIR[Harg] 1.50×10<sup>6</sup> 1.00×10<sup>6</sup> 5.00×10<sup>5</sup> 200 400 600 800 1000

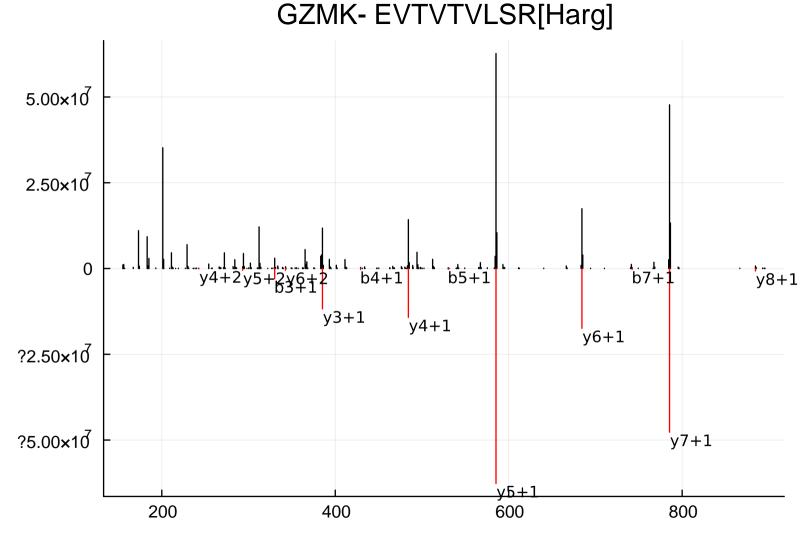


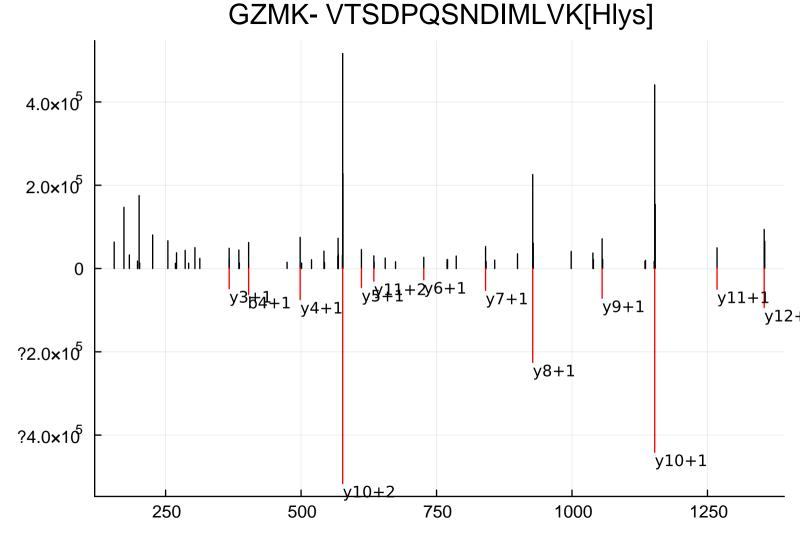


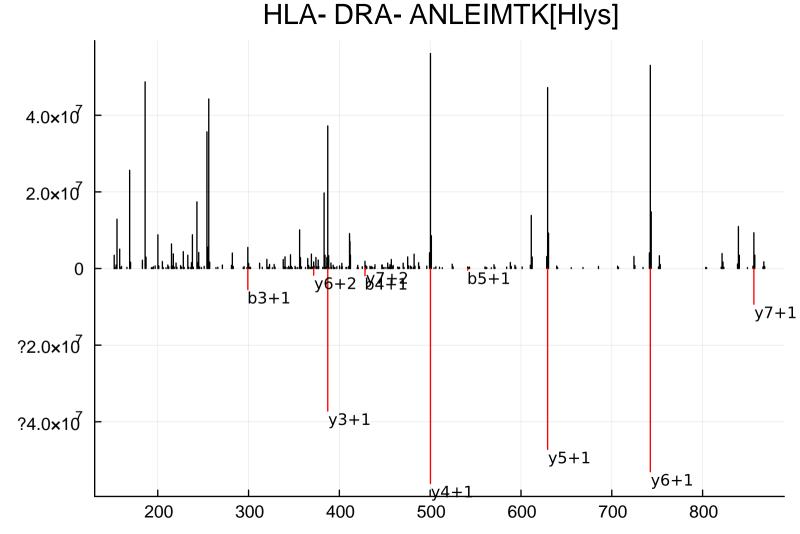


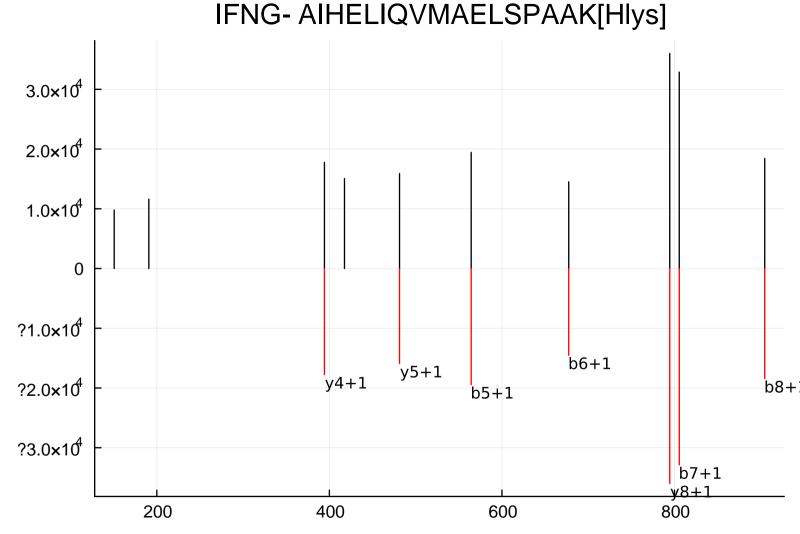


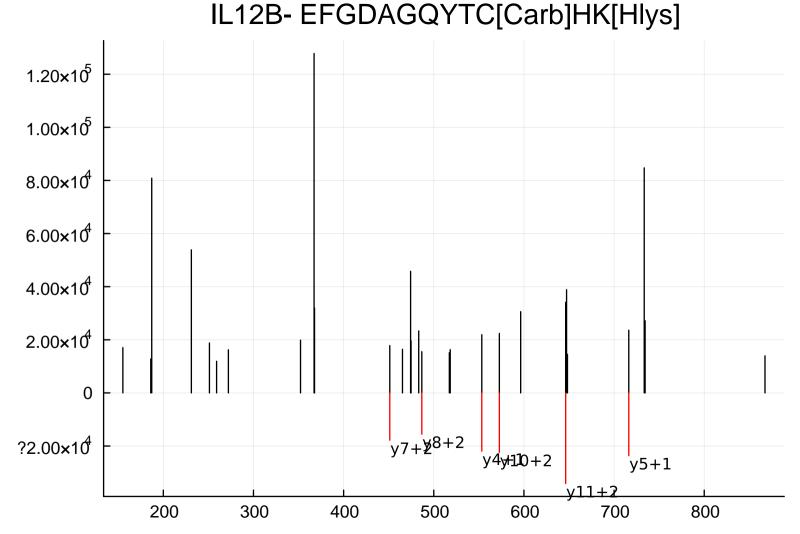


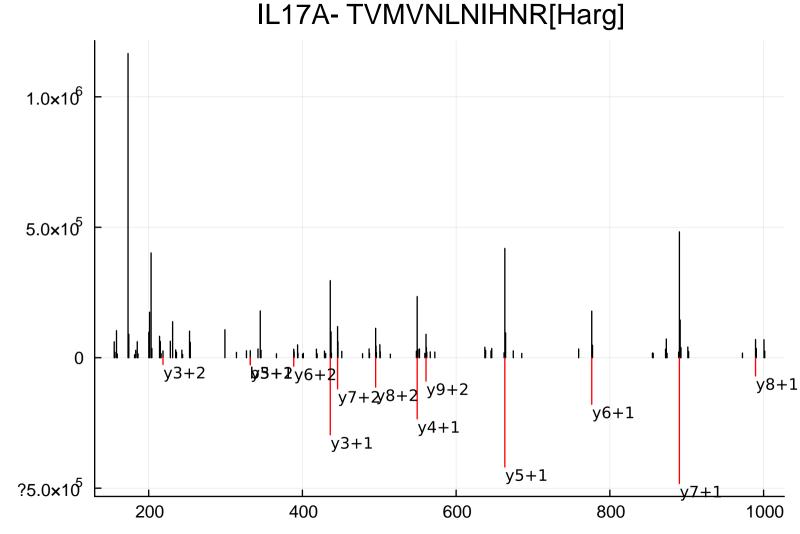


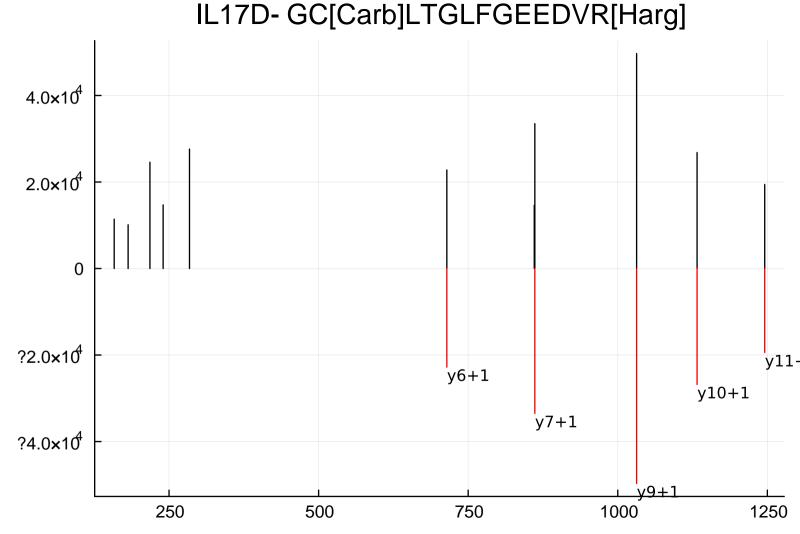


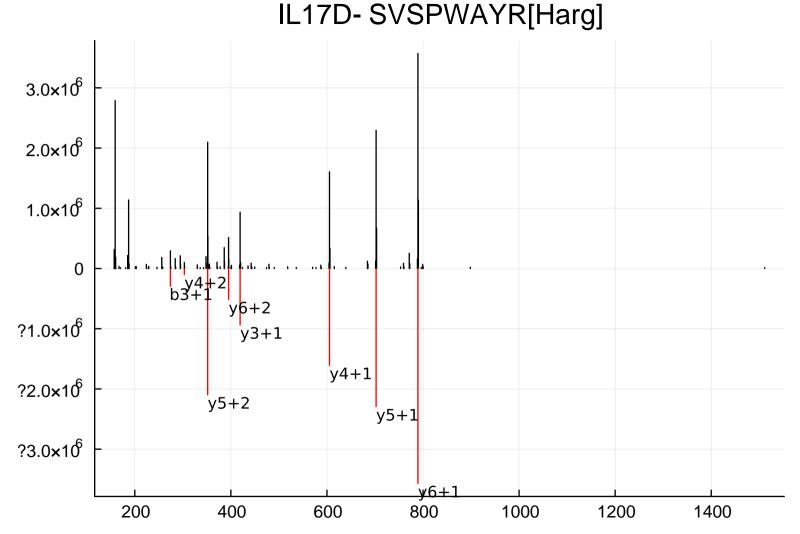


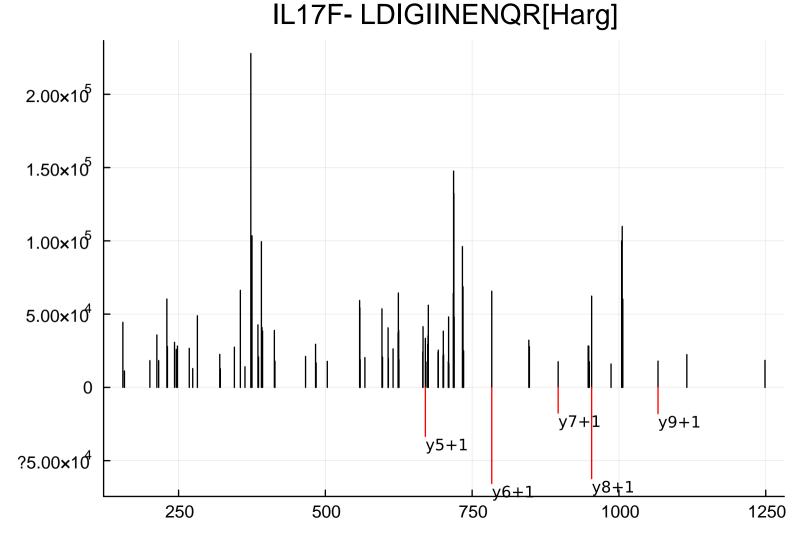


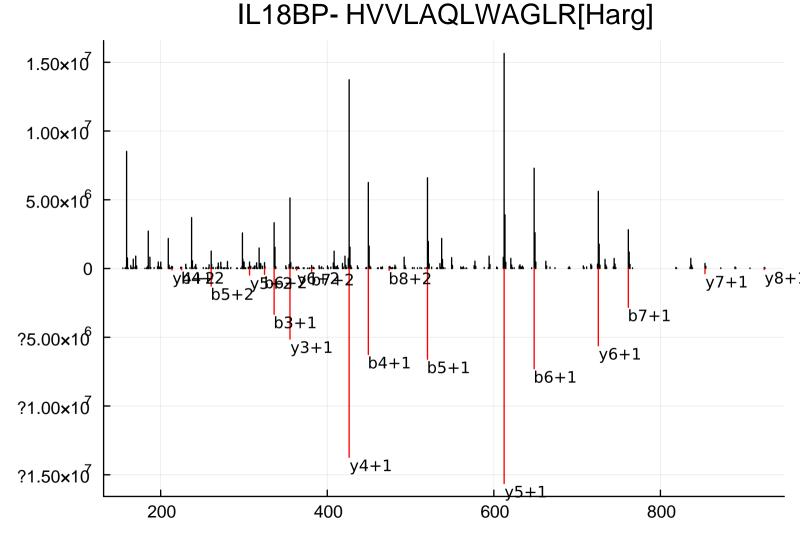


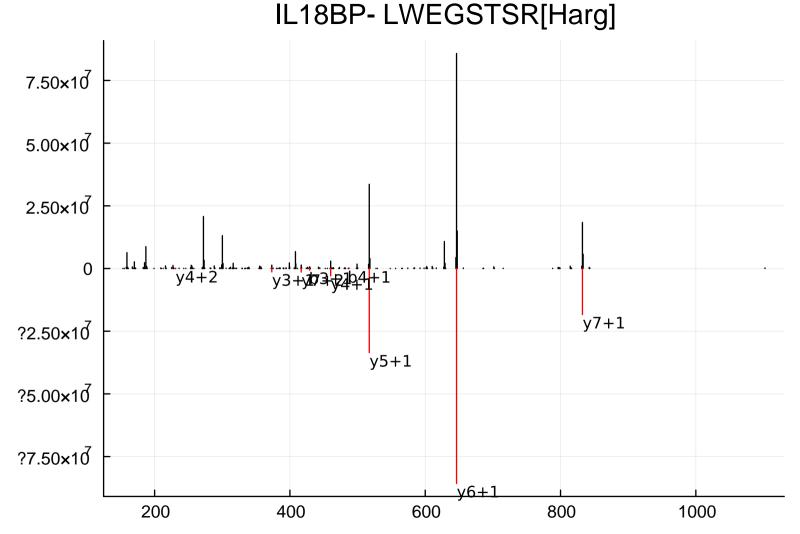


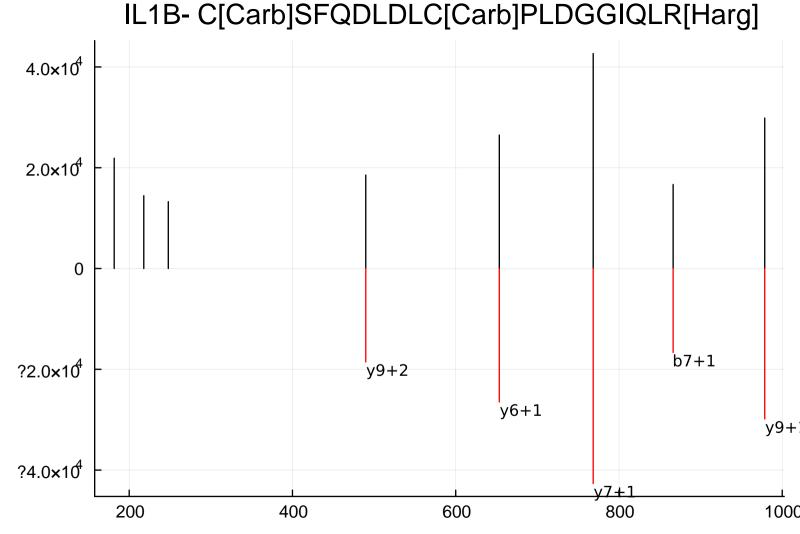


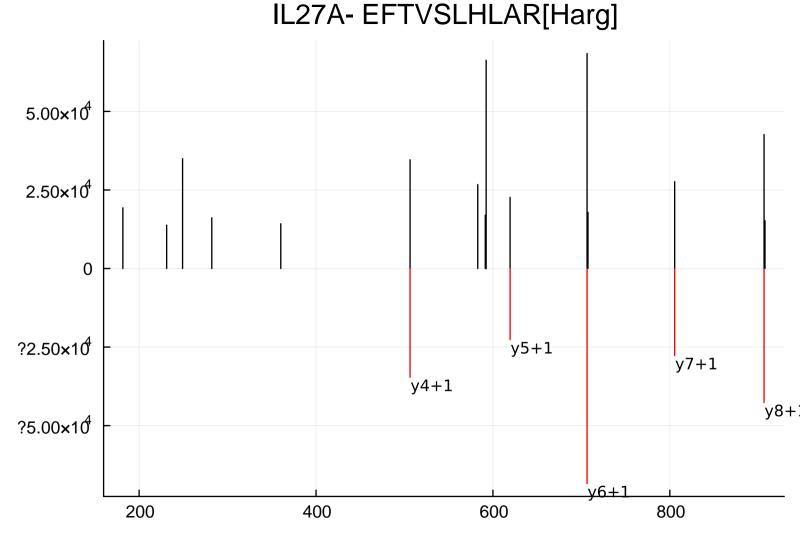


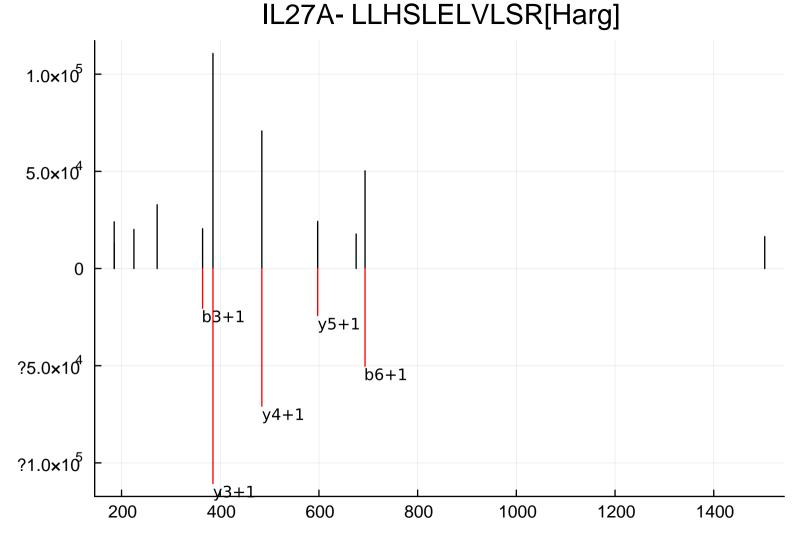




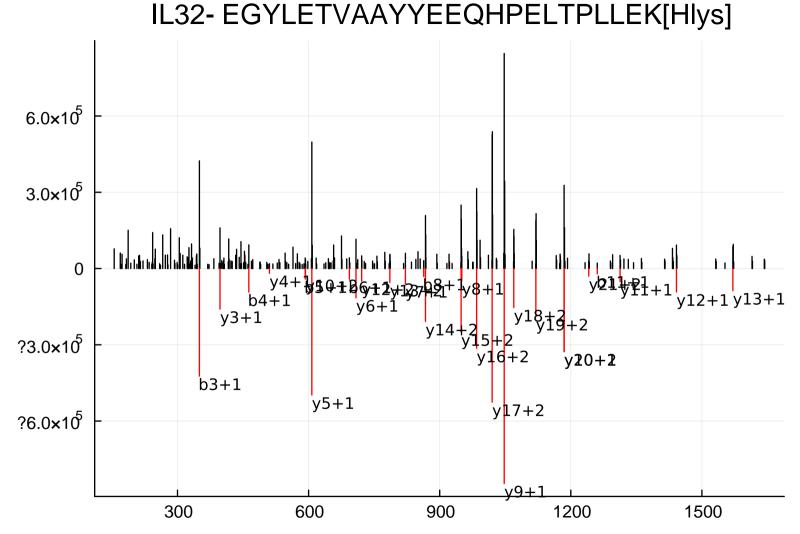




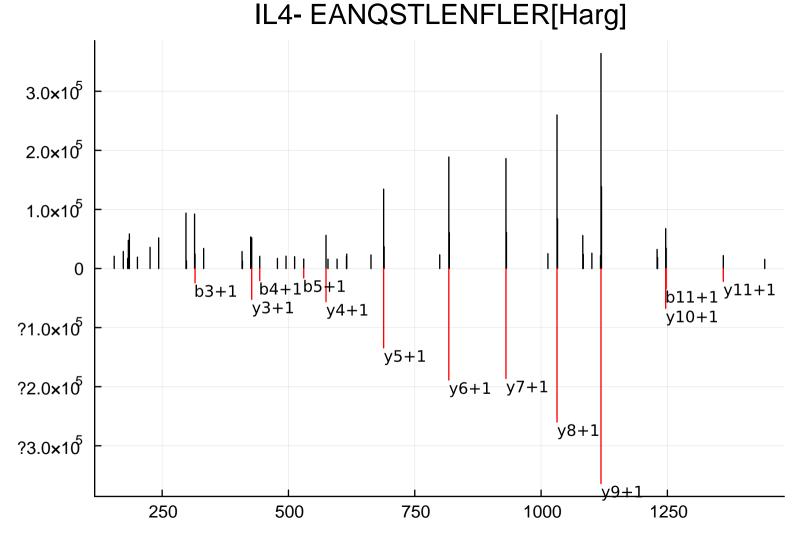


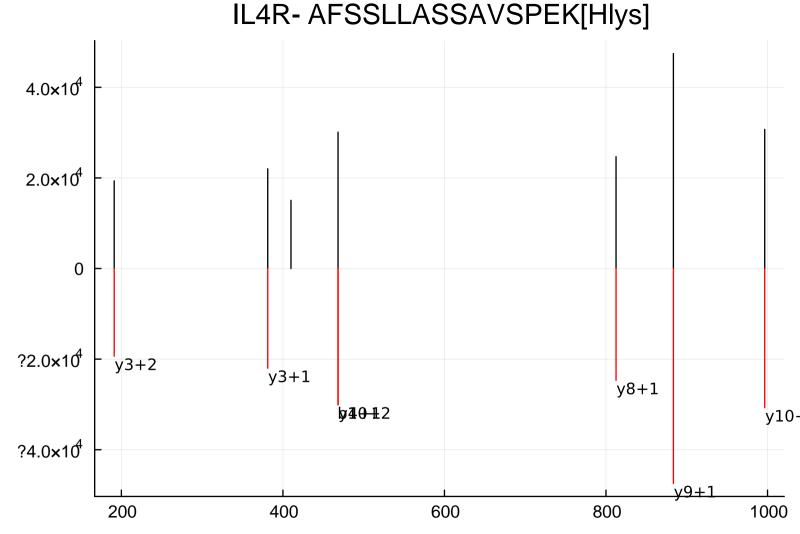


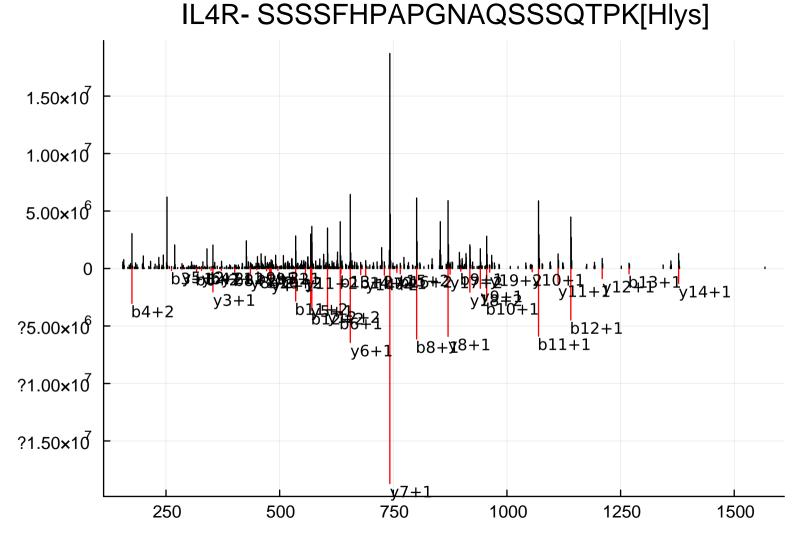
## IL2RG- NLEDLVTEYHGNFSAWSGVSK[Hlys] 4.0×10<sup>6</sup> 2.0×10<sup>6</sup> y12+1 ?2.0×10<sup>6</sup> y15 + 2v19+2 300 600 900 1200 1500

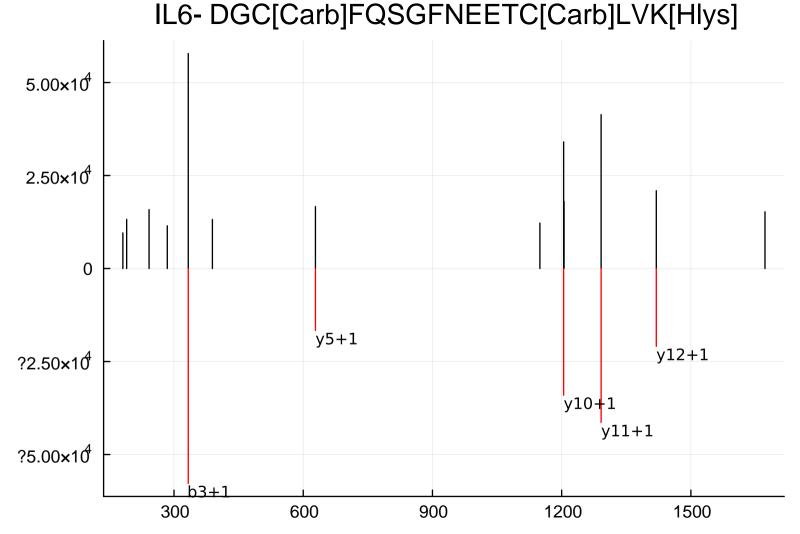


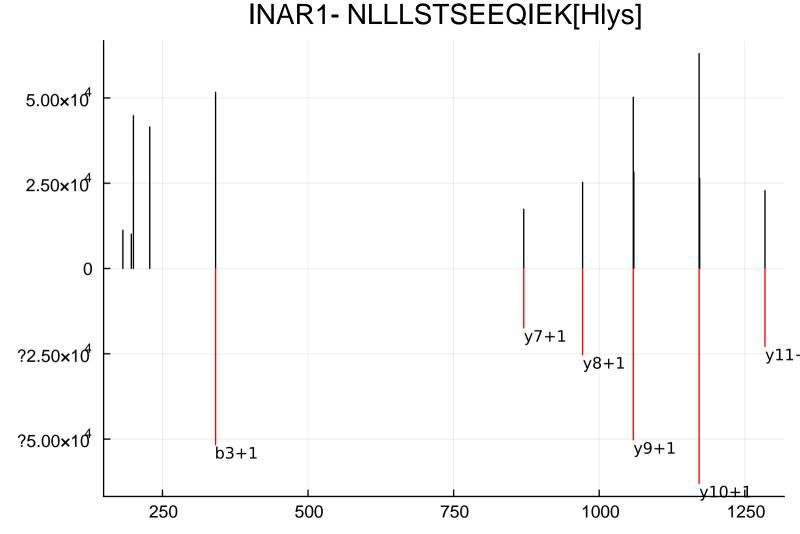
## IL32- GQVMSSLAELEDDFK[Hlys] $4.0 \times 10^{5}$ 3.0×10<sup>5</sup> 2.0×10<sup>5</sup> 1.0×10<sup>5</sup> 0 b4+1 y3+1 b12+1 y4+1 ?1.0×10<sup>5</sup> 99 + 1y10 + 1y8+1 y12+1 ?2.0×10<sup>5</sup> b3+1 200 400 600 800 1000 1200 1400

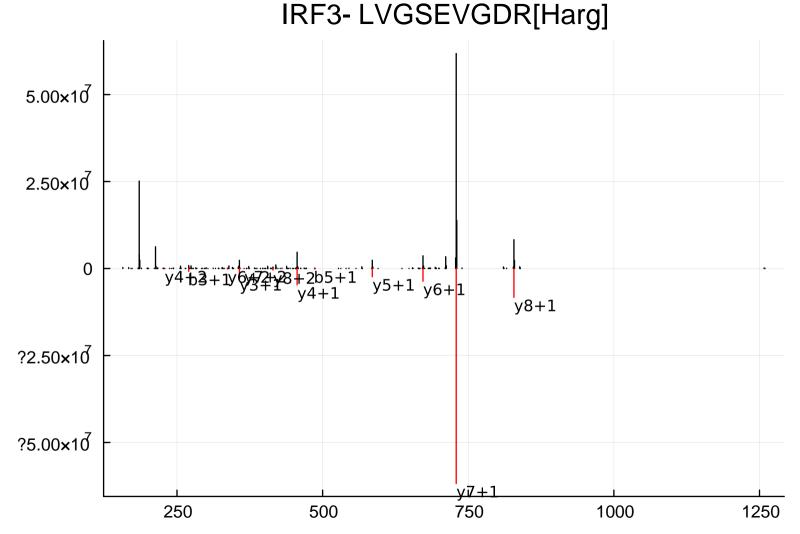


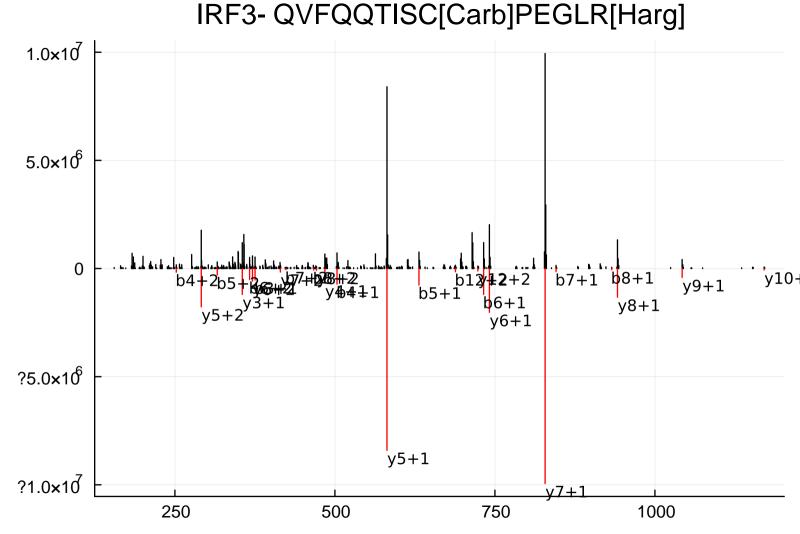


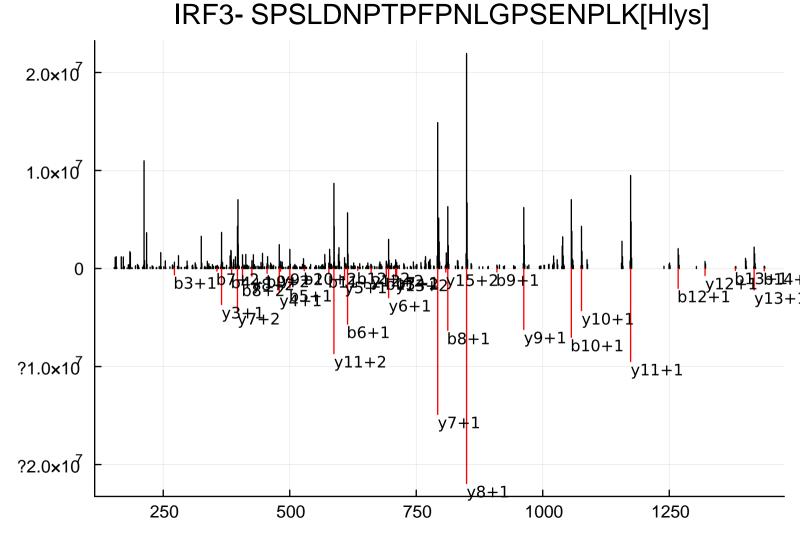


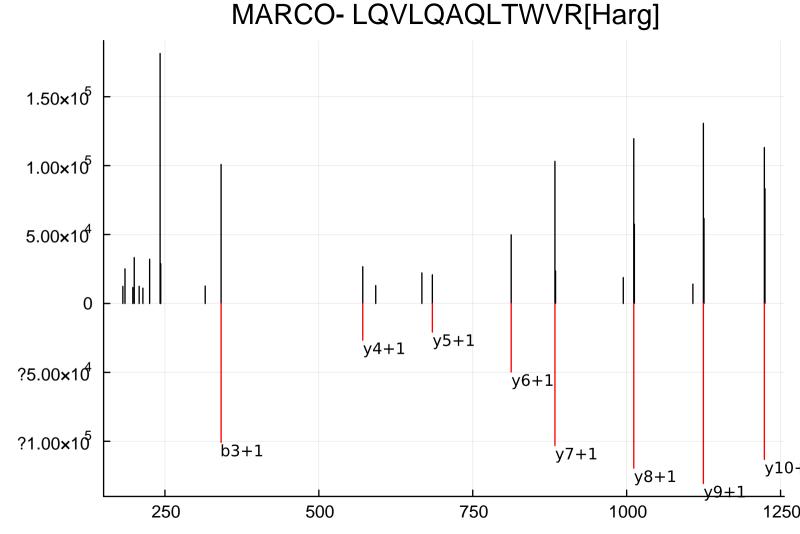


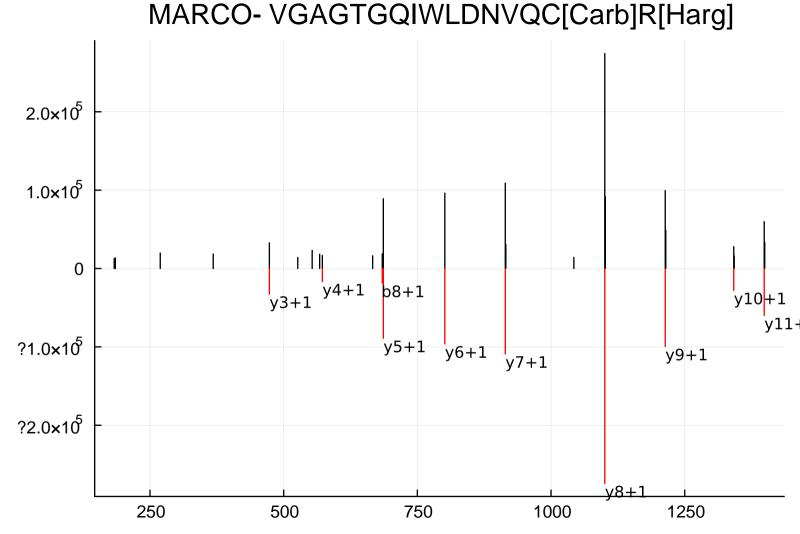


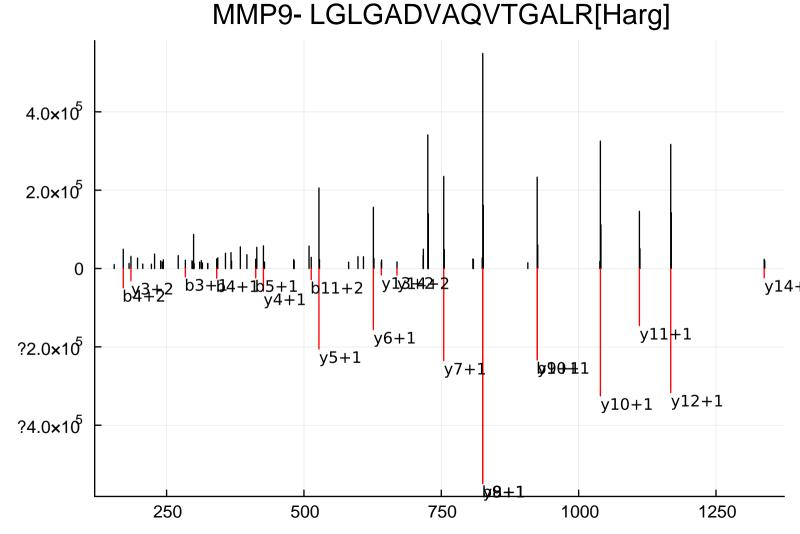


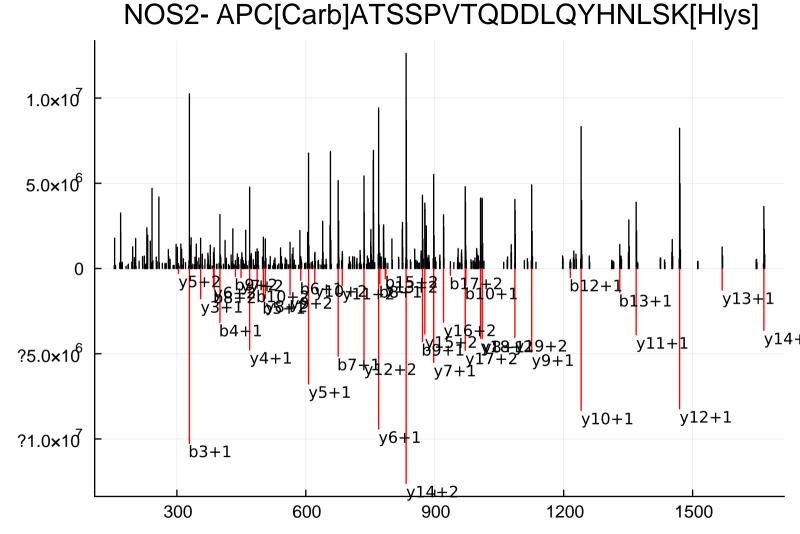


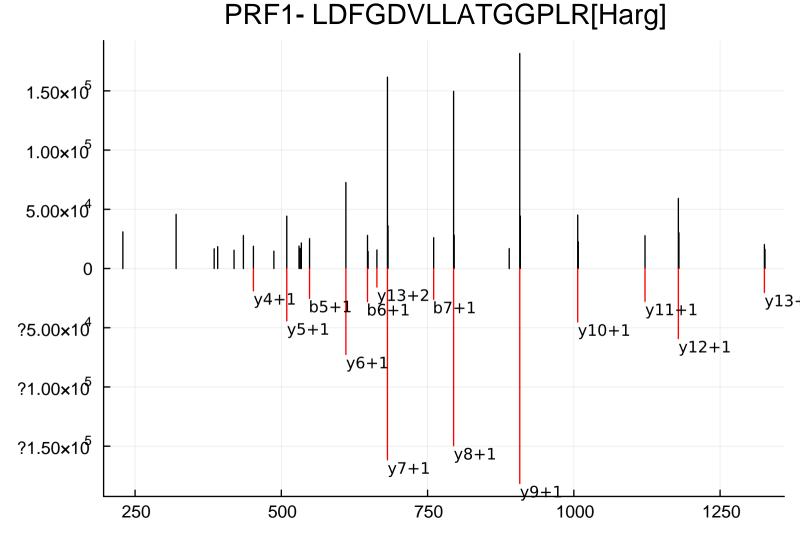












## SIGLEC1- LVASTLQGVGGPEGSSPR[Harg] 2.0×10<sup>7</sup> 1.0×10<sup>7</sup> 0 y11+1 ?1.0×10<sup>7</sup> y5+1 y7+2 ?2.0×10<sup>7</sup> 250 500 750 1000 1250

