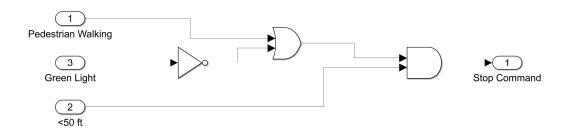
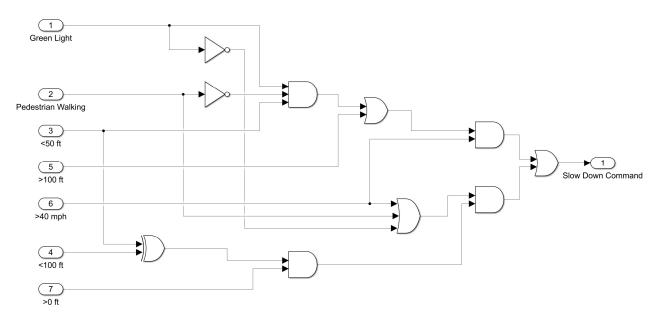
Logic Diagrams incorported into Simulink Model

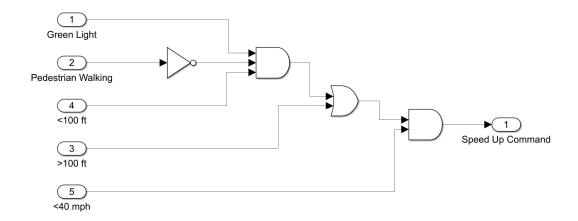
Stop Logic Diagram



Slow Down Logic Diagram



Speed Up Logic Diagram



No Change Logic Diagram

