

IPUS.2018.R1 tools - February 23rd, 2018

This release contains the tools and manuals for the IPUS tools.

New instructions

The syntax for IPUS v2 introduces ldonl, ldotl and loopl

Invocation

IPUS supports 32 and 64 bits ARM ABIs:

- -a32 generates an ARM object file
- -a64 generates an AArch64 object file
- -misp2 accepts new v2 instructions
- -misp1 (default) accepts v1
- --version displays the binutils and the IPUS versions

The GNU assembler reference manual describes other options.

Known Issues

- Objdump-ipus needs to specify on the command line -mppc -z to correctly disassemble code

Fixed issues

- Registers sortmax, sortmin, sortmedian are defined.
- Fixed syntax for ASRW min1,min2 -> ASR w,min1,min2