IPUV.2018.R1 tools - February 23rd, 2018

This release contains the tools and manuals for the IPUV tools.

New instructions

The syntax for IPUV v2 introduces ldon1, ldot1, ldvo1 and loop1

Invocation

IPUS supports 32 and 64 bits ARM ABIs:

- -a32 generates an ARM object file
- -a64 generates an AARch64 object file
- -misp2 accepts new v2 instructions
- -misp1 (default) accepts v1
- --version displays the binutils and the IPUV versions

The GNU assembler reference manual describes other options.

Known Issues

• Objdump-ipuv needs to specify on the command line -mppc -z to correctly disassemble code

Fixed issues