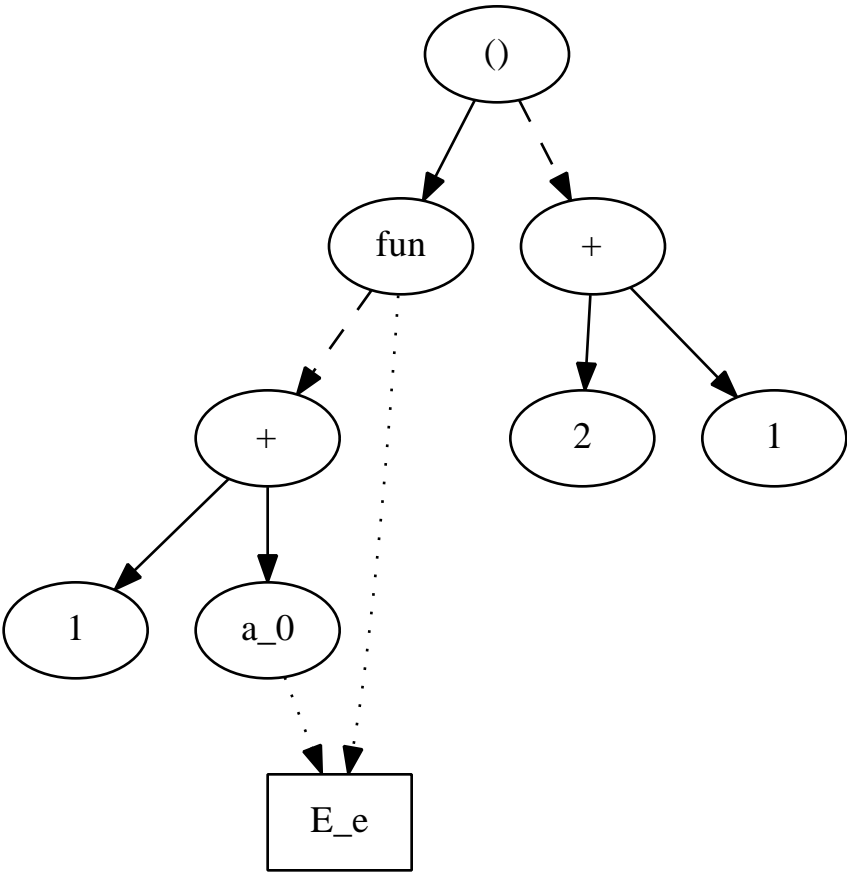
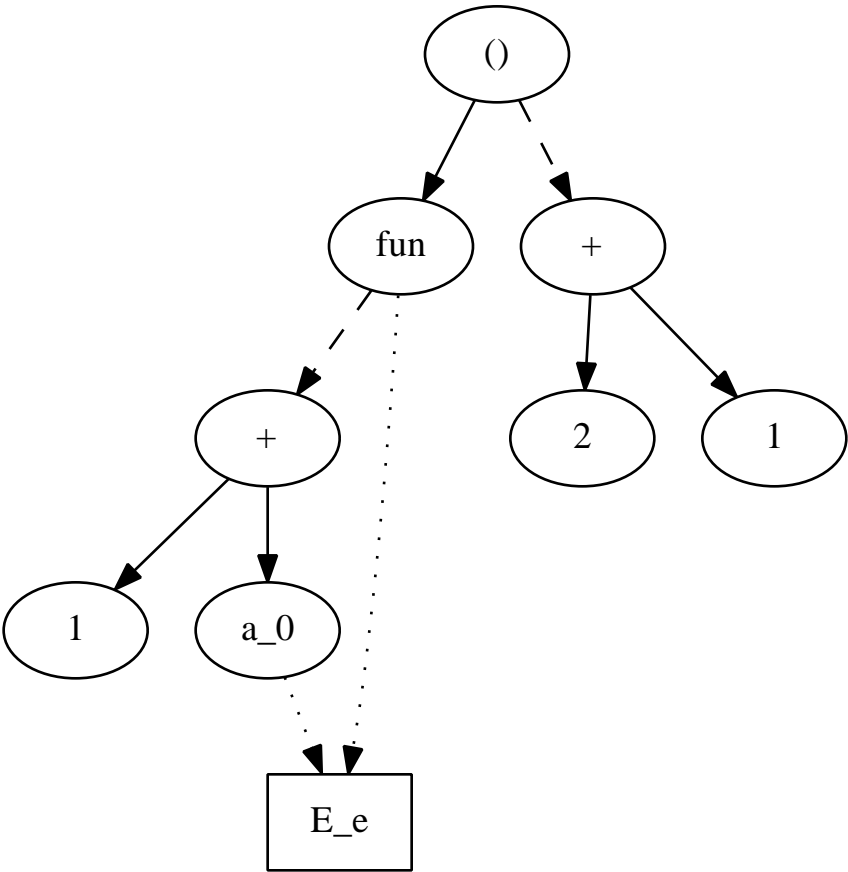


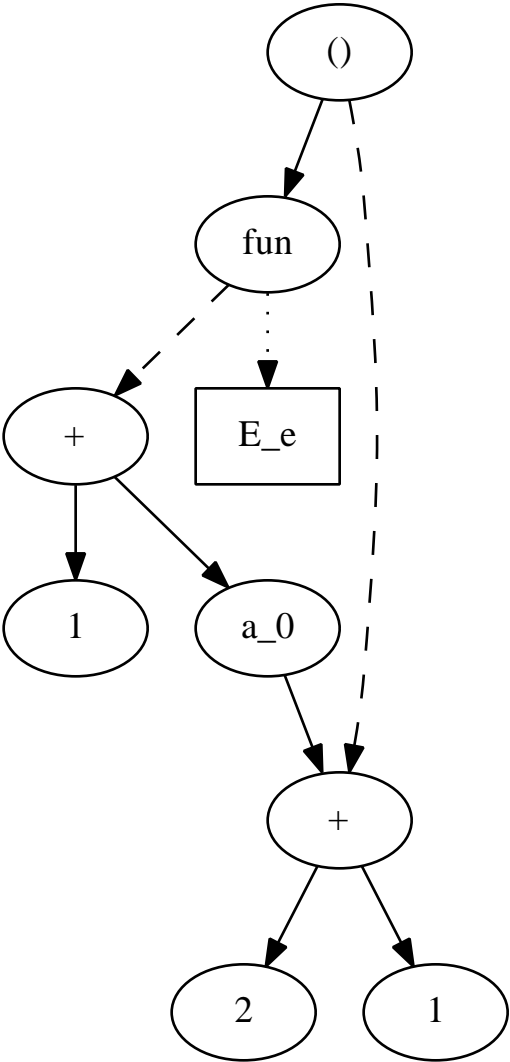
unopt



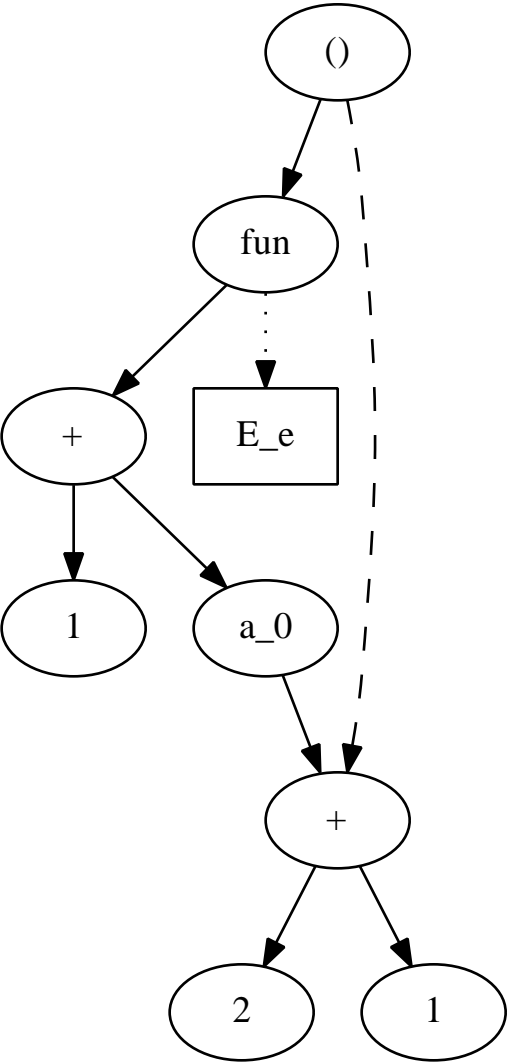
cfold



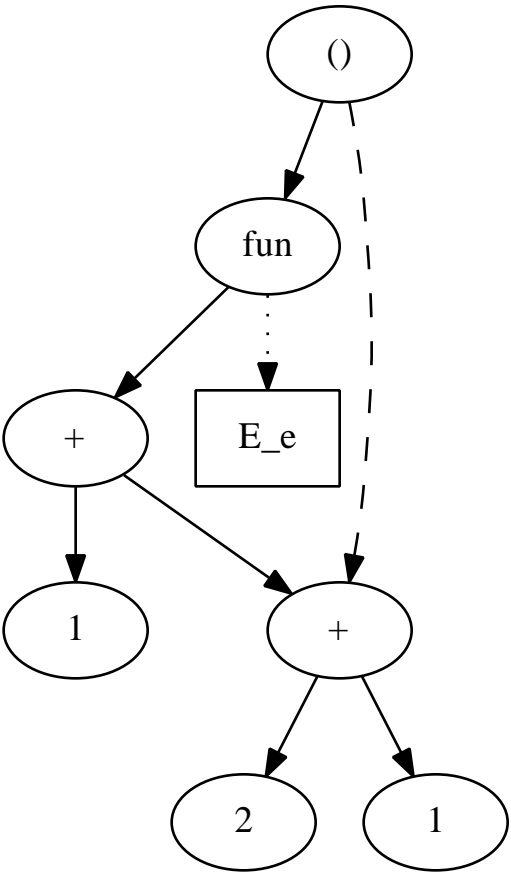
deprom



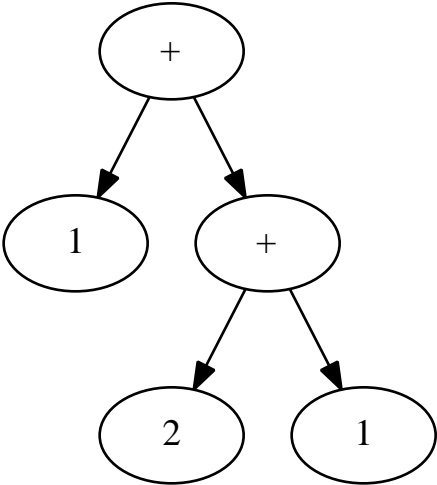
activate



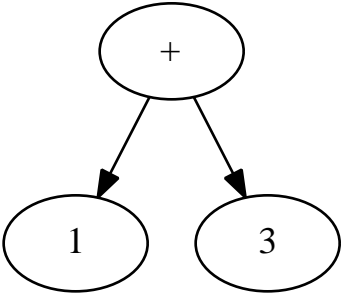
loadElim



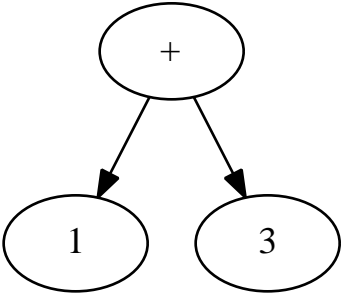
inline



cfold

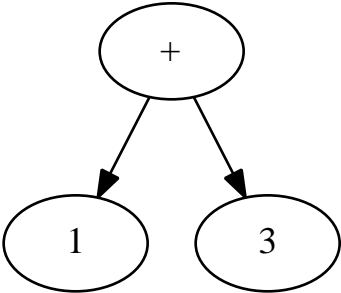


deprom

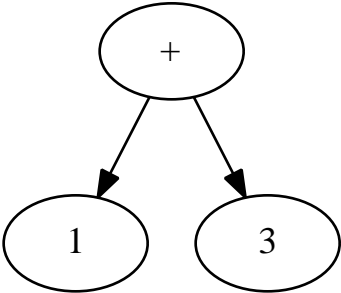




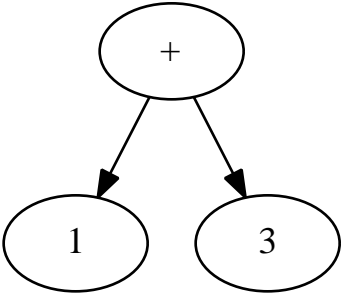
activate



loadElim



inline



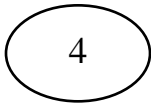
cfold

4

deprom

activate

loadElim

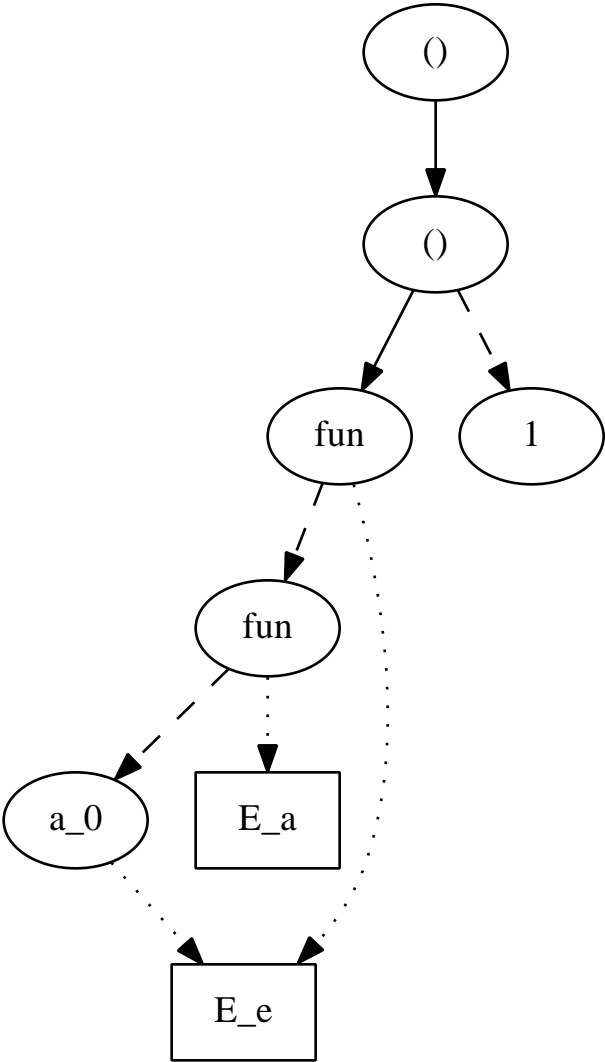


inline

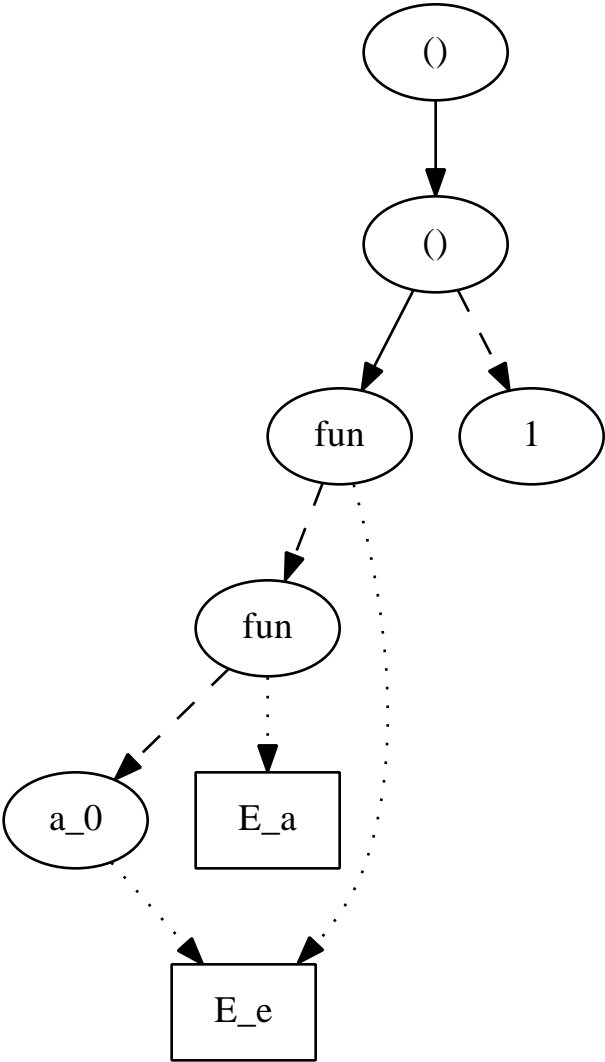
4



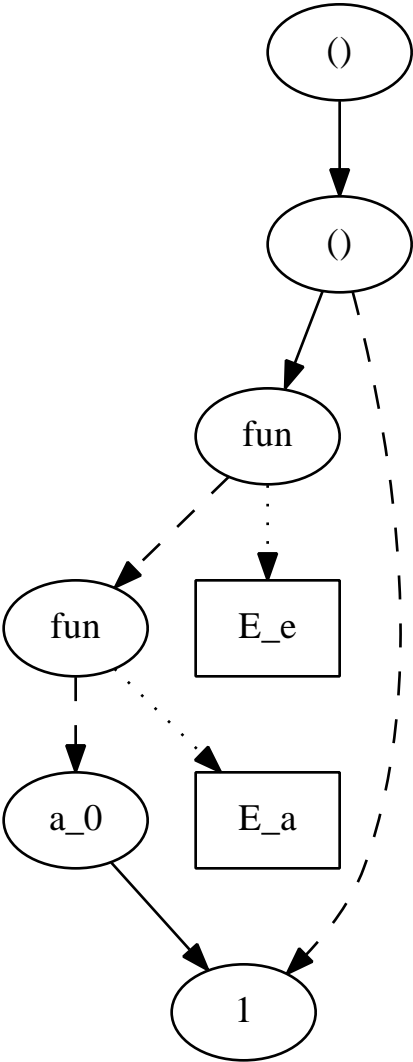
unopt



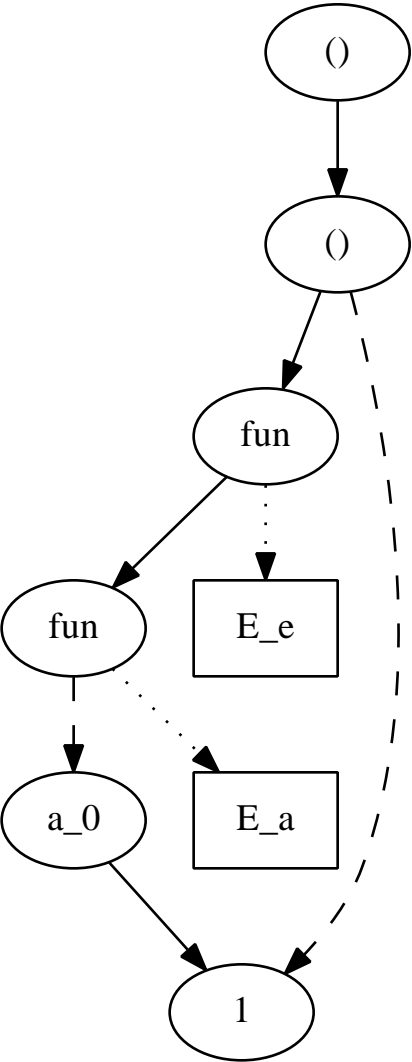
cfold



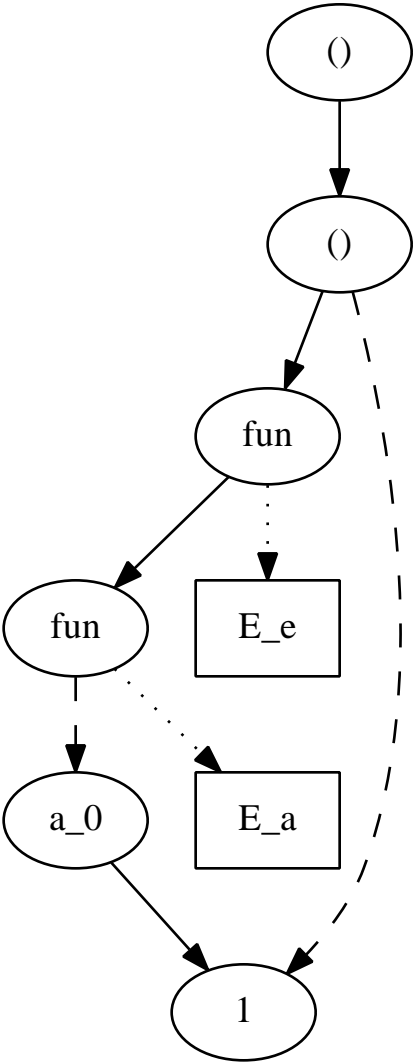
deprom



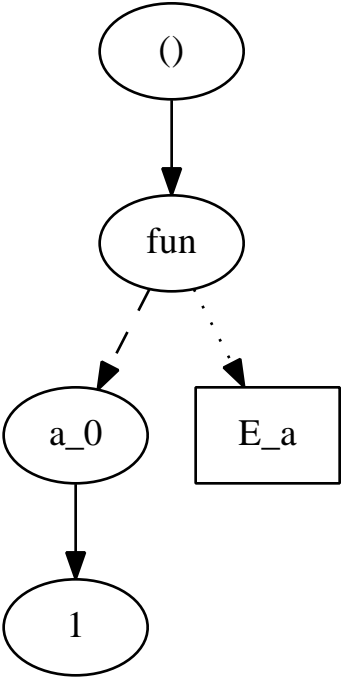
activate



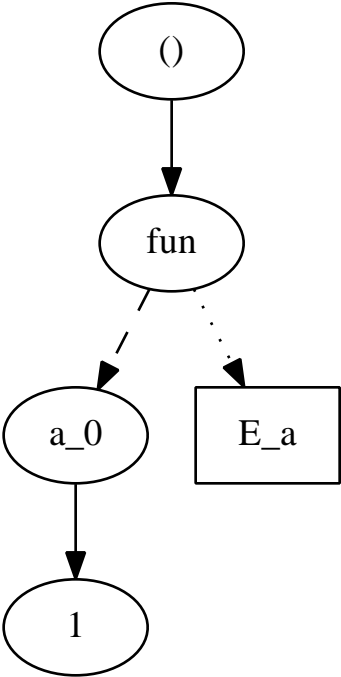
loadElim



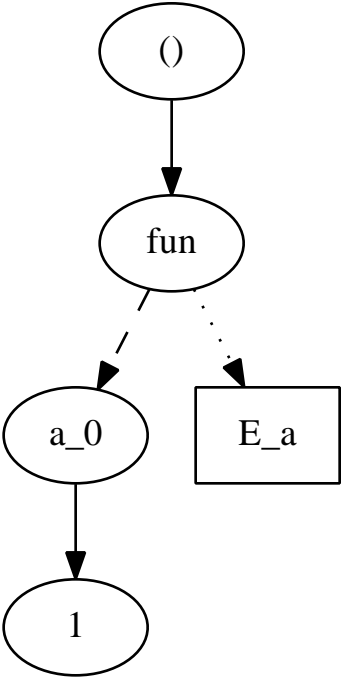
inline



cfold

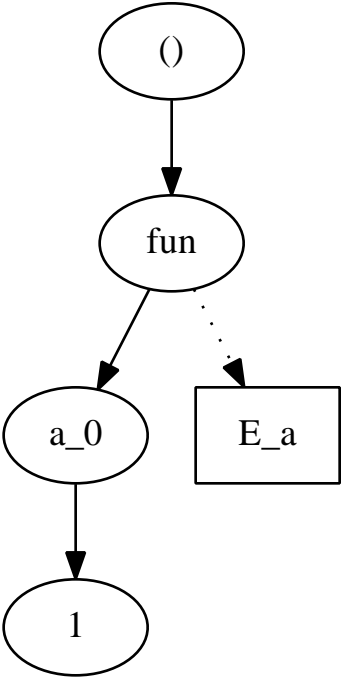


deprom

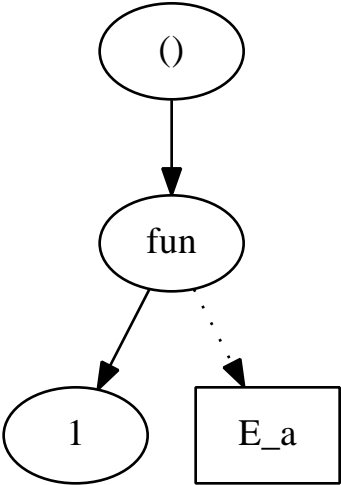




activate



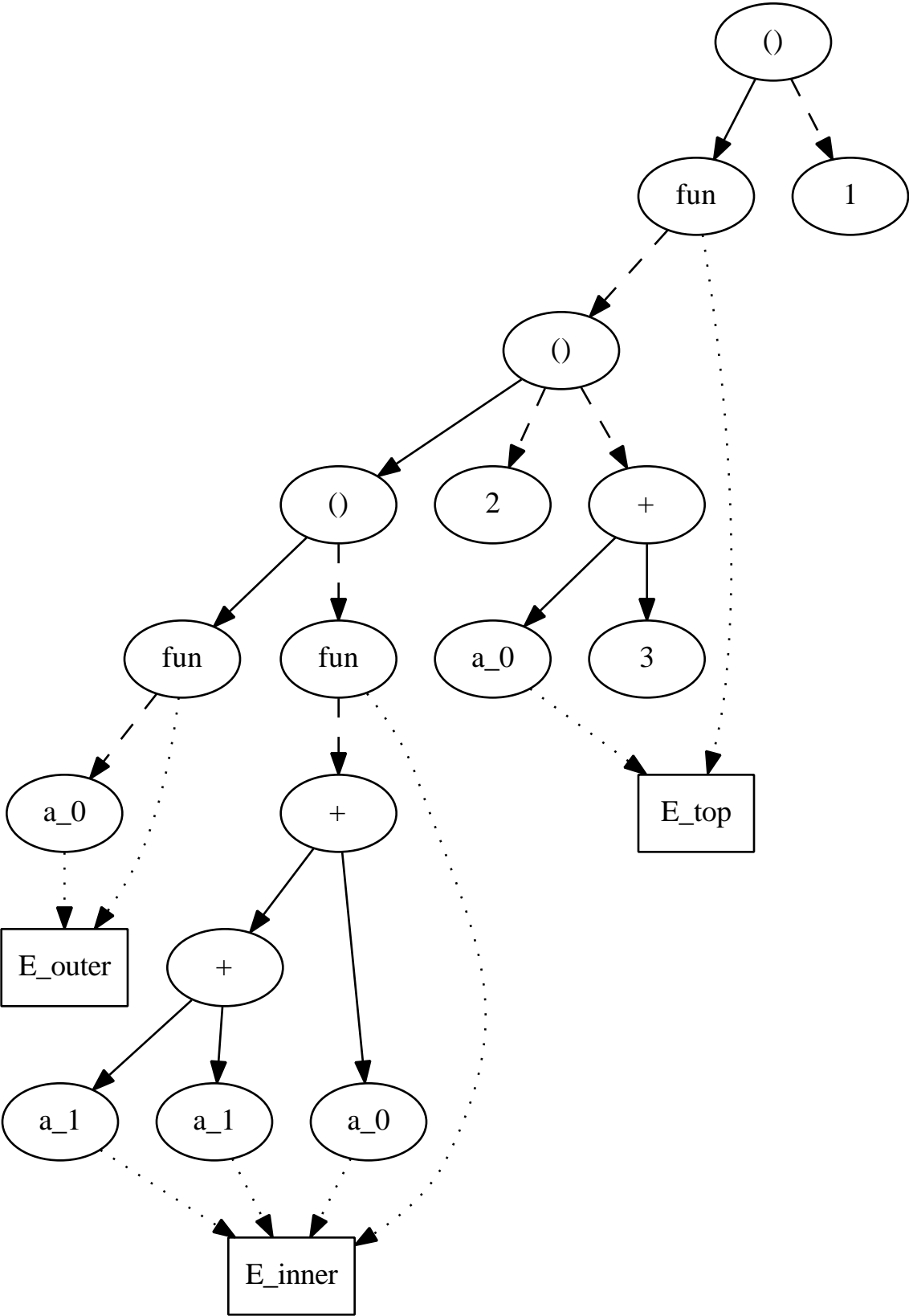
loadElim



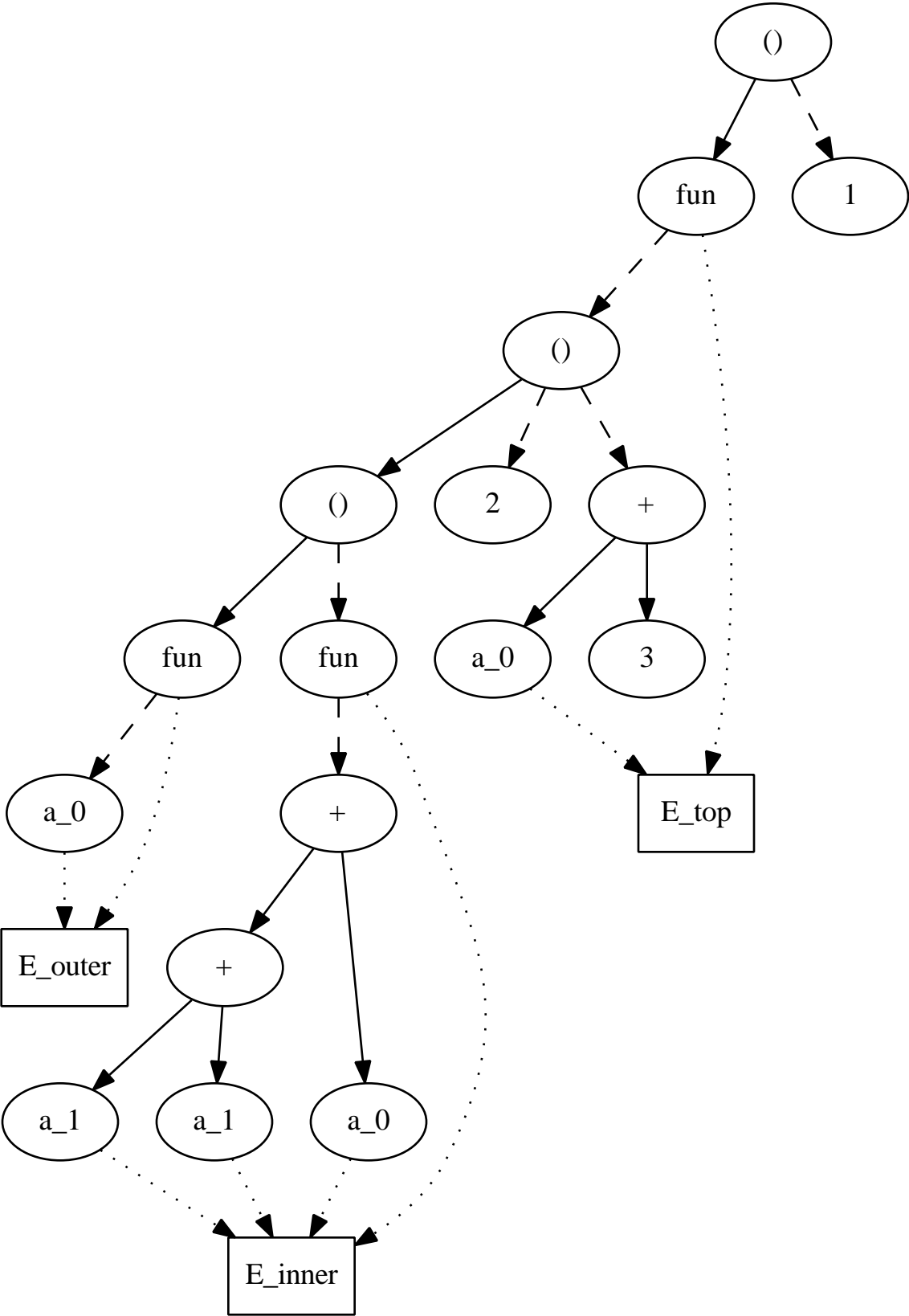
inline

1

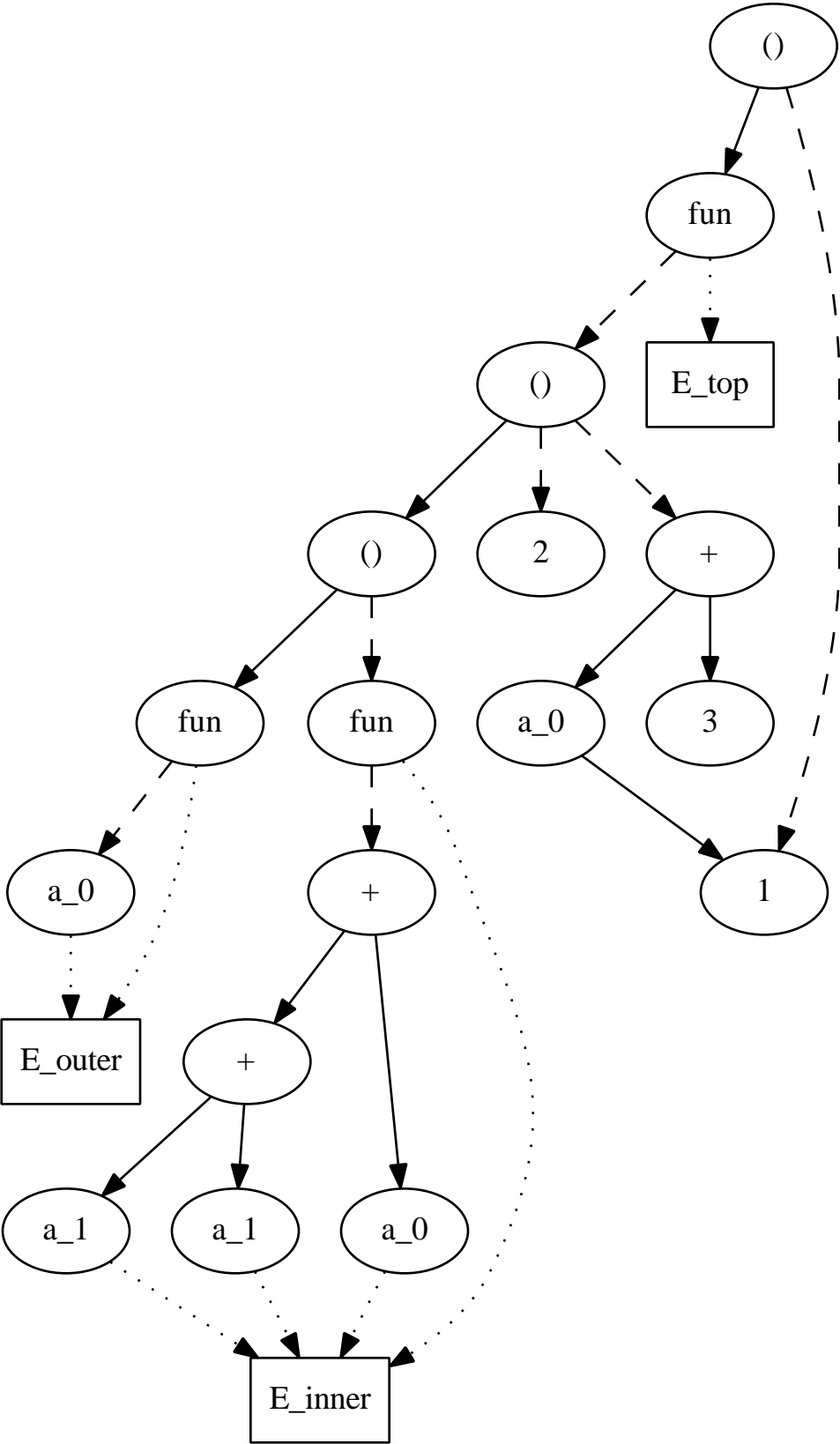
unopt



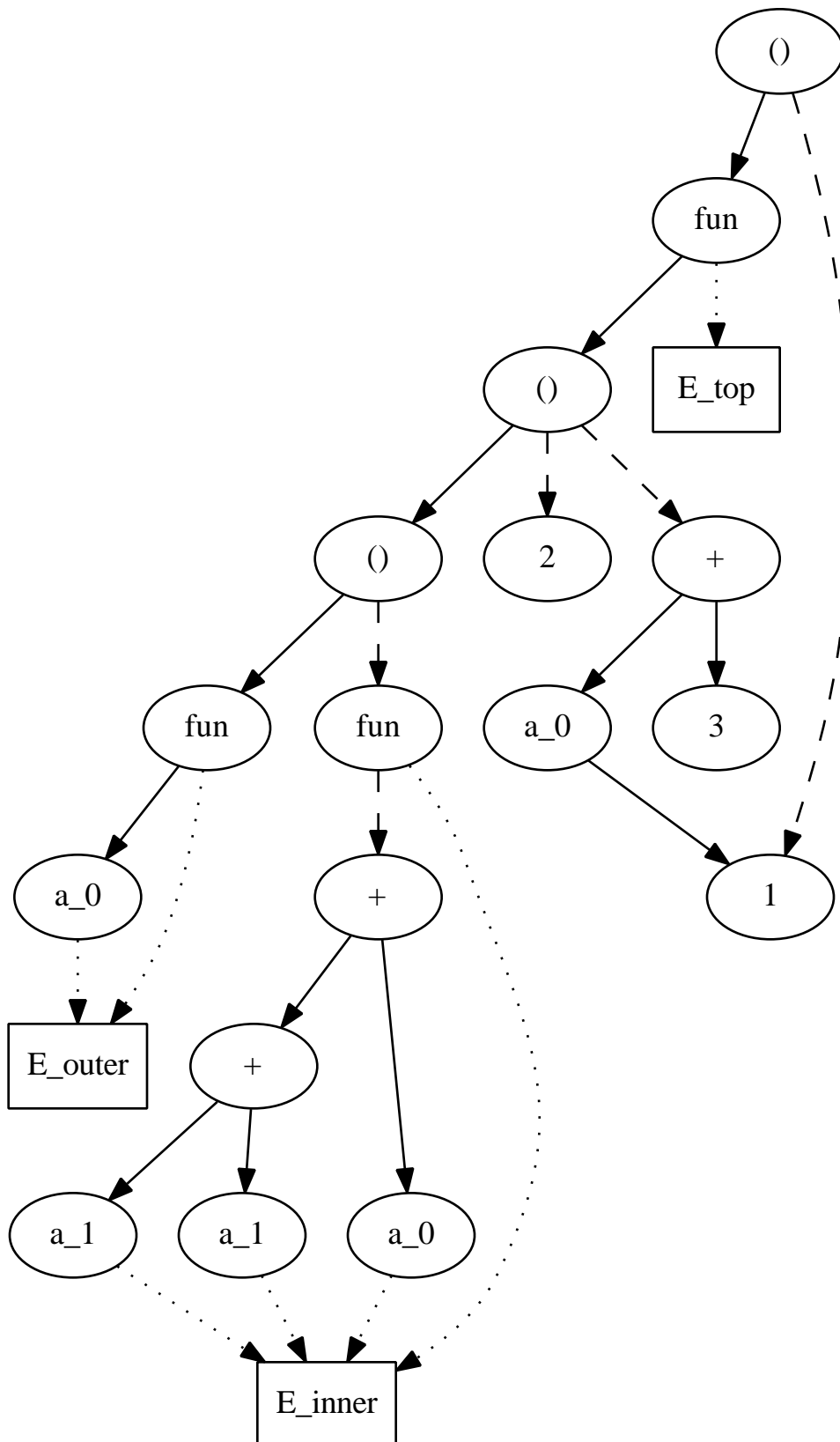
cfold



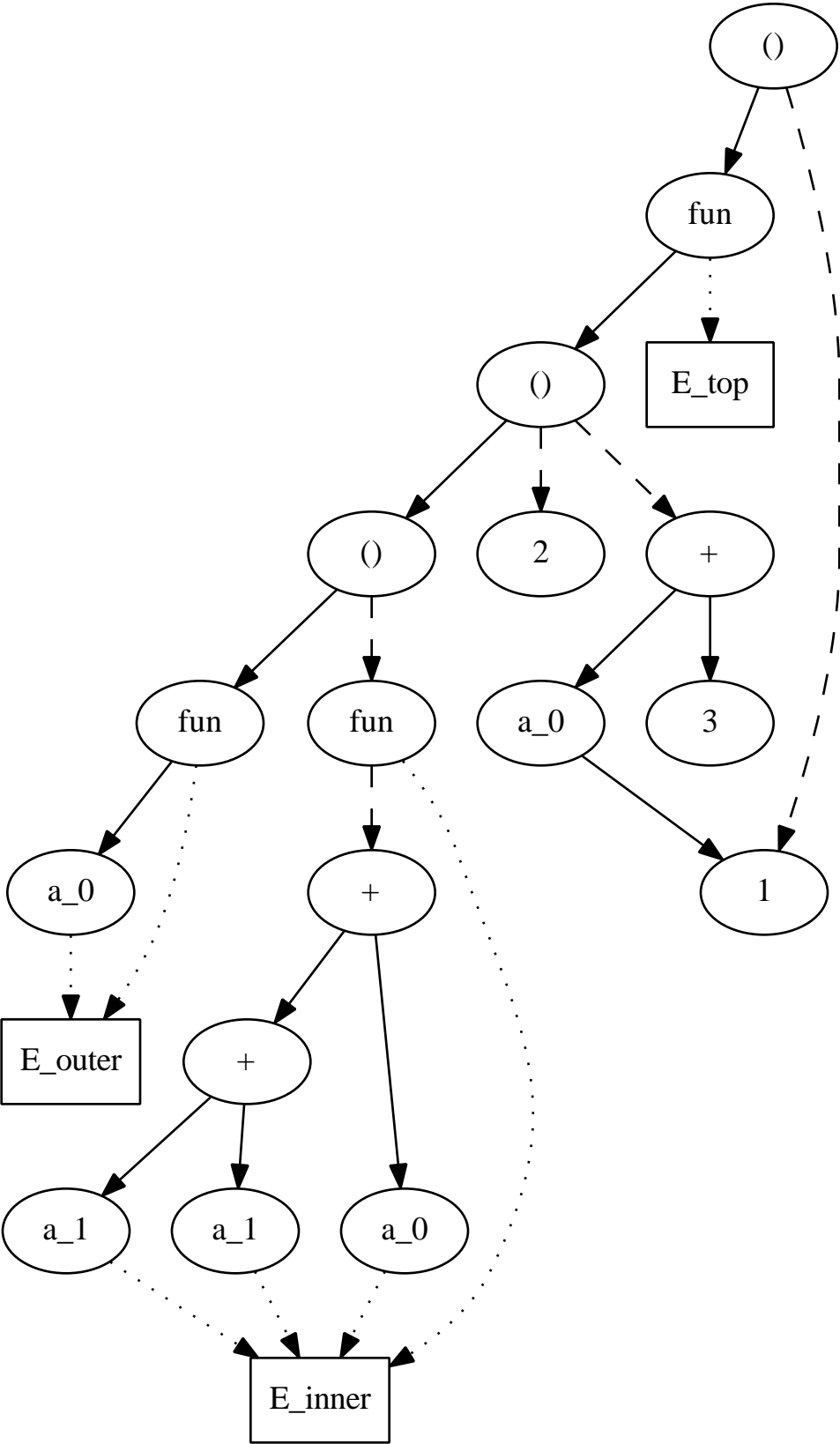
deprom



activate

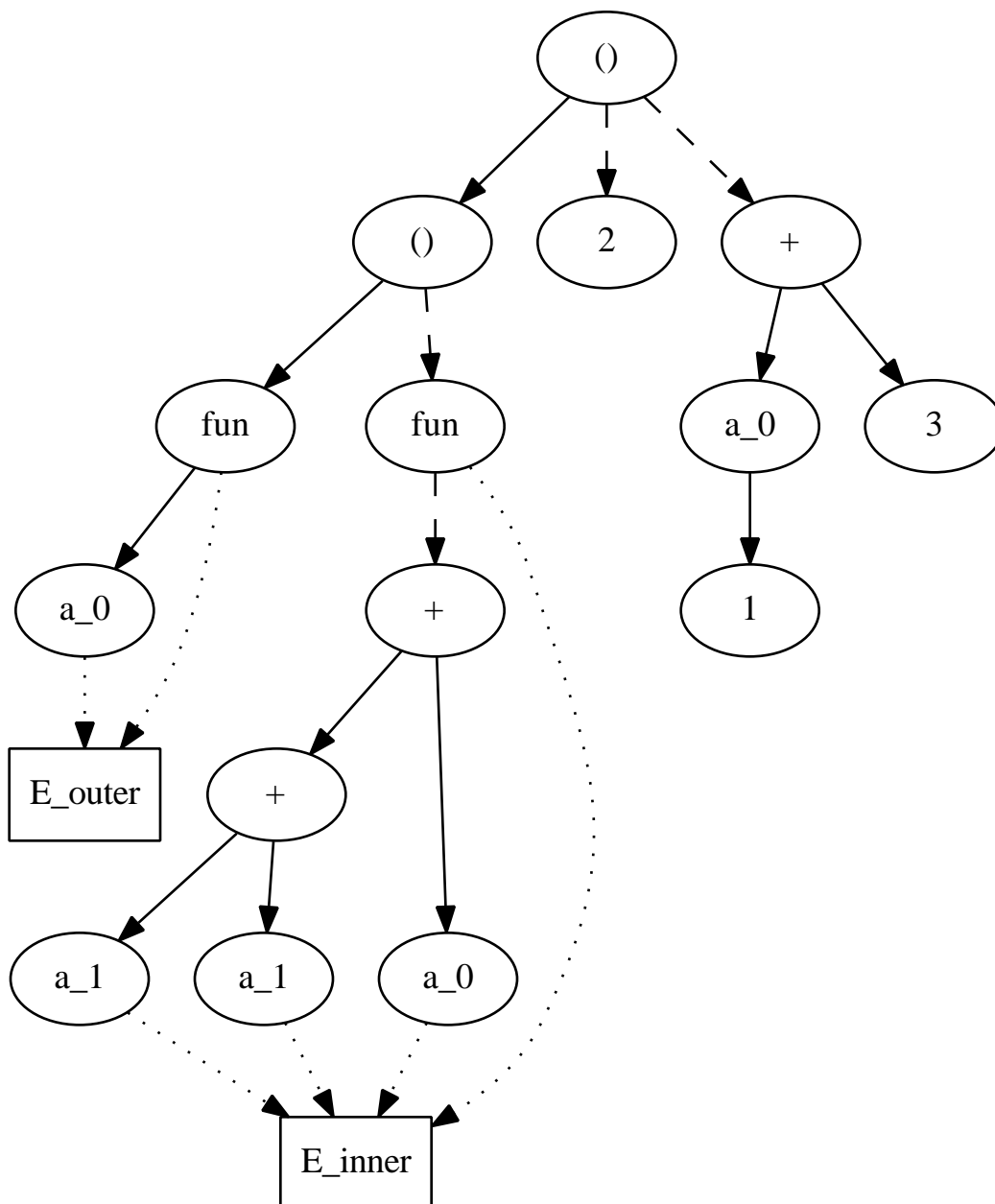


loadElim

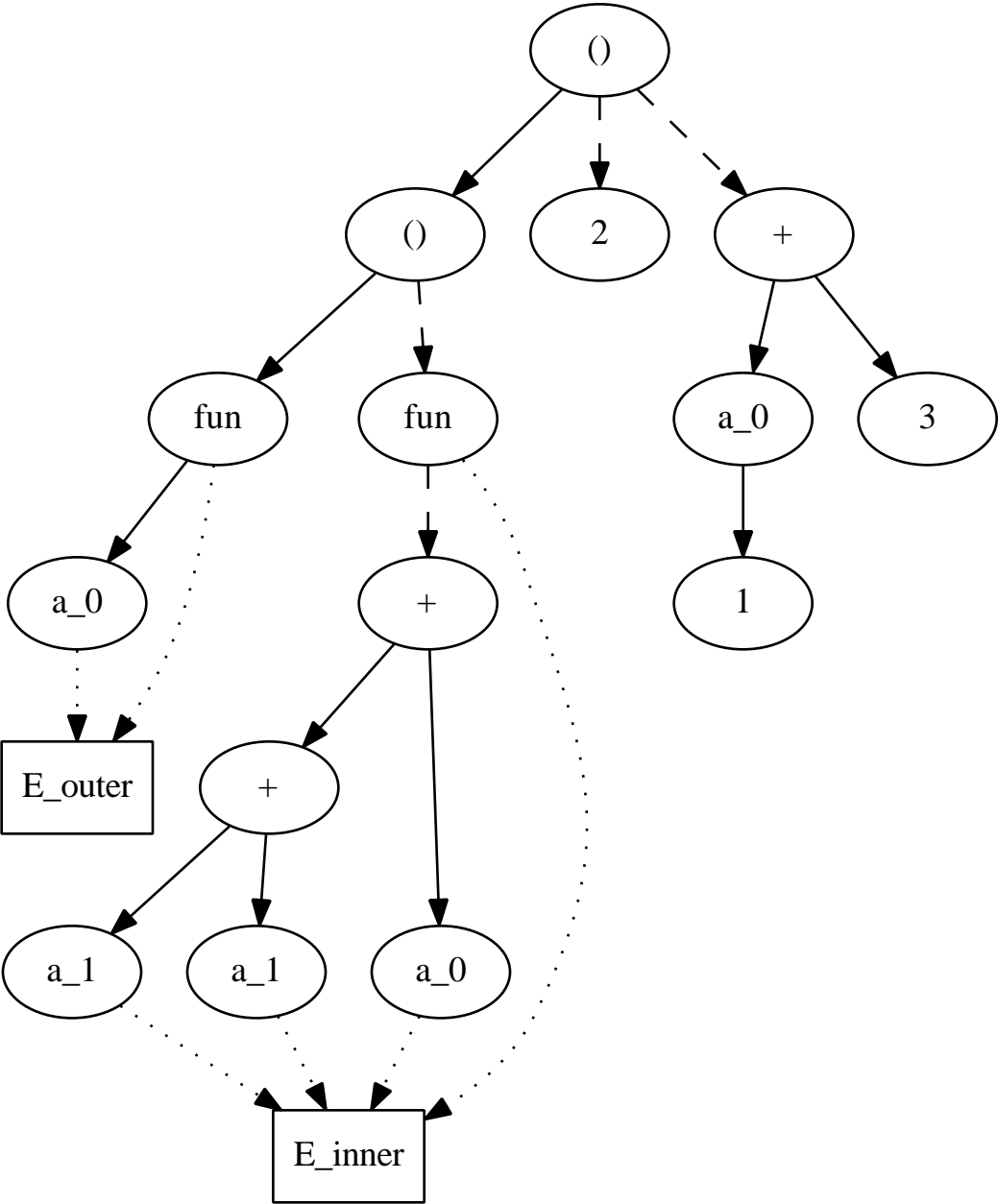




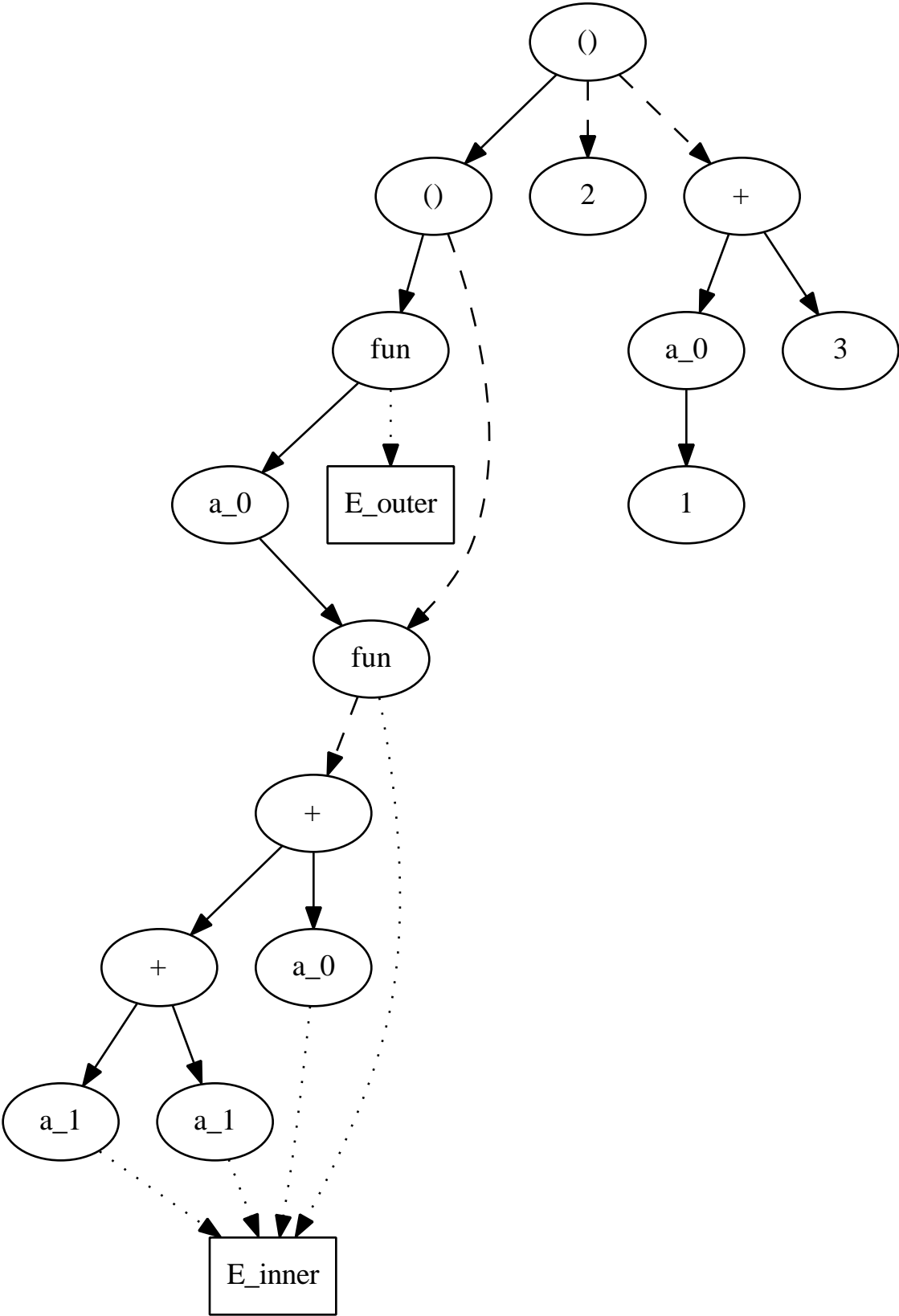
inline



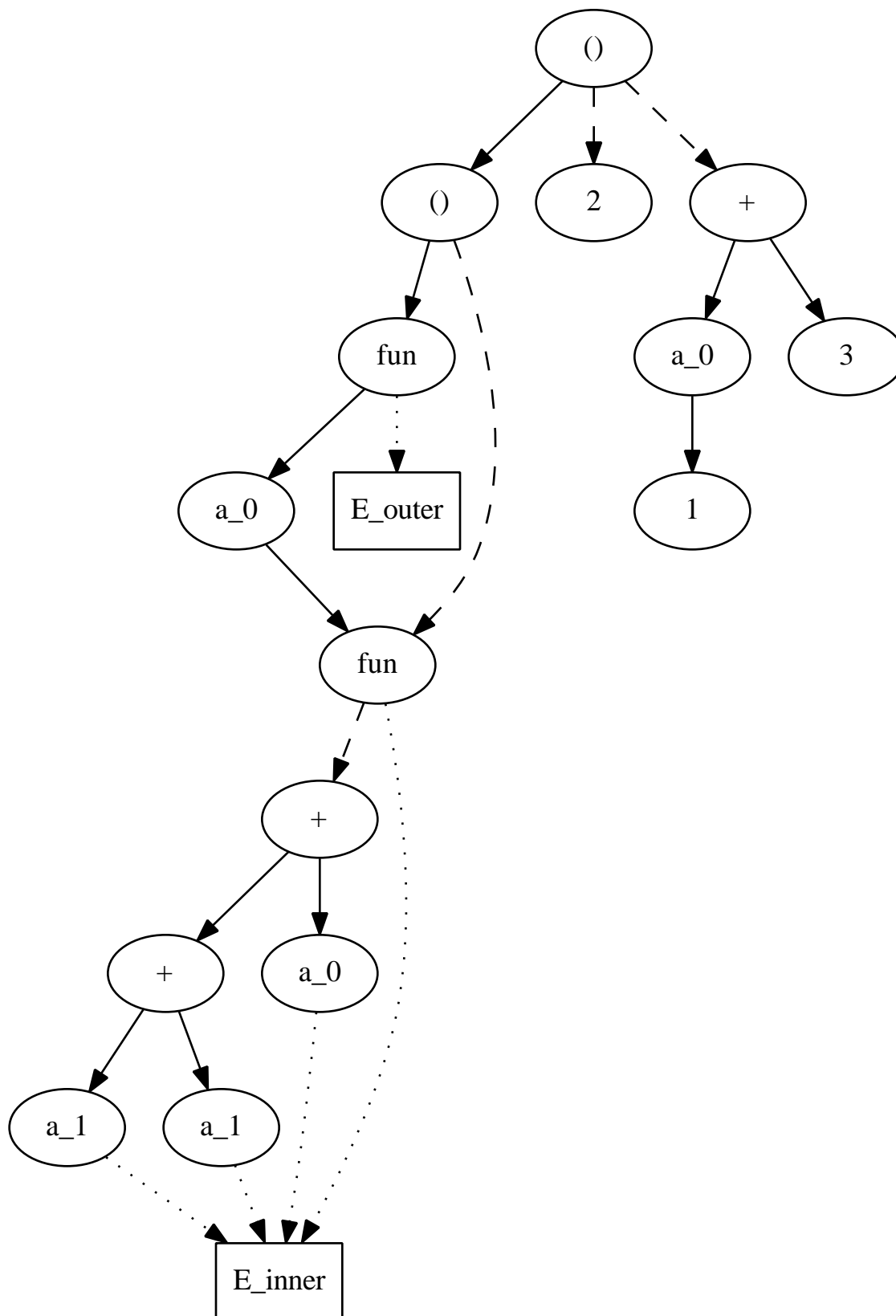
cfold



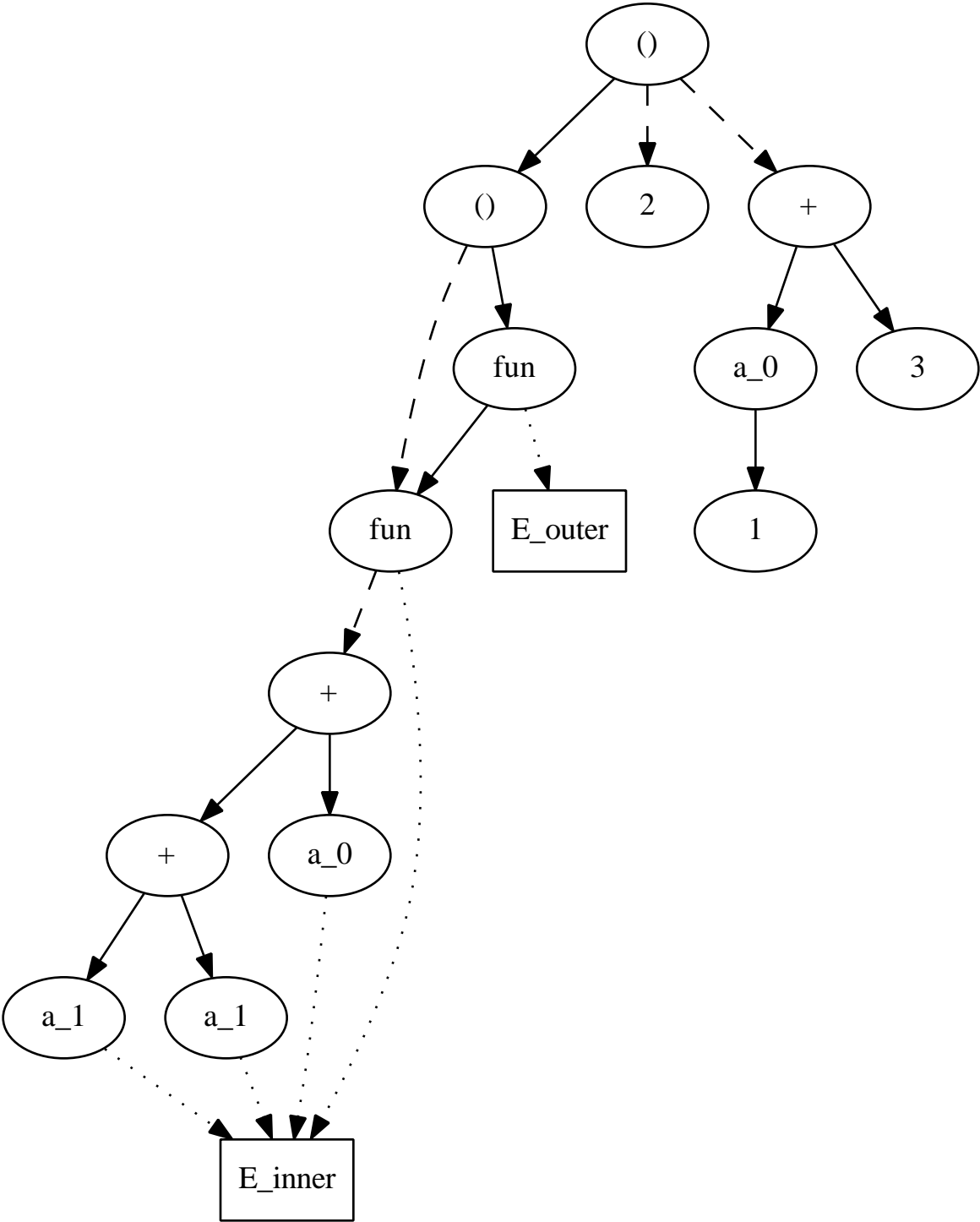
deprom



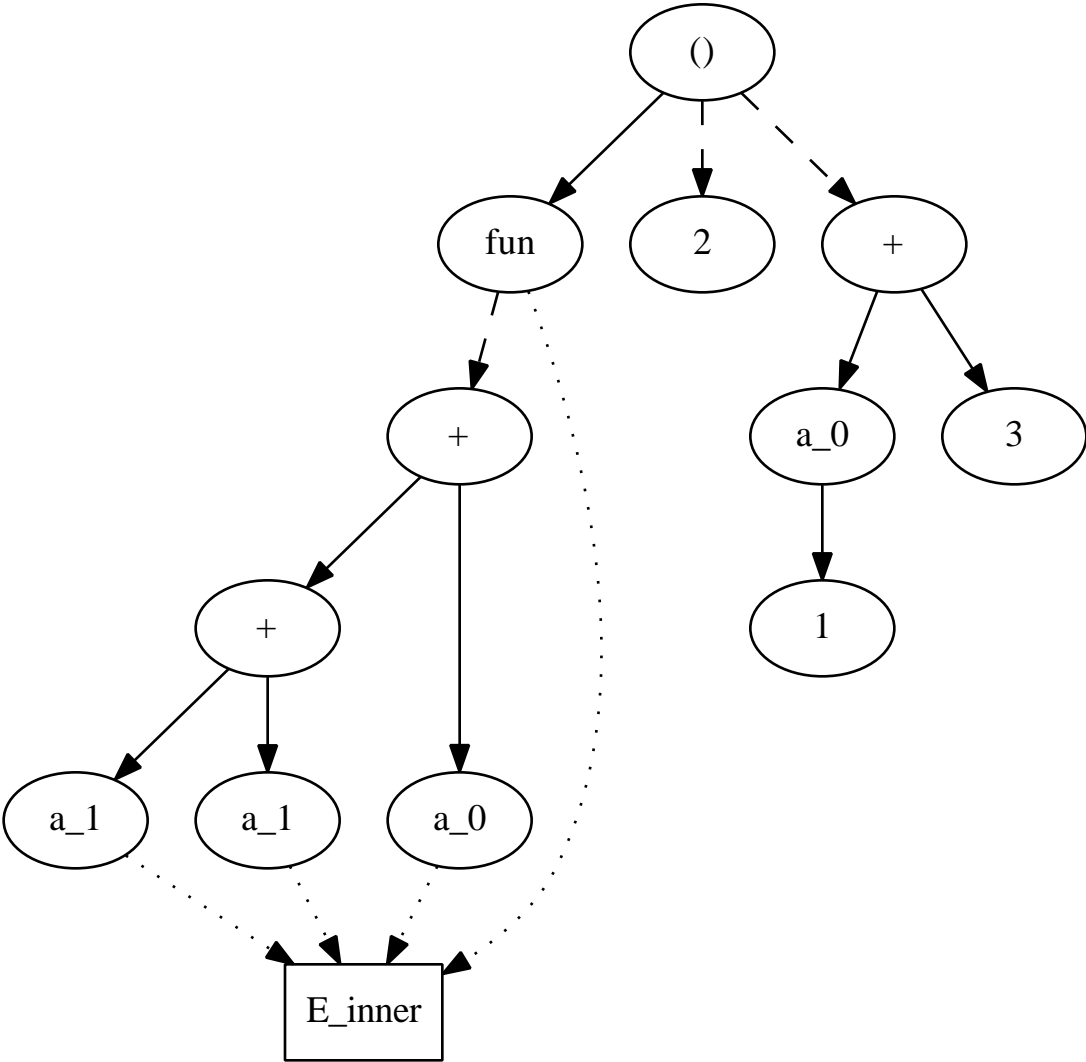
activate



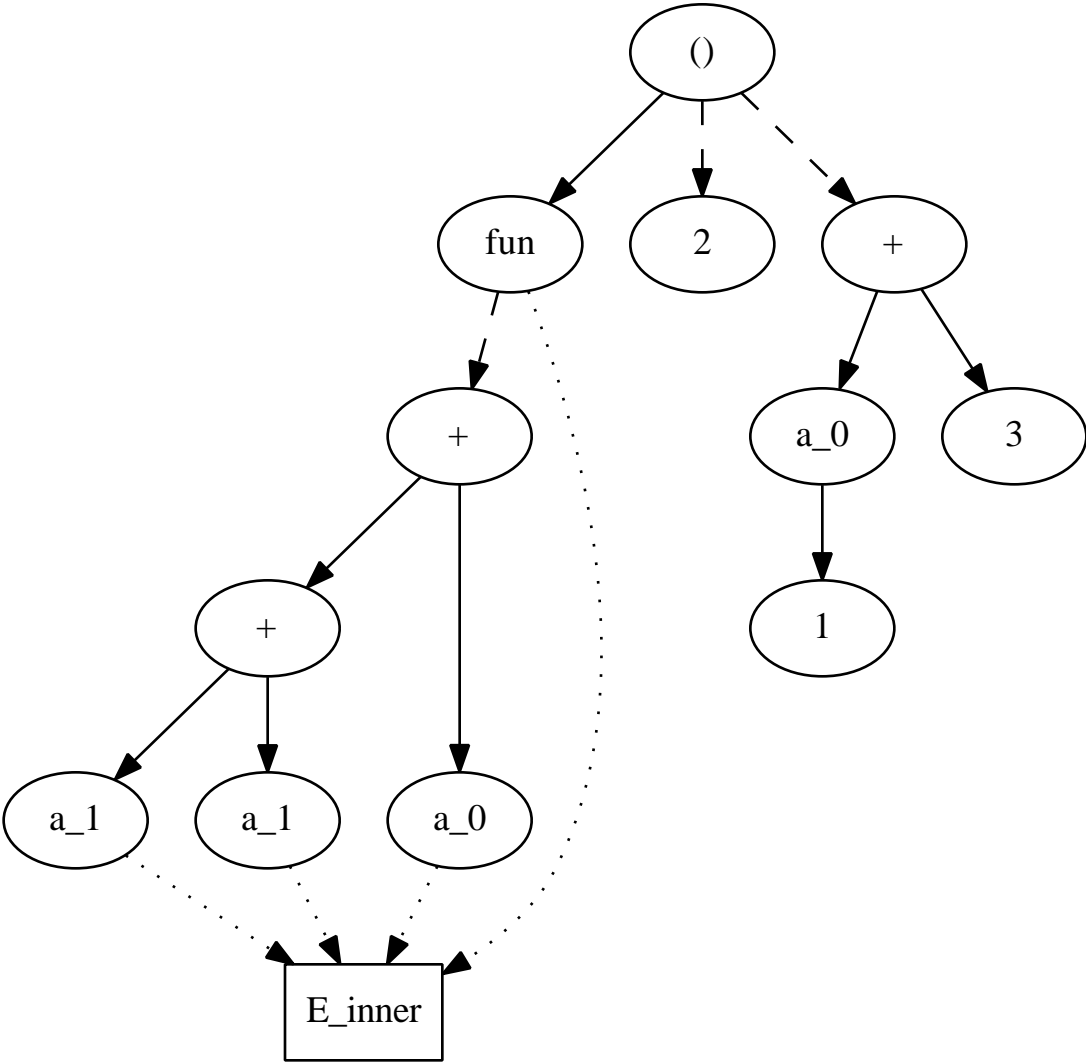
loadElim



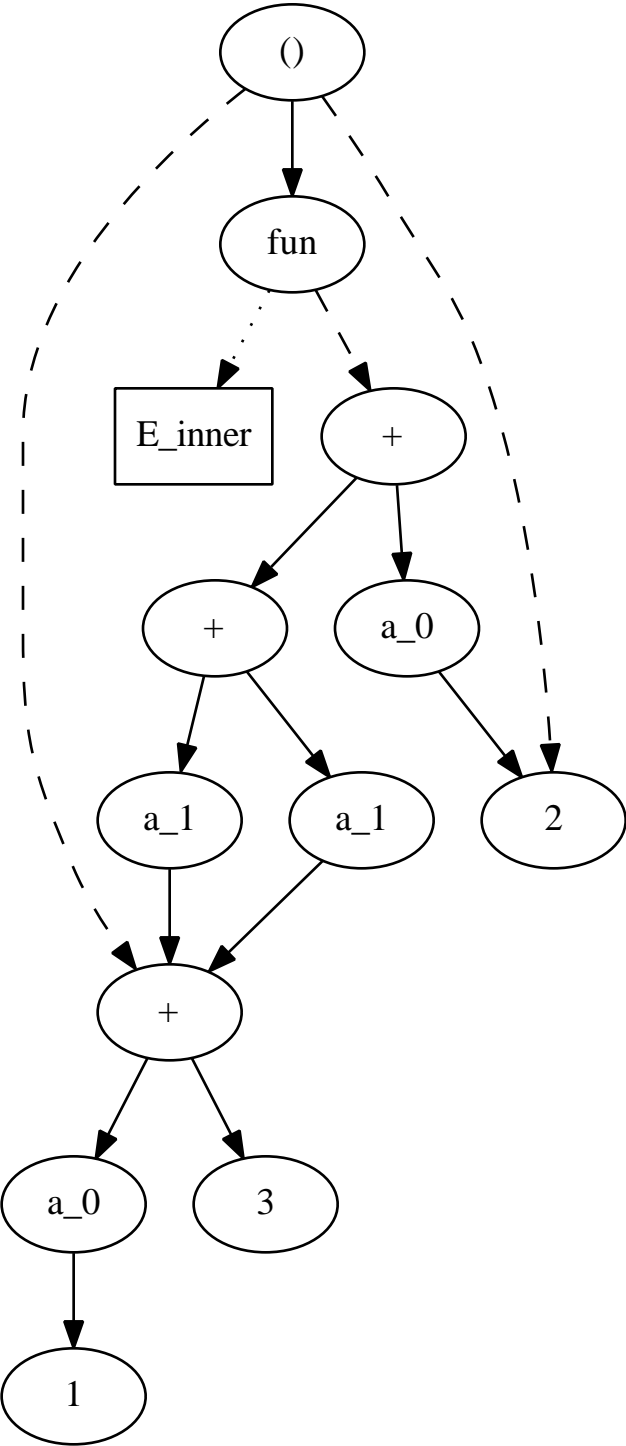
inline



cfold

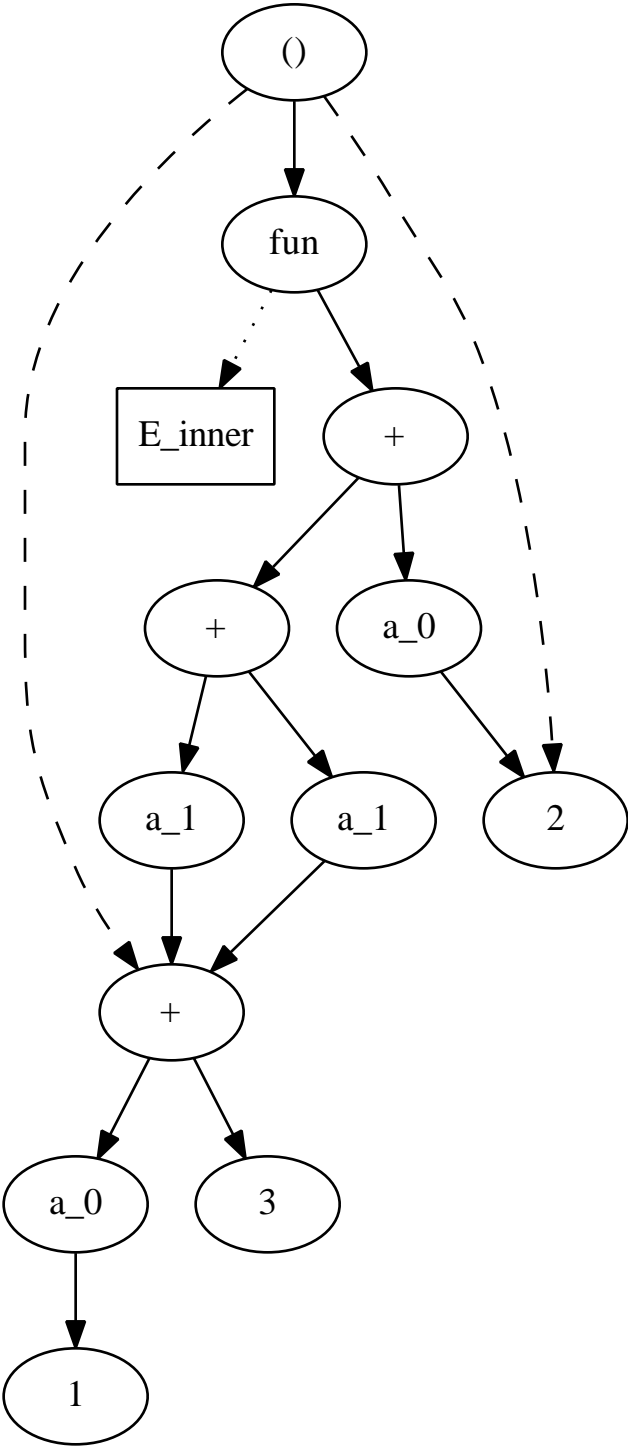


deprom

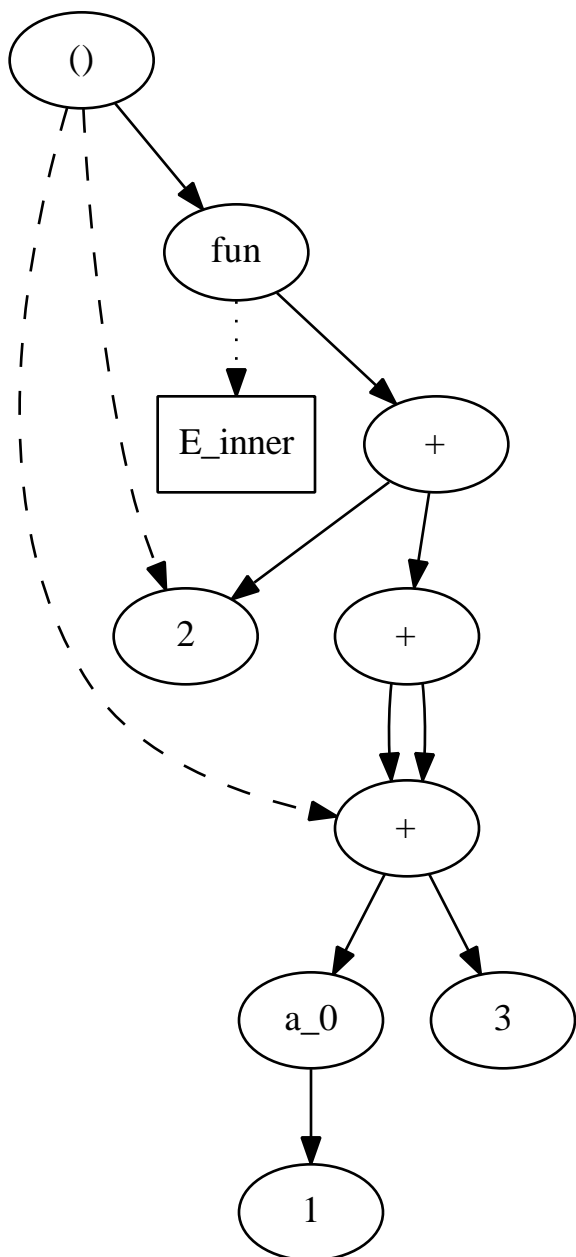




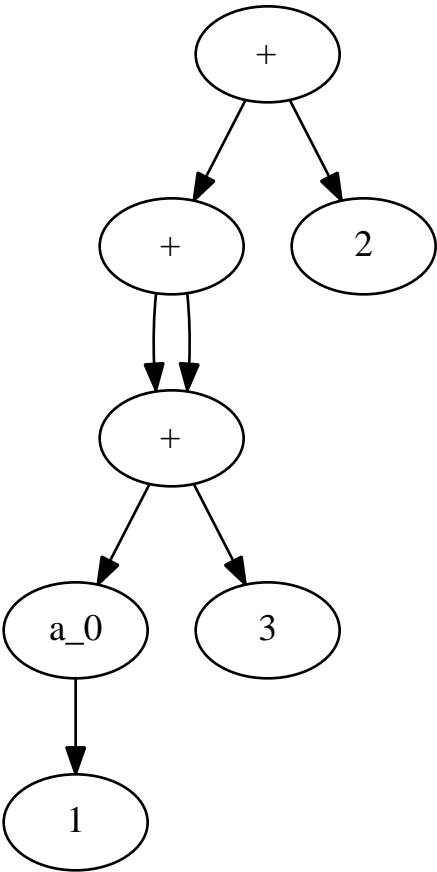
activate



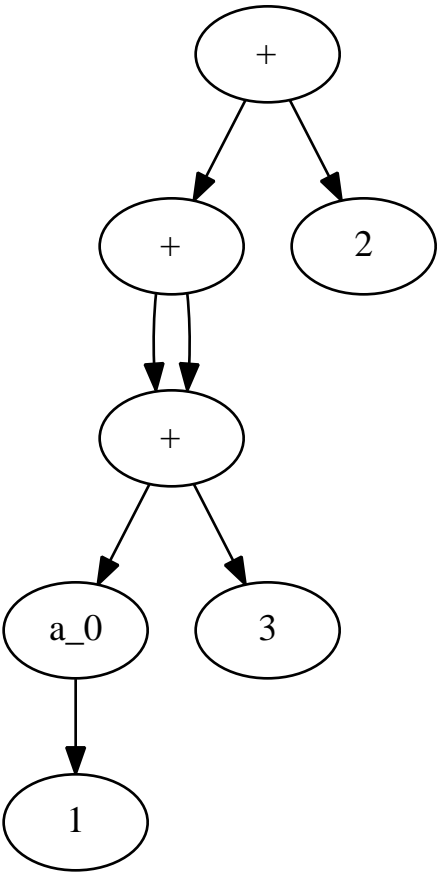
loadElim



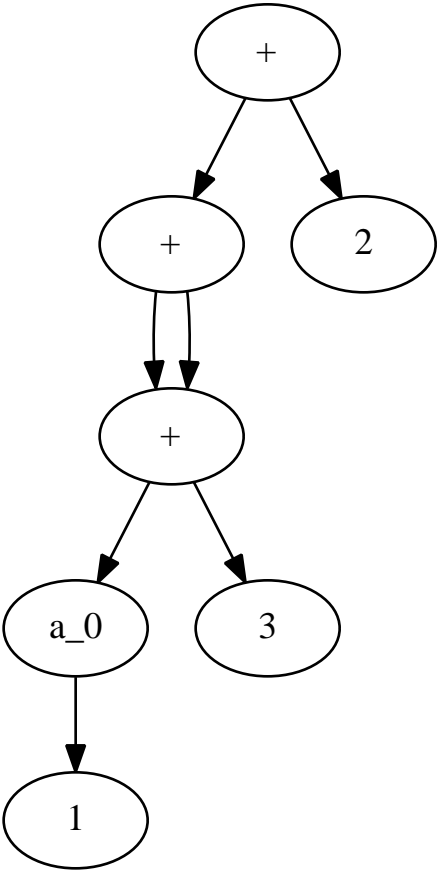
inline



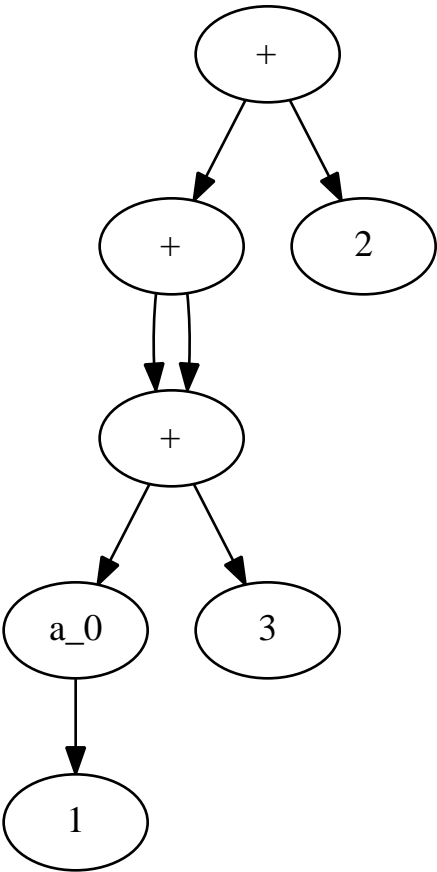
cfold



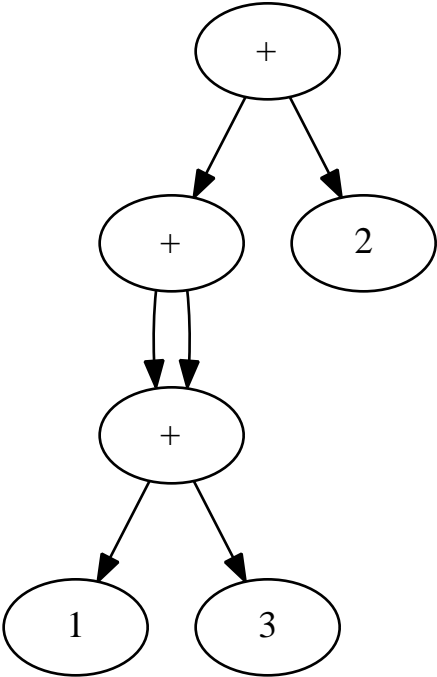
deprom



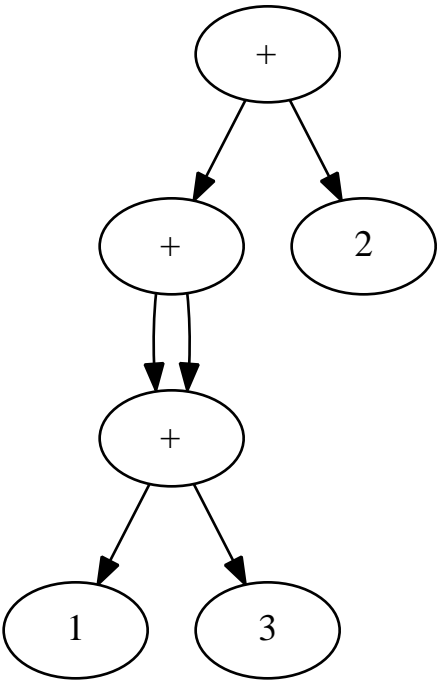
activate



loadElim

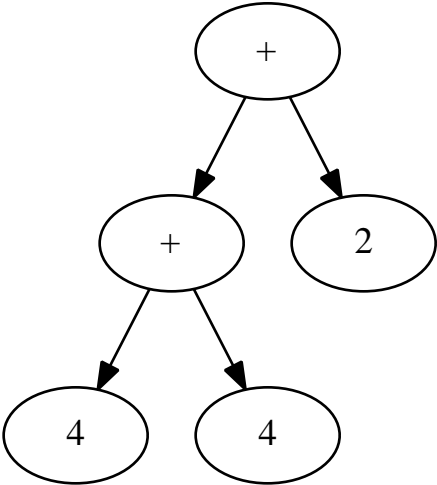


inline

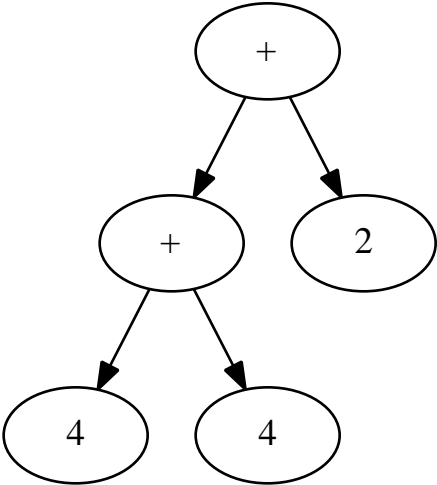




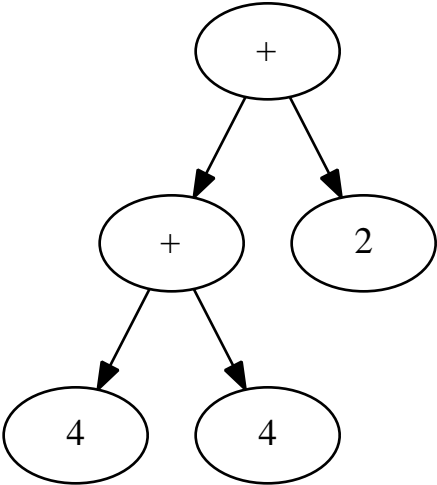
cfold



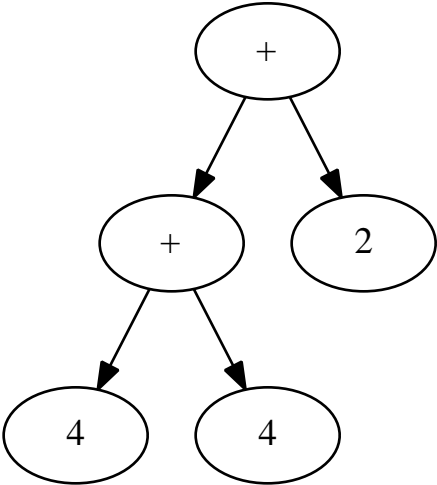
deprom



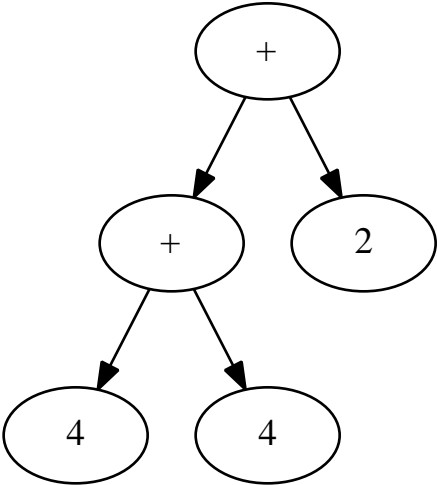
activate



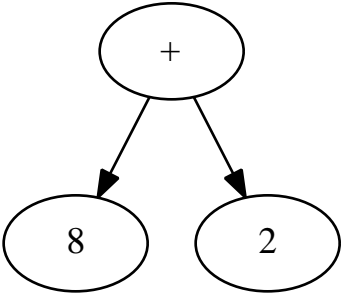
loadElim



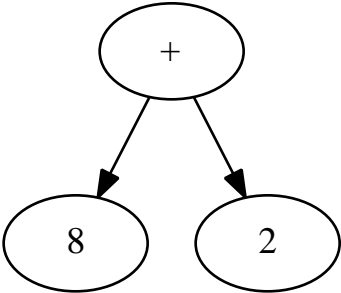
inline



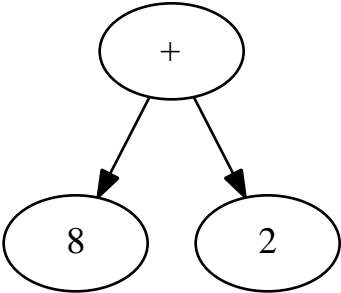
cfold



deprom

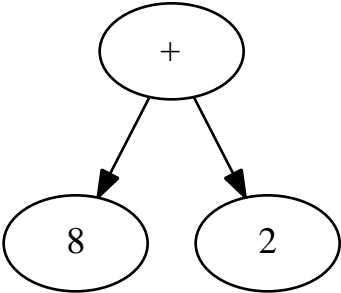


activate

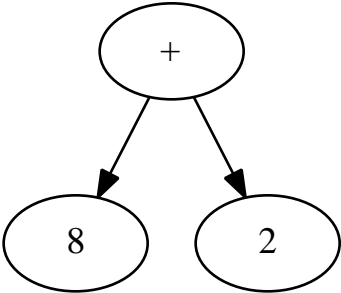




loadElim



inline



cfold

10

deprom

activate

loadElim

10

inline