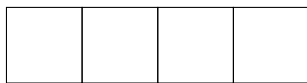
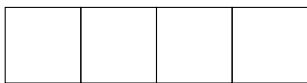
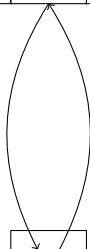
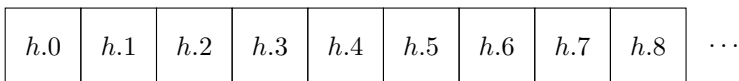


Input

Output



Hauptspeicher



Datenkeller

$d.1$

$d.2$

$d.3$

$d.4$

$d.5$

$d.6$