

# Synthetically Encoding 10 nm Morphology in Silicon Nanowires

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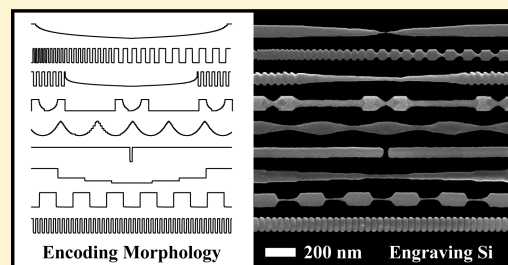
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**S** Supporting Information

**ABSTRACT:** Si nanowires (NWs) have been widely explored as a platform for photonic and electronic technologies. Here, we report a bottom-up method to break the conventional “wire” symmetry and synthetically encode a high-resolution array of arbitrary shapes, including nanorods, sinusoids, bowties, tapers, nanogaps, and gratings, along the NW growth axis. Rapid modulation of phosphorus doping combined with selective wet-chemical etching enabled morphological features as small as 10 nm to be patterned over wires more than 50  $\mu\text{m}$  in length. This capability fundamentally expands the set of technologies that can be realized with Si NWs, and as proof-of-concept, we demonstrate two distinct applications.

First, nanogap-encoded NWs were used as templates for Noble metals, yielding plasmonic structures with tunable resonances for surface-enhanced Raman imaging. Second, core/shell Si/SiO<sub>2</sub> nanorods were integrated into electronic devices that exhibit resistive switching, enabling nonvolatile memory storage. Moving beyond these initial examples, we envision this method will become a generic route to encode new functionality in semiconductor NWs.

**KEYWORDS:** Silicon nanowire, bottom-up lithography, plasmonics, surface-enhanced Raman spectroscopy, resistive switching



Most semiconductor technologies rely on the ability to pattern materials with nanometer-scale features using top-down lithographic tools. Over the past decade, however, bottom-up chemical methods to control the size, shape, and composition of nanoscale materials have progressed rapidly, resulting in a diverse set of well-controlled morphologies including dots, rods, ribbons, and wires.<sup>1–3</sup> Semiconductor nanowires (NWs) are recognized as an especially important technological building block because the high aspect ratio can be used for longitudinal transport of electrical or optical signals.<sup>1,2</sup> A variety of devices have been demonstrated, including sensors,<sup>4</sup> waveguides,<sup>5</sup> phase-change memory,<sup>6</sup> light-emitting diodes,<sup>7</sup> and solar cells.<sup>8–11</sup> Nevertheless, current NW-based technology has been limited by the material’s translational symmetry and the inability to pattern arbitrary, nanometer-scale morphological features.

NWs are typically synthesized using the vapor–liquid–solid (VLS) mechanism,<sup>12</sup> in which a metal nanoparticle catalyzes one-dimensional growth of a single-crystalline semiconductor material. Advancements in VLS-based technologies have generally involved synthesis of new materials or heterostructures.<sup>13</sup> For instance, NW superlattices, in which the composition of a NW is modulated along the growth axis, have been reported for Si/SiGe,<sup>14</sup> GaAs/GaP,<sup>15</sup> InAs/InP,<sup>16,17</sup> Zn-doped InP,<sup>18</sup> CdSe/ZnSe,<sup>19</sup> and P-doped Si.<sup>20</sup> Modulation of NW growth conditions often results in crystallographic twin planes and faceting of the NW surface, providing some capability to control morphology during synthesis.<sup>17,18,21,22</sup> In addition, NW superstructures have been reported in the form of controllably kinked NWs that break the one-dimensional shape.<sup>23,24</sup> Nevertheless, only a handful of reports describe

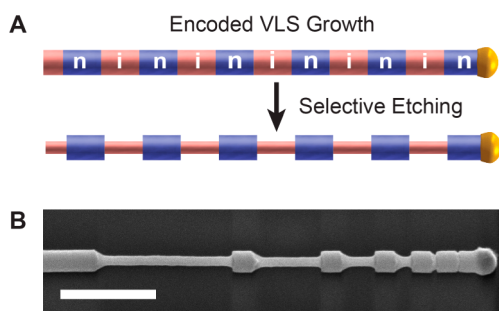
efforts to alter the NW geometry and encode specific morphology. For example, controlled vapor–solid overcoating on the NW surface has been used to create tapered structures<sup>25</sup> and periodic variations in morphology.<sup>26</sup> In addition, modulation of the size of the metal catalyst during growth has been shown to provide control over the NW diameter.<sup>27,28</sup> For metal NWs, electrodeposition in metal oxide templates followed by wet-chemical etching has been used to create wires with alternating, nanoscale structures.<sup>29,30</sup> However, a method for accurate, nanometer-scale control of morphology in single-crystalline semiconductor NWs has not been developed. Here, we demonstrate a new method to achieve this type of high-fidelity shape control, a process which we term “ENGRAVE” for “Encoded Nanowire Growth and Appearance through VLS and Etching.” The key aspects and capabilities of this method are illustrated in Figure 1 and elaborated below.

Si NWs were grown by a VLS mechanism in a home-built, hot-wall chemical vapor deposition (CVD) system at 420 °C using Au nanoparticles as catalysts, silane (SiH<sub>4</sub>) as the source of Si, and hydrogen (H<sub>2</sub>) as the carrier gas (see the Supporting Information for details). As illustrated schematically in Figure 1A, an additional flow of phosphine (PH<sub>3</sub>) was rapidly modulated during growth<sup>20</sup> to encode varying levels of P, an n-type substitutional dopant with high solubility in Si.<sup>31–33</sup> The etch rate of doped Si with aqueous KOH solution is well-known to decrease with higher dopant concentration.<sup>34</sup> This

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**Figure 1.** Synthesis of Si NWs with encoded morphology. (A) Schematic illustration of NW growth including rapid modulation of P dopant incorporation to form heavily doped n-type (n) and undoped intrinsic (i) segments that are selectively etched using wet-chemical methods to form a grating. (B) SEM image of a NW grating encoded (from left to right) with sequential intrinsic segments for 200, 100, 50, 25, 10, and 5 s; scale bar, 500 nm. Segments that are not etched correspond to an encoded n-type doping level of  $5 \times 10^{20} \text{ cm}^{-3}$ .

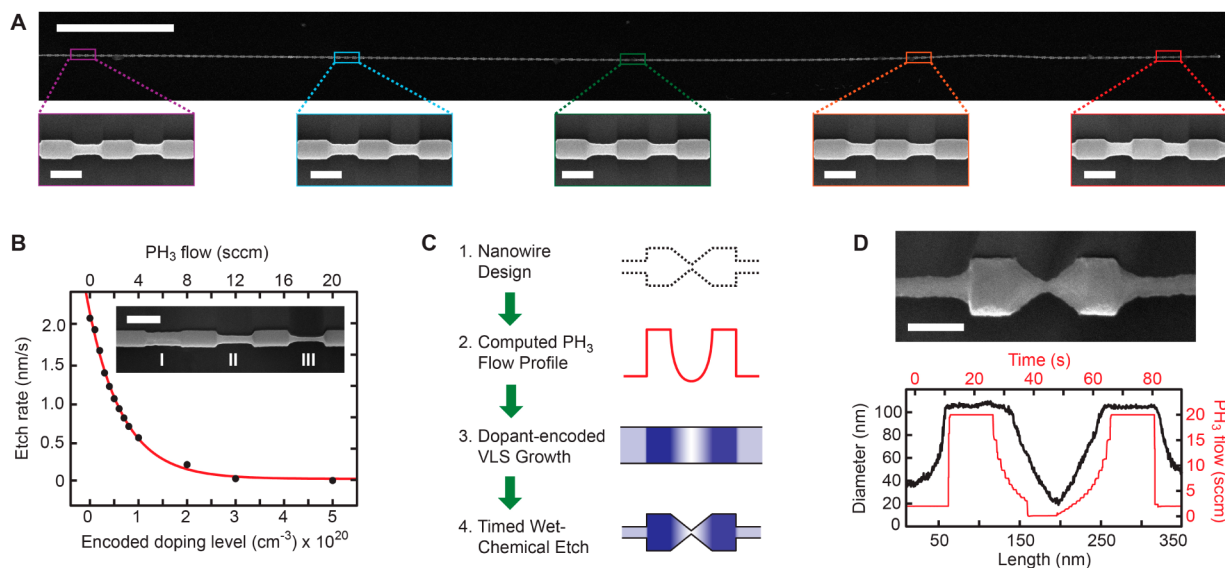
effect has been observed in doped Si NWs, producing changes in diameter along the axis.<sup>35,36</sup> Here, we develop this effect as a tool to encode arbitrary, high-resolution morphology along the NW growth axis, enabling new technological applications of Si NWs.

To delineate the spatial resolution of the ENGRAVE process, we synthesized NWs with six intrinsic segments encoded along the axis for increasingly short time scales. As shown in Figure 1B, wet-chemical etching of these segments yielded an abrupt and conformal reduction in the NW diameter. The largest segment, encoded for 200 s, produced a feature  $\sim 700$  nm in length while the smallest segment, encoded for 5 s, produced a feature  $\sim 10$  nm in length, defining the lower limit of the spatial resolution for this process. This example also demonstrates that

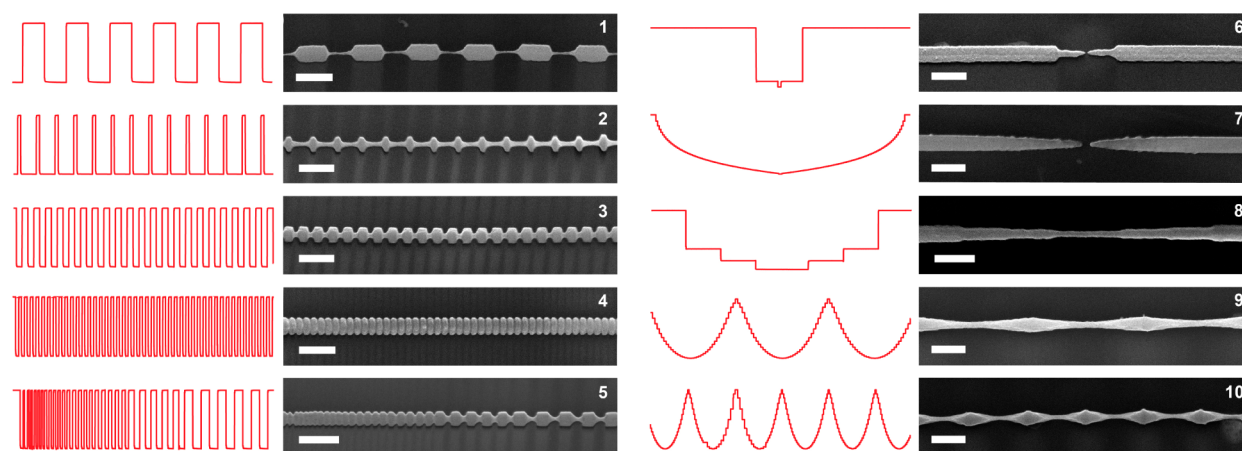
NW growth time is directly proportional to spatial length scale. Quantitative analysis of the NW growth rate yielded a value of  $213 \pm 6 \text{ nm/min}$ , which was used throughout this work to convert growth times to length scales. This rate is comparatively slow because of the low CVD temperature,  $420^\circ\text{C}$ , chosen to minimize radial overcoating and doping of the NW surface, a known problem during Si NW synthesis.<sup>37,38</sup> By minimizing the overcoating, we could encode high-fidelity nanoscale features over macroscopic length scales, as exemplified in Figure 2A by the 400 nm pitch grating encoded over  $50 \mu\text{m}$  of a single NW.

For the synthesis of NWs with complex morphology, we measured the etch rate of Si NWs encoded with P doping levels ranging from  $5.0 \times 10^{20}$  to less than  $1.0 \times 10^{19} \text{ dopants/cm}^3$ , as depicted in Figure 2B. Note that these doping levels were calculated from the gas-phase ratio of Si to P during CVD growth and the actual values could be lower as a result of incomplete P incorporation.<sup>33</sup> Quantitative evaluation of the etch rate reveals a nonlinear dependence on doping level that is well approximated with a single exponential function and varies from 2.1 nm/s for “intrinsic” segments with doping levels  $<1.0 \times 10^{19} \text{ cm}^{-3}$  to negligible etching ( $<0.1 \text{ nm/s}$ ) with heavily doped segments. The exponential dependence is most likely a result of the logarithmic dependence of the Fermi level position on the doping level, which modulates the rate of Si oxidation and dissolution at the semiconductor–solution interface.<sup>34</sup>

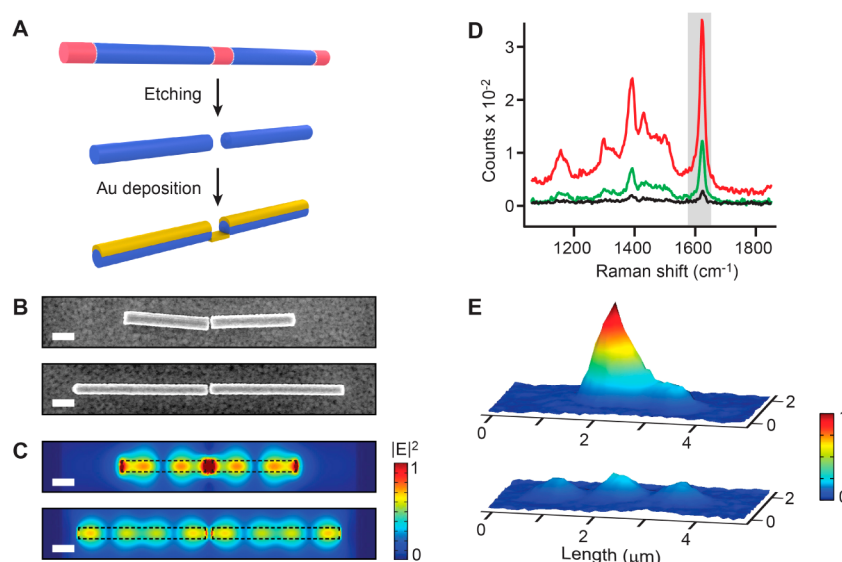
The precise calibration of the NW growth and etch rates enables rational design and synthesis of arbitrary high-resolution morphologies, as outlined schematically in Figure 2C. This process involves (1) design of the morphological profile, (2) conversion of the physical profile into a dopant profile, (3) VLS growth of the dopant-encoded NW, and (4) wet-chemical etching. As an example, we used this procedure to form the bowtie structure depicted in Figure 2D. The



**Figure 2.** Characterization of NW growth, etching, and morphology. (A) Upper: SEM image of a grating-encoded NW more than  $50 \mu\text{m}$  in axial length; scale bar,  $5 \mu\text{m}$ . Lower: Higher-magnification SEM images of select sections of the NW in the upper panel; scale bars, 200 nm. (B) Radial etch rate of Si NWs as function of encoded P doping levels. Red curve represents the best fit to a single exponential function. Inset: SEM image of a NW with segments I, II, and III encoded with P doping levels of  $1 \times 10^{20} \text{ cm}^{-3}$ ,  $5 \times 10^{19} \text{ cm}^{-3}$ , and intrinsic, respectively, and etched for 25 s; scale bar, 200 nm. (C) Schematic of the sequential process for bottom-up synthesis of complex NW morphologies. (D) Upper: SEM image of a NW encoded with a bowtie; scale bar, 100 nm. Lower: NW diameter (black curve and left-hand axis) as a function of length for the bowtie shown in upper panel, and measured phosphine flow rate (red curve and right-hand axis) in standard cubic centimeters per minute (sccm) as a function of time during CVD growth.



**Figure 3.** SEM images and phosphine flow profiles for the synthesis of Si NWs with complex morphology. The measured phosphine flow profile used to encode the morphology of each segment is depicted in red to the left of each SEM image; all scale bars, 200 nm. The flow rates vary from 0 to 20 sccm for each NW.



**Figure 4.** Nanogap-encoded NWs for plasmonics. (A) Schematic illustration of Au deposition on a nanogap-encoded NW. (B) SEM images of nanogap-encoded Si NWs with 50 nm Au, gaps of  $\sim 30$  nm, and segment lengths of  $\sim 775$  nm (upper image) and  $\sim 1175$  nm (lower image); scale bars, 200 nm. (C) Finite-element optical simulations of the Si/Au nanogap structures depicted in panel B showing the scattered field ( $|E|^2$ ) in the plane above the NW resulting from illumination at normal incidence with a transverse-magnetic plane wave at 633 nm. The optically excited SPP mode is on-resonance and off-resonance in the upper and lower images, respectively; scale bars, 200 nm. (D) Raman spectra of methylene blue collected from the planar Au film (black), the off-resonance NW (green), and on-resonance NW (red). The shaded region denotes the spectral range used to generate spatial maps of the Raman intensity. (E) Three-dimensional spatial maps of the relative Raman signal intensity generated by raster scanning a 633 nm laser over on- and off-resonance nanogap-encoded Si/Au structures with segment lengths of  $\sim 775$  nm (upper) and  $\sim 1175$  nm (lower), respectively.

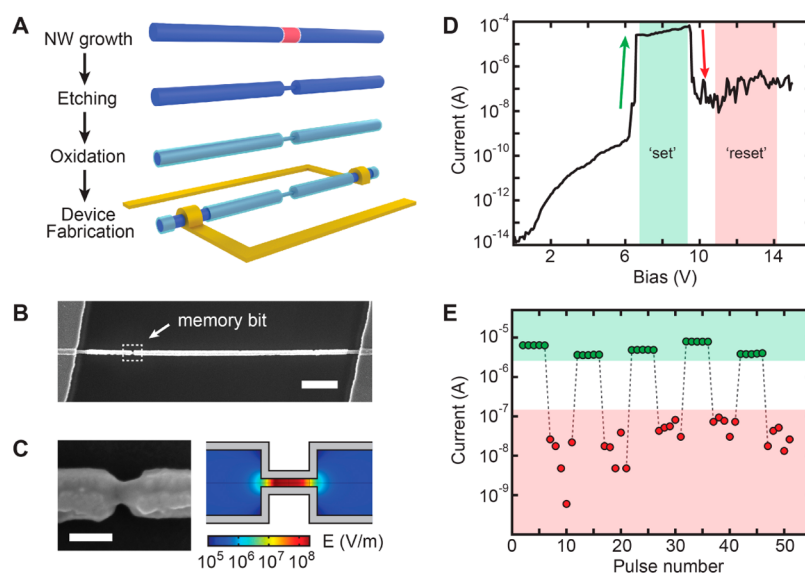
phosphine flow profile for the bowtie (red curve Figure 2D) is complex, requiring over 25 changes in flow rate over a time scale of one minute. The diameter profile (black curve Figure 2D) shows the resulting structure to be smoothly tapered with a monotonically decreasing then increasing diameter that reduces to a diameter of  $\sim 15$  nm at the narrowest point. Note that the phosphine flow profile was modified to be asymmetric around the flow minimum to account for dopants retained by the Au catalyst, a phenomenon termed the reservoir effect.<sup>39</sup>

We used other complex doping profiles to encode the range of morphological features shown in SEM images in Figure 3. These structures include periodic (images 1–4) or nonperiodic (image 5) gratings, nanogaps with gap sizes as small as 10 nm (images 6–7), suspended nanorods (image 8), and sinusoidal

profiles (images 9–10). These morphologies could each enable a different class of NW-based technology. For instance, the suspended nanorods could be used as mechanical oscillators for nanoelectromechanical systems,<sup>40</sup> periodic gratings for optical applications in nanophotonics,<sup>41</sup> and nonperiodic gratings as a method to control thermal transport along the wires, enabling the use of Si as a thermoelectric material.<sup>42,43</sup> Although full development of these applications is outside the scope of this work, below we highlight two distinct applications of ENGRAVE NWs in the areas of plasmonics (Figure 4) and electronics (Figure 5).

Field-enhanced spectroscopies, such as surface-enhanced Raman spectroscopy (SERS), are often performed using Noble metal nanostructures that support surface plasmon





**Figure 5.** Nanorod-encoded NWs for nonvolatile memory. (A) Schematic illustration of the sequential process used to fabricate a nonvolatile memory bit. (B) SEM image of a NW device encoded with a nonvolatile memory bit (dashed box) showing Ti/Pd Ohmic contacts on the far left and right; scale bar, 1  $\mu\text{m}$ . (C) Left: SEM image of the encoded memory bit corresponding to the dashed white box in panel B; scale bar, 100 nm. Right: finite-element simulation of the electric field magnitude across the NW at an applied bias of +8 V plotted in a logarithmic color scale for a nanorod segment 50 nm in length and 10 nm in diameter. (D) Characteristic switching  $I$ – $V$  curve for an ENGRAVE NW memory device. The shaded green and red regions define the set and reset bias ranges, respectively. (E) Resistive switching behavior over 10 memory cycles. Dashed lines represent the set/reset pulses between current readings, which were acquired five times at 1 V between each set or reset pulse.

polaritons (SPPs).<sup>44</sup> Through careful design of the shape of a nanostructure, SPP resonances can be used to confine and amplify incident electromagnetic fields at specific wavelengths and spatial positions.<sup>45</sup> Following the procedure depicted in Figure 4A, we used nanogap-encoded NWs as the topological templates for Noble metal films, creating Si/Au nanostructures with tunable SPP resonances. As shown by the SEM images in Figure 4B, deposition of  $\sim 50$  nm of Au on the NWs by physical vapor deposition preserved the high-resolution structures and nanogap morphology. We used finite-element optical simulations (see Figure 4C) to design Si/Au ENGRAVE structures with specific SPP characteristics. The NWs behave as plasmonic resonator antennas<sup>46</sup> in which the length of the segments adjacent to the gap control the field enhancement as a result of constructive or destructive interference of the SPP wave along the NW axis.<sup>47,48</sup> For a wavelength of 633 nm, we found that segments  $\sim 775$  nm in length were on-resonance, producing intense fields in the gap, while segments  $\sim 1175$  nm in length were off-resonance, exhibiting weaker field enhancement. As proof-of-concept, we performed SERS imaging on the Si/Au NWs coated with methylene blue.<sup>47,48</sup> We observed a greater than 10-fold Raman signal enhancement from the on-resonance structure, as shown by the spectra in Figure 4D. In addition, Raman imaging (see Figure 4E) confirmed that the signal enhancement is localized to a narrow spatial region around the gap. In comparison, the off-resonance NW shows a weak Raman signal arising from both the gap and the two ends of the rods, a result that is in good agreement with the optical simulations in Figure 4C. These results highlight the capability for ENGRAVE to serve as a simple route to create nanoplasmonic structures with tunable resonances for field-enhanced spectroscopy<sup>44</sup> and nanophotonic technologies.<sup>45</sup>

As a second application of ENGRAVE, we fabricated core/shell Si/SiO<sub>2</sub> nanorod-encoded devices for use as resistive switches in nonvolatile memory.<sup>6,49,50</sup> The fabrication steps are

illustrated in Figure 5A. First, an n-type/intrinsic/n-type NW was synthesized with a 50 nm intrinsic channel. Second, the channel was etched to a diameter of  $\sim 30$  nm to produce a suspended nanorod. Third, the wire was thermally oxidized to create a  $\sim 10$  nm diameter Si core encased by an oxide shell. Fourth, electrical contacts were fabricated to the two n-type segments adjacent to the intrinsic channel, as shown by the SEM image in Figure 5B. Device simulations<sup>51</sup> (see Figure 5C) indicate that this geometry concentrates the voltage drop and electric field within the narrow channel region, enabling a resistive switching effect as observed in a planar Si/SiO<sub>2</sub> system.<sup>49,50</sup> Initial current–voltage ( $I$ – $V$ ) measurements showed an Ohmic response from the device with a resistance of 54.7 k $\Omega$ , indicating the presence of a thin Si filament encapsulated by the SiO<sub>2</sub> shell. After an electroforming process consisting of multiple  $I$ – $V$  sweeps at high bias (see the Supporting Information for details), the device converged to the characteristic “switching”  $I$ – $V$  behavior (see Figure 5D) expected from a Si/SiO<sub>2</sub> system.<sup>49,50</sup> The  $I$ – $V$  curve exhibits a low voltage region that “sets” the device to a low resistance state and a high voltage region that “resets” the device to a high resistance state. In the latter state, we observed variations in the current (see Figure 5D,E) similar to those reported previously in Si/SiO<sub>2</sub> resistive switching memory devices and attributed to tunneling current fluctuations.<sup>49</sup>

To use the nanorod-encoded device as nonvolatile memory, we applied sequential “set” and “reset” voltage pulses (100  $\mu\text{s}$ ) of 8 and 12 V, respectively, to reversibly change the resistance of the device. As shown in Figure 5E, we cycled the NW device through ten memory states and achieved on/off current ratios of nearly  $10^2$ . With further development, we expect that at least 100 memory bits could be encoded on a single NW and lower voltage operation achieved with smaller nanorod segments. These initial results demonstrate the facile integration of ENGRAVE structures in electronic devices and furthermore

highlight the emergent electronic characteristics that can be encoded through morphology.

In summary, we have demonstrated a method, termed ENGRAVE, to encode high-resolution morphology along the growth axis of Si NWs. We anticipate this process will be extended to other NW materials, such as GaAs/GaP<sup>52,53</sup> et al., that can be selectively etched with gas-phase or wet-chemical techniques and will represent a general route to encode new functionality in semiconductor NWs.

## ■ ASSOCIATED CONTENT

### Supporting Information

Details of NW synthesis, characterization methods, device fabrication, and measurements. This material is available free of charge via the Internet at <http://pubs.acs.org>.

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### Author Contributions

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### Notes

The authors declare no competing financial interest.

## ■ ACKNOWLEDGMENTS

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