High-Performance OTFTs Using Surface-Modified Alumina Dielectrics

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We show novel and selective means to modify the dielectric surfaces in organic TFTs. Modification schemes include alkylphosphonic acid monolayers that have a strong affinity for alumina surfaces. Monolayers form robust, extremely uniform thin films and are deposited through simple spin-coating with a dilute solution of the monolayer precursor in solvent. Adding monolayers to organic TFTs has resulted in polycrystalline devices with mobilities nearly equal to single-crystal values while maintaining acceptable values of other device parameters (for example, the threshold voltage, on/off ratio, and subthreshold slope) required for fully functional integrated circuits.

Introduction

Organic semiconductors have seen wide publicity in potential applications ranging from photoconductors to large-area lighting and low-cost electronics to wallpaper TVs. In recent years, major advances in semiconductor performance^{1,2} and organic device construction^{3–8} have widened the number of projected applications and heightened the need for research into performance-enhancing treatments^{9–12} and processes, ^{13,14} operational testing, ¹⁵ and circuit design¹⁶ suitable for the range of performance seen in organic active materials.

It is widely accepted that polycrystalline films of pentacene, the material with the best thin-film transistor (TFT) performance to date, provide acceptable simple devices under the best preparation conditions and that improved performance, by any definition, may be necessary for the long-term operation of complex organic electronic devices. Well-controlled and optimized conditions result in TFT mobilities for pentacene devices ranging from 0.5 cm²/Vs to near the single-crystal mobility, $\sim\!\!3~{\rm cm}^2/{\rm Vs}$. High-end performance has been seen sporadically and generally only on test structures with the highest-quality device layers. A recent exception is a report by Infineon of a mobility of 3 cm²/Vs for pentacene TFTs with polymeric gate dielectrics. 17

Some research from Jackson's group⁹ has focused on the surface modification of high-quality thermal silicon oxides using organosilanes, resulting in pentacene device constructions with enhanced mobilities. PARC has recently extended this research to cover organosilanes bearing a variety of different terminal groups¹² to investigate the effects of surface energy on the performance of polymeric active layers. The results of these studies have been encouraging in terms of boosting device performance, but ultimately, chemistries are needed that will result in improved performance for lower-cost, more processible dielectrics such as alumina (Al₂O₃), which can be applied by plasma-enhanced chemical vapor deposition (PECVD), sput-

tering, or electron beam processes. In addition, alumina has a higher dielectric constant than silica and may provide a route to devices with lower operating voltages.

Our work shows the use of robust chemistry to form uniform, single monolayers on aluminum oxide dielectrics without the likelihood of side reactions and excess deposition that are often observed for silane-based chemistries. These side reactions may result in oligomeric material that is not surface-attached, leaving reactive groups exposed to the sensitive semiconductordielectric interface. 18,19 Using phosphonic acid-terminated materials results in stable, high-quality monolayer films without the possibility of side reactions and, once rinsed, without the presence of unbound reactive groups.^{20–23} Devices fabricated with pentacene deposited on top of self-assembled monolayers (SAMs) of alkanephosphonic acids display device mobilities rivaling single-crystal results, even in polycrystalline thin films. The chemistry and coating method is quick and facile and would lend itself well to an in-line manufacturing process. Our best results have been found on long (C₁₂-C₁₆) alkyl chain, methyl-, or phenyl-terminated phosphonic acid monolayers. These results are also general with respect to the organic semiconductor in that performance improvements have been demonstrated for 2,9dimethylpentacene and could be extended to sexithiophene, tetracene, perylene, and other modified pentacene materials.²⁴ We believe that this surface treatment method is generalizable to any organic semiconductor that grows with a lamellar or platelike habit and is also general for any aluminum oxide surface where the root-mean-square (rms) roughness is less than the thickness of the subsequently applied monolayer. Furthermore, phosphonic acid SAMs have an affinity for other transition-metal oxides that are widely used in electronic devices such as indium tin oxide (ITO). 20,25-27

Materials

Phosphonobenzene (mp $162-165\,^{\circ}\text{C}$) and 1-phosphonooctane (mp $99-102\,^{\circ}\text{C}$) were purchased from Aldrich Chemicals and Alfa Aesar, respectively, and were used as received. 1-Phosphonohexadecane was received from Oryza Laboratories as a mixture with the corresponding monoethyl and diethyl esters. A mixture of 5.0 g (approximately 14 mmol) of this material

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and 40 mL of concentrated hydrochloric acid was heated at 100 °C for 12 h. The mixture was diluted with 10 mL of water, and the precipitated solid was collected by filtration. Recrystallization from heptane afforded 3.27 g (76%) of white crystals, mp 95–97 °C. Other phosphonic acids were generally synthesized by a two-step procedure involving the initial reaction of the corresponding bromide or iodide with triethyl phosphite via the Arbuzov reaction.²⁸ Converting the resulting diethyl phosphonate esters to the free acids was effected in high yield under mild conditions by reaction with bromotrimethylsilane followed by methanolysis of the intermediate silyl ester.²⁰ The preparation of 1-phosphono-(3*R*/*S*,7*R*,11*R*)-3,7,11,15-tetramethylhexadecane is typical.

1-Phosphono-(3*R*/*S*,7*R*,11*R*)-3,7,11,15-tetramethylhexadecane. A mixture of 21.69 g (60 mmol) of 1-bromo-(3R/S,7R,-11R)-3,7,11,15-tetramethylhexadecane²⁹ and 25 g (150 mmol) of triethyl phosphite was heated at 150 °C. After 18 h, an additional 20 g (120 mmol) of triethyl phosphite was added, and heating was continued for 24 h. Diethyl ethylphosphonate and other volatiles were distilled (bp 30-50 °C at 0.05 mmHg), and bulb-to-bulb distillation of the concentrate provided 23.51 g (94%) of 1-(diethylphosphono)-(3R/S,7R,11R)-3,7,11,15-tetramethylhexadecane as a clear, colorless liquid, bp 190-230 °C at 0.05 mmHg. To a solution of 14.6 g (35 mmol) of the diethyl phosphonate ester in 40 mL of dichloromethane was added 15.0 g (98 mmol) of bromotrimethylsilane. After 24 h at room temperature, the solution was concentrated to a pale yellowish liquid, and the intermediate silyl phosphonate ester was dissolved in 100 mL of methanol. The resultant solution was stirred at room temperature for 30 min and concentrated to a white solid. Dissolution in methanol and concentration were repeated two times, and recrystallization from ethyl acetate afforded 10.5 g (83%) of 1-phosphono-(3R/S,7R,11R)-3,7,11,-15-tetramethylhexadecane as white crystals, mp 69–72 °C. ¹H NMR (CDCl₃): δ 0.86 (m, 15 H), 1.0–1.5 (m, 22 H), 1.52 (m, 1 H), 1.70 (m, 3 H), 11.12 (br s, 2 H); 13 C NMR (CDCl₃): δ 18.89, 18.95, 19.52, 19.59, 19.61, 19.65, 19.57, 22.56, 22.66, $22.70 (J_{CP} = 147 \text{ Hz}), 24.29, 24.41, 24.73, 27.89, 28.65 (J_{CP})$ = 5 Hz), 28.75 (J_{CP} = 5 Hz), 32.70, 32.71, 33.31 (J_{CP} = 17 Hz), 36.54, 37.21, 37.31, 37.33, 37.36, 37.38, 37.43, 37.47, 39.29; ³¹P NMR (CDCl₃): δ 38.81; HRMS: calculated for $C_{20}H_{44}O_3P$ (M + H⁺) 363.3028, measured 363.3030.

Methyl 11-Phosphonoundecanoate.³⁰ This compound was prepared from methyl 11-bromoundecanoate in 81% overall isolated yield. The final product was obtained as white crystals from a 4:1 mixture of water and 2-propanol, mp 84–88 °C. ¹H NMR (CDCl₃): δ 1.30 (m, 12 H), 1.60 (m, 4 H), 1.70 (m, 2 H), 2.32 (t, J = 6 Hz, 2 H), 3.64 (s, 3 H), 11.01 (br s, 2 H); ¹³C NMR (CDCl₃): δ 21.83, 24.72, 25.13 ($J_{\rm CP} = 145$ Hz), 28.81–29.14 (6 C), 30.22 ($J_{\rm CP} = 18$ Hz), 33.87, 51.25, 174.16; ³¹P NMR (CDCl₃): δ 32.67; HRMS: calculated for C₁₂H₂₆O₅P (M + H⁺) 281.1518, measured 281.1533.

1-Phosphono-1H,1H,2H,2H-perfluorodecane. This compound was prepared from 1-iodo-1H,1H,2H,2H-perfluorodecane in 48% overall isolated yield. The final product was obtained as white crystals from ethyl acetate, mp 170–178 °C. ¹H NMR (d_4 -methanol): δ 1.92 (m, 2 H), 2.44 (m, 2 H); ¹³C NMR (d_4 -methanol): δ 19.55 (d, J_{CP} = 142 Hz), 26.85 (t, J_{CF} = 22 Hz); ³¹P NMR (d_4 -methanol): δ 24.6; HRMS: calculated for C₁₀H₆F₁₆O₃P (M − F⁺) 508.9799, measured 508.9793.

13-Phenyl-1-phosphonotridecane. This compound was prepared from 13-phenyl-1-bromotridecane³¹ in 83% overall isolated yield. The final product was obtained as white crystals from acetonitrile or hexane, mp 83–86 °C. ¹H NMR (CDCl₃):

 δ 1.24 (m, 18 H), 1.62 (m, 4 H), 1.75 (m, 2 H), 2.60 (t, J=6 Hz, 2 H), 7.20 (m, 5 H), 10.27 (br s, 2 H); ¹³C NMR (CDCl₃): δ 22.26, 25.46 ($J_{CP}=147$ Hz), 29.31, 29.60–29.90 (7 C), 30.69 ($J_{CP}=18$ Hz), 31.78, 36.23, 125.77, 128.44 (2 C), 128.62 (2 C), 143.18; ³¹P NMR (CDCl₃): δ 38.63; HRMS: calculated for C₁₉H₃₄O₃P (M+H⁺) 341.2246, measured 341.2240.

16-Chloro-1-phosphonohexadecane. To a solution of 9.9 g (31 mmol) of 16-bromo-1-hexadecanol in 100 mL of dichloromethane at 0 °C was added 3.1 g (31 mmol) of triethylamine. A solution of 2.9 g (37 mmol) of acetyl chloride in 20 mL of dichloromethane was added dropwise, and the reaction mixture was stirred at 0 °C for 2.5 h. The solution was washed with water until the pH of the aqueous phase was approximately 6, and the organic phase was dried over MgSO₄. Concentration gave 10.5 g (93%) of crude 1-acetoxy-16-bromohexadecane, which was used without further purification. A mixture of 10.4 g (29 mmol) of the acetoxy derivative and 14.4 g (87 mmol) triethyl phosphite was heated for 48 h at 145 °C. Diethyl ethylphosphonate and other volatiles were distilled (bp 30-50 °C at 0.05 mmHg), and the crude concentrate of 16-acetoxy-1-(diethylphosphono)hexadecane was used without further purification. A mixture of 3.0 g (7 mmol) of the diethyl phosphonate ester and 35 mL of concentrated hydrochloric acid was heated at 100 °C for 9 days. The mixture was diluted with 20 mL of water, and the precipitated solid was collected by filtration. Recrystallization from a 15:1 mixture of heptane and 2-propanol afforded 1.35 g (44%) of 16-chloro-1-phosphonohexadecane as a white solid, mp 94–97 °C. ¹H NMR (d_8 tetrahydrofuran): δ 1.23, (m, 20 H), 1.40 (m, 4 H), 1.58 (m, 4 H), 1.74 (m, 2 H), 3.51 (t, J = 6 Hz, 2 H), 10.78 (br s, 2 H); ¹³C NMR (d_8 -tetrahydrofuran): δ 23.70 ($J_{CP} = 4$ Hz), 27.78, $27.82 (J_{CP} = 143 \text{ Hz}), 29.85, 30.24, 30.46 - 30.63 (8 \text{ C}), 31.64$ $(J_{\rm CP} = 17 \text{ Hz}), 33.64, 45.51; {}^{31}{\rm P} \,{\rm NMR} \,(d_8\text{-tetrahydrofuran}): \,\delta$ 36.25; HRMS: calculated for $C_{16}H_{35}ClO_3P$ (M + H⁺) 341.2012, measured 341.2015.

Sample Preparation

Alumina-coated (1500-Å) Si $\langle 100 \rangle$ wafers were obtained from Silicon Valley Microelectronics (San Jose, CA) with an Al gate layer (5000 Å) applied to the back side of each doped wafer. Wafers were quartered and cleaned using consecutive solvent rinses in acetone, methanol, 2-propanol, and water, blown dry in a stream of N₂, and finally exposed to 5 min of UV/ozone in a home-built chamber. Cleaned quarters were then coated with the SAM of interest by spin-coating a few milliliters of a 0.1 wt % solution of the monolayer precursor in ethanol (500 rpm for 5 s, 2000 rpm for 30 s). Samples were then baked on a vacuum hotplate at 150 °C for 3 min, rinsed with copious amounts of absolute ethanol, and blown dry with a stream of N₂.

Sample Characterization

Single-wavelength ellipsometry (Gaertner model L116-A), assuming a refractive index of 1.45 for the monolayers,³¹ was performed on each type of monolayer-coated sample. Water contact-angle measurements (AST Products, video contact-angle analyzer, model VCA-2500XE) were also obtained for each type of monolayer. Table 1 shows thickness and static, advancing, and receding water contact-angle (st/adv/rec WCA) data for selected phosphonic acid SAMs studied in this work. The compounds listed in Table 1 represent a wide range of terminal functional groups. Water completely wet the surface of cleaved silica and alumina controls, rendering contact-angle measurements impossible. The proximity of receding contact angle to

TABLE 1: Thickness and Water Contact-Angle Data of **SAMs and Surfaces**

sample	coating thickness (Å)	st/adv/rec WCA	
alumina control	N/A	<40/<40/<40	
silica control	N/A	<40/<40/<40	
13-phenyl-1-phosphonotridecane	17	88/91/79	
1-phosphonohexadecane	18	109/116/103	
1-phosphono-1H,1H,2H,2H- perfluorodecane	9^a	110/121/101	
phosphonobenzene	25^{b}	80/85/44	
methyl 11-phosphonoundecanoate	N/A	65/72/53	
16-chloro-1-phosphonohexadecane	N/A	83/86/79	
1-phosphono-(3 <i>R</i> / <i>S</i> ,7 <i>R</i> ,11 <i>R</i>)-3,7,11,15-tetramethylhexadecane	14	112/113/100	
1-phosphonooctane	10	107/113/101	

^a A refractive index of 1.35 was used to obtain the ellipsometric thickness for monolayers of 1-phosphono-1H,1H,2H,2H-perfluorodecane. b The thickness value for phosphonobenzene is higher than that expected for a single monolayer and is attributed to large crystals, observed by AFM, that formed on the surface during the coating process. This was the only coating observed to be nonuniform in film quality.

advancing contact angle is often viewed as an indication of film quality. On the basis of our understanding of the literature and AFM images of phosphonic acid coatings on alumina, the film quality of these samples was excellent, except for films formed from phosphonobenzene. Higher WCA samples gave the best device performance, as detailed in Table 2, with the exception of 1-phosphono-1H,1H,2H,2H-perfluorodecane. Data represented in Table 1 are averages of three measurements on each sample. Silica controls were included in order to compare our work with recent publications whereas alumina controls served as our reference point for comparison with the SAM-coated

AFM (Digital Instruments, Nanoscope IIIa) images were also obtained for bare alumina samples and samples of alumina coated with each monolayer to guarantee uniform coverage and to measure surface roughness. Single-crystal silicon tappingmode tips were obtained from MikroMasch (NSC12 UL-

TRASHARP cantilevers). Spring constants typically ranged from 4.5 to 14 N/m, and resonant frequencies, from 150 to 310 kHz (manufacturer's specifications). Images were obtained at scan rates of 1-2 Hz at a resolution of 512 lines per image.

Once monolayer-coated samples had been prepared, control samples were prepared using the same series of solvent rinses used for monolayer preparation, followed by 15 min of exposure to UV/ozone to ensure the removal of solvent residue. Control samples and monolayer-coated samples were then placed in a vacuum deposition chamber (2 \times 10⁻⁶ Torr), and pentacene (Aldrich, gradient sublimed under flowing H2 in N2 to a maximum temperature of 300 °C) was sublimed (260–300 °C) onto the samples as a blanket layer to a final thickness of ~ 300 Å, determined from AFM step-height images, at a rate of 0.1 Å/s. Alternatively, 2,9-dimethylpentacene, synthesized in-house and purified under similar conditions, was sublimed as a blanket layer. Coated samples were moved to a second vacuum chamber $(2 \times 10^{-6} \text{ Torr})$ where Au (600-Å thickness, as measured by a quartz crystal microbalance, QCM) was vapor deposited through a shadow mask to form source and drain contacts. The Al coating on the back side of the doped wafers serves as a field plane gate. Resultant TFTs had dimensions ranging from 40 to 100 μ m (channel length, L) by 1000 μ m (channel width, W).

Device Testing

TFTs were characterized using an HP Semiconductor Parameter Analyzer by sweeping the gate voltage, $V_{\rm g}$ (+10 to -40 V), and allowing the drain voltage, $V_{\rm d}$, to remain constant at -40 V. Device traces as shown in Figure 1 were obtained under ambient conditions for at least two separate pentacene depositions on each type of monolayer, and at least three TFTs were measured from each deposition run. Devices fabricated with 2,9-dimethylpentacene were tested in an identical fashion using only a single deposition run.

 $I_{\rm d}^{1/2} - V_{\rm g}$ and $I_{\rm d} - V_{\rm g}$ traces were used to estimate the saturation mobility (μ) , threshold voltage (V_t) , subthreshold slope (S), and on/off ratio. Table 2 shows averaged device properties for the

TABLE 2: TFT Data for Control and SAM-Coated Surfaces

sample	$V_{\rm t}\left({\sf V}\right)$	mobility (cm ² /Vs)	S (V/decade)	on/off	TFTs/ dep runs
		Pentacene			
alumina control	-6.0	1.1	0.9	6.1×10^{6}	39/6
silica control ^a	-9.4	1.0	1.6	3.0×10^{6}	40/6
13-phenyl-1- phosphonotridecane	-3.2	1.8	0.8	5.0×10^{6}	8/2
1-phosphonohexadecane (exceptional)	-7.8	3.3	2.9	1.6×10^{6}	8/2
1-phosphonohexadecane (overall average)	-5.0	2.0	5.1	2.4×10^{6}	34/10
1-phosphono-1H,1H,2H,2H- perfluorodecane	-3.5	0.4	7.5	1.5×10^{3}	11/3
phosphonobenzene	-4.5	0.1	2.4	3.6×10^{5}	8/2
methyl 11- phosphonoundecanoate	-7.3	0.4	3.8	3.0×10^{6}	9/3
16-chloro-1- phosphonohexadecane	-8.3	0.5	2.3	4.8×10^{5}	11/3
1-phosphono-(3 <i>R</i> / <i>S</i> ,7 <i>R</i> ,11 <i>R</i>)- 3,7,11,15-tetramethylhexadecane	+5.4	1.8	2.8	4.5×10^{5}	13/3
1-phosphonooctane	-2.1	2.1	2.5	1.7×10^{6}	12/4
	2	,9-Dimethylpentacene	1		
alumina control	-3.6	1.1	1.2	1.3×10^{6}	4/1
1-phosphonohexadecane	-3.8	2.5	1.9	2.0×10^{6}	6/1

^a Silica values differ significantly from the values reported in ref 24 because of the decreased surface roughness of the lot of silica wafers used for this study.

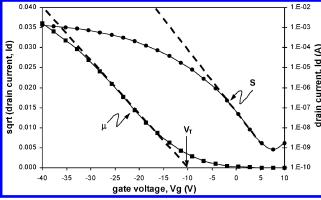


Figure 1. Representative TFT trace for pentacene deposited on an alumina sample treated with 1-phosphonohexadecane. V_d is held constant at -40 V, and V_g is swept from +10 to -40 V. A linear fit to the $I_d^{1/2}-V_g$ trace permits the extraction of saturation mobility and threshold voltage (V_0) (input parameters are W, L, and the specific capacitance of the oxide),³² and a linear fit to the I_d-V_g trace allows us to extract the subthreshold slope (S) and the on/off ratio. Device parameters for this sample were $\mu_{\rm sat}=3.4$ cm²/Vs, $V_{\rm t}=-10.4$ V, S=3.4 V/decade, and on/off = 1.5×10^6 . Traces similar to this one for identical voltage excursions were obtained for all of the samples listed in Tables 1 and 2.

same series of monolayers listed in Table 1 for both pentacene and 2,9-dimethylpentacene. All device results are compared with silica and alumina control values. Methyl-terminated and phenylterminated alkanephosphonic acid SAMs showed the largest increase in device performance, with improvements of about 200% (and even 300% in some cases) observed for longer alkyl and branched alkyl chains. Devices fabricated with 2,9dimethylpentacene as the semiconductor also showed a 200-300% improvement in the mobility. Smaller improvements were also observed for other treatments with various chemical terminations and chain lengths. Chlorine- and fluorineterminated monolayers, decreased the pentacene TFT performance. In general, and in basic agreement with prior work with silane monolayers, high water contact angles (80-115°) resulted in the largest performance increase. A slight increase in the subthreshold slope was observed for most of these surface treatments.³³ Currently, we do not have independent control of device parameters, but other materials and methods are under investigation toward this end. The fluorinated phosphonic acid monolayers, despite high WCAs, gave lower mobility. This suggests that chemistry as well as surface energy may be important in device performance. Low WCAs (40-80°) also appeared to decrease device performance. Additionally, preliminary results on sexithiophene devices indicate that the interaction of heteroatoms in the semiconductor with the surface treatment may also play a role. The data represented in Table 2 are averaged over the total number of TFTs measured in several pentacene depositions. This is indicated in the last column of the Table, where the total number of TFTs measured over the number of deposition runs included in the TFT count is indicated (e.g., the alumina control data can be interpreted as 39 individual transistors sampled over 6 separate pentacene deposition runs). Typically, four TFTs were measured on each sample. Two entries are listed for 1-phosphonohexadecane. The exceptional values occurred over two deposition runs where every TFT tested on each sample behaved with a mobility near 3 cm²/Vs. A larger number of deposition runs for the same surface coating (including the exceptional runs) gives an average device mobility of 2 cm²/Vs.

AFM images (Figure 2) of very thin (10-75 Å) layers of pentacene deposited on these monolayer coatings seem to

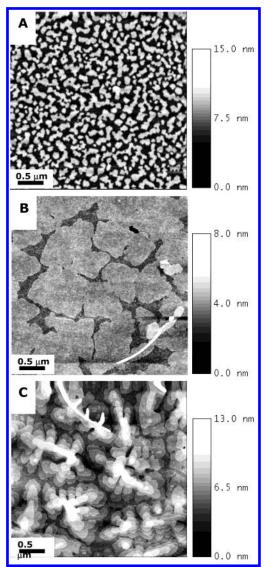


Figure 2. AFM images of 1-phosphonohexadecane with increasing coverage of pentacene. Bright areas in the images are interpreted as increasing film thickness in the z, or out-of-plane, direction. This series can be interpreted as snapshots of film growth. The thickness of these ultrathin layers was estimated using a QCM. (A) Initial stages (10-Å coverage) show a large number of nucleation sites and some lateral growth with no growth in the vertical direction. (B) With increasing coverage (35 Å), a second layer of material is nucleated on top of the first layer as the first layer continues to fill in laterally (lateral growth \gg vertical growth). (C) With further pentacene deposition (75 Å), the vertical growth rate seems to more nearly equal the rate of lateral growth in that several incomplete layers are manifested as lamellae in the pentacene grains.

indicate that sharp, well-defined grain boundaries are not present in the first few full monolayers of pentacene high-performance films. This could explain our near single-crystal pentacene mobilities in that the interfacial phosphonic acid SAM enables the formation of several molecular layers of near single-crystal-quality pentacene. A X-ray diffraction results of deposited films of pentacene (thickness $\sim 300 \text{ Å}$) indicate that we generally have films composed primarily of the thin-film phase (d spacing = 15.1 Å) with small fractions of the bulk phase present. By controlling the surface chemistry, we provide a smooth, passivated surface on which pentacene can grow, improving the crystallinity in the early stages of pentacene film growth. The result is a polycrystalline device in which the first few molecular layers have been engineered to give behavior approaching that

of a bulk, single-crystal device. It is widely believed that most of the charge transport in organic TFTs occurs in the first few monolayers of the semiconductor; therefore, the quality of these layers may very well determine the ultimate mobility as well as other relevant performance characteristics of the devices.

Summary

High-performance pentacene devices have been reliably fabricated using phosphonic acid-treated alumina dielectrics. Results indicate that both chemistry and surface energy play important roles in determining device performance. Furthermore, monolayers can be integrated into fully patterned device structures because of the affinity of phosphonic acids for alumina. Initial results show that these monolayers reduce the effective surface roughness on an electron beam-deposited oxide (rms roughness $\sim 1-2$ nm).³⁵ Chain length appears to be a major factor in optimizing the performance of rough oxides with these coatings and needs to be carefully investigated to realize equivalent improvements in fully patterned devices. If the chain is too short, then the oxides may not be fully passivated because of disorder in the coatings. Research continues in this area to find other useful forms of surface modification as well as to track the shelf life and operational stability of these devices. Additionally, research is underway to develop surface modification methods that provide independent control of device parameters (e.g., treatments that would provide a mobility of 2 and might allow one to change the threshold voltage from a positive to a negative value). Low-temperature, high-vacuum measurements and studies of the effects of surface roughness are also in progress in collaboration with the University of Minnesota.

Conclusions

We present here a survey of a method²⁴ that may be applied to create enhanced performance in organic semiconductor devices through the use of phosphonic acid chemistry to attach SAMs to relevant device oxides such as aluminum oxide. The coatings appear to provide a smooth, noninteracting surface on which the semiconductor can grow with near single-crystal quality for the first few full monolayers. Pentacene devices treated with 1-phosphonohexadecane have shown a 300% improvement in mobility over untreated control devices, and other phosphonic acid SAMs have reliably resulted in 200% improvement without sacrificing acceptable values for other important device parameters. In addition, this method has been applied to show similar improvements in the performance of devices fabricated with 2,9-dimethylpentacene and shows promise for other organic semiconductors including tetracene, perylene, and sexithiophene. Continuing research includes the integration of these and other surface treatment methods into devices fabricated with low-temperature materials and processes (for example, polymeric substrates and electron beam-deposited oxides).36

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