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Catalyst-free growth of uniform ZnO nanowire arrays on prepatterned substrate

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Uniform and ordered ZnO nanowire arrays have been fabricated on the nanopatterned SiO₂/GaN substrate without metal catalysts using hydrothermal synthesis. The nanopatterns on SiO₂/GaN substrate with an average diameter of 65 nm are produced by inductively coupled plasma etching using anodic alumina template as a mask, which provides nucleation sites for the vertical ZnO nanowires growth. High quality of the aligned uniform ZnO nanowire arrays grown on GaN substrate was confirmed by x-ray diffraction, transmission electron microscopy, and photoluminescence. This growth technique provides a cost-effective approach to fabricate ordered nanowire arrays with controlled size, which may benefit the nanowire device applications. © 2006 American Institute of Physics. [DOI: 10.1063/1.2425036]

In recent year, one dimensional ZnO nanostructures have been receiving particular attention due to its direct band gap of 3.37 eV and a large exciton binding energy of 60 meV, which make it a promising candidate for applications in blue-UV light emitting, field emission, and sensing. 1-3 Lots of efforts have been done to fabricate ZnO nanowires and nanorods with average diameters typically ranging over an order of magnitude from 20 to 200 nm, and lengths from hundreds of nanometers up to several micrometers by using catalytic growth via the vapor-liquid-solid mechanism, metal-organic chemical vapor deposition, pulsed laser deposition, and wet chemical methods. The ability to fabricate nanomaterials with a high degree of regularity and alignment is important to control the material properties and realize the functional device applications. It is reported that precoated buffer layers or textured seed layers have been used as substrates for improving the alignment of ZnO nanowires. 14,15 These precoated buffer layers or textured seed layers behave as the nucleus for growth of wires. Ordered ZnO nanowires embedded in anodic alumina template also have been fabricated. 16 In the common catalyst-assisted approach, the uniformity and alignment of ZnO nanowires can be improved by spatially selective deposition or prepatterning of catalyst. 17-20 The catalysts which are well defined by lithography or template-based nanopatterning including porous aluminum oxide template, self-assembly nanosphere lithography guided vapor-liquid-solid growth of nanowires. In this letter, we present an efficient technique to fabricate dense uniform ZnO nanowire arrays on prepatterned SiO₂/GaN substrate without a catalyst via wet chemical method. Similar as patterning of catalyst, the prepatterned GaN substrate provides nucleation sites for the vertical ZnO nanowire growth. Wet chemical method is considered to offer several advantages, such as large-area applicability, relatively low process temperature, and environmental friendliness. The nanopatterns on the substrate are created by etching substrate using anodic aluminum oxide (AAO) as a mask. AAO produced with ordered nanopores with a narrow distribution of tunable diameter pores ranging from ~10 to 500 nm is a promising template for nanofabrication. ^{21–24} Furthermore, the use of GaN layer also offers the advantage such as the controllability of electrical conductivity in a broad range, which allows for greater flexibility in device design, and the potential realization of a new class of vertical aligned nanoheterojunction devices. ^{25–27}

The general procedure to grow uniform ZnO nanowires is described as follows: A thin layer of SiO_2 (~50 nm) is deposited on the top of a 2 µm thick metal-organic chemical vapor deposition grown GaN film by plasma enhanced chemical vapor deposition, followed by evaporation of 1 μ m of aluminum onto the substrate. Then the aluminum film is anodized using a two-step anodization process using 0.3M oxalic acid at 40 V. CF₄-based inductively coupled plasma (ICP) etching is employed to transfer the nanopores from the AAO template to the SiO₂ film. After pattern transfer to the SiO₂ layer, the AAO template is selectively etched away in 5 wt % phosphoric. Then the nanopatterned SiO₂/GaN substrate is dipped in an autoclave. The details about hydrothermal synthesis of ZnO nanowires are similar to Ref. 28: Zinc acetate was employed as the Zn source, and the pH was adjusted to 10 using NH₄OH. The temperature was kept at 100 °C. The sample was characterized by field-emission scanning electron microscopy, transmission electron microscopy, x-ray diffraction, and photoluminescence (PL). PL spectra were recorded with a Renishaw 2000 micro-Raman-PL setup. The 325 nm line of the He-Cd laser was used as the source of excitation.

Figure 1(a) shows a SEM image of nanopatterns on SiO_2/GaN . It is reported that the AAO template is an effective mask for the plasma etching process.²⁹ The mean diameter of the pores as measured from SEM is ~ 65 nm with an average interpore distance of 110 nm. The pore density is

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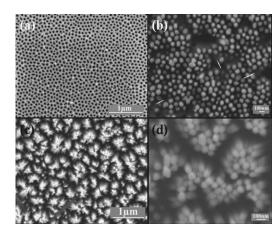


FIG. 1. (a) SEM image of nanopattern in the SiO_2/GaN substrate; (b) SEM image of initial ZnO nanostructure growth indicating the nucleation in the nanoopening of SiO_2 ; [(c) and (d)] SEM images of dense and uniform ZnO nanowire arrays.

about 10¹⁰ cm⁻². The diameter and the periodicity of the nanopores in SiO2 were directly transferred from the original AAO template due to the anisotropic ICP etching. The pore diameter and density in the AAO film can be easily controlled by tuning the anodization conditions such as applied voltage and electrolyte concentration. Compared to typical lithographic patterning approaches, such nonlithographic patterning based on self-organization processes offers a number of advantages for fabricating arrays of nanostructures, including the ability to pattern nanoscale features over large areas simultaneously, relative process simplicity, and low cost. Figure 1(b) shows the SEM image of the ZnO nanostructures in the initial growth stage. The size distribution of ZnO nanodot arrays mimics that of AAO template. Most of ZnO nanodots extrude out of SiO₂, only few still inside the nanopores of SiO₂ as pointed out by the arrows in Fig. 1(b). It is well known that the net free energy plays an important role in the aqueous system.³⁰ Initial nucleation will occur at lower net free energy sites. Precoating the substrate with a ZnO buffer layer before the hydrothermal synthesis to lower the energy barrier for nucleation is a well-developed technique to promote high-density nucleation and oriented growth of nanowires in the hydrothermal synthesis.³¹ ZnO and GaN have the same wurtzite-type structures with a low lattice mismatch. The net free energy for ZnO nucleus on GaN surface is much smaller than SiO₂ surface, which will favor the nucleation of ZnO on the patterned GaN surface in our experiment. These uniform ZnO nuclei will guide the growth to form the nanowire arrays, as shown in Figs. 1(c) and 1(d). The growth of ZnO is consistent with the model of ZnO crystal growth proposed by Li et al. 32 The hexagonal (0001) plane of ZnO with wurtzite structure is the closest packed plane in the crystal, and stacking along the [0001] direction becomes energetically favorable. The deposition based on GaN (0001) plane and the growth along the normal direction of the substrate surface were faster than the lateral one and resulted in that the crystals had a high aspect ratio shape. The ZnO nanowire arrays are grown over the entire substrate. The average diameter and density of ZnO nanowire arrays are ~ 65 nm and $10^{10}/\text{cm}^2$ defined by the nanopatterns in the SiO₂, which can be easily controlled by the AAO anodization conditions. The ZnO nanowires grown on GaN substrate are hexagonally shaped with flat and smooth surface as shown in the Fig. 1(d), strongly suggesting that

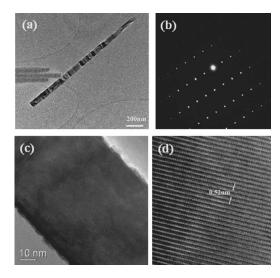


FIG. 2. (a) Low-magnification TEM image of ZnO nanowire grown on GaN substrate; (b) selective area electron diffraction pattern of ZnO nanowire; [(c) and (d)] high-magnification TEM image of a single ZnO nanowire.

these nanowires grow along the $\langle 001 \rangle$ direction. These nanowires with flat and smooth surfaces may have important implications when they are used as effective laser media. This c-axis oriented growth was also confirmed by long angle x-ray diffraction (XRD) θ -2 θ scan (not shown here). Only two ZnO/GaN (002) and (004) peaks were observed besides sapphire substrate. Since no catalyst was used in our hydrothermal synthesis, there is no metal catalyst detected in the XRD measurement.

TEM images including low-magnification image, high-resolution image, and SAED patterns of individual ZnO nanowire further confirm the structure of ZnO nanowire, as suggested by both SEM and XRD. Figures 2(a) and 2(b) show a low-magnification TEM image of an individual nanowire scratched from the substrate, and its selected area electron diffraction (SAED) pattern reveals the single crystal wurtzite structure of ZnO nanowire. The length and diameter of ZnO nanowire are about 2 μ m and 65 nm. Figures 2(c) and 2(d) show the high resolution TEM image of a nanowire, indicating that the growth direction of ZnO nanowire is.[0001]. No dislocations or stacking faults are observed in areas examined. The lattice spacing of 2.6 Å between adjacent lattice planes corresponded to the distance between two (002) crystal planes.

Room temperature photoluminescence spectroscopy was performed to investigate the optical properties of ZnO nanowire arrays. Strong exciton emission was observed with the peak at 378 nm with a FWHM of 10 nm, as shown in Fig. 3. The near UV emission is attributed to the excition transitions in the ZnO material. The narrow FWHM value of PL spectrum and the absence of the broad yellow band in the PL spectrum which is usually due to the recombination of a photogenerated hole with an electron occupying the oxygen vacancy indicate the high optical quality of ZnO nanowires using our method, which is due to lattice match between nanowires and substrate as well as the uniformity of the nanowires.

In summary, we have demonstrated the growth of dense and uniform single crystalline ZnO nanowire arrays on the prepatterned SiO₂/GaN substrate by using hydrothermal synthesis method. The nanopattern on SiO₂/GaN substrate is produced by ICP etching using anodic aluminat template as a

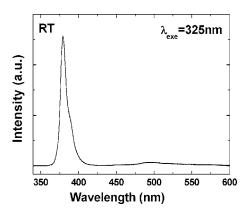


FIG. 3. Room temperature PL spectrum of ZnO nanowire arrays.

mask, which provides the nucleation of the vertical ZnO nanowire growth. The nanowire have uniform hexagonal structure with an average diameter of 65 nm and a length of about 2 μ m. The nanowires are of high crystallinity and good optical property. This fabrication process offers a simple and cost-efficient method to fabricate ordered nanowire arrays with controlled size by tuning the AAO template anodization conditions, which may benefit the nanowire device applications.

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