SUBJECT INDEX

Activities sequences			24	(1988)681
Alternating sequential-parallel systems			24	(1988) 95
Application layer protocol			24	(1988)111
Asymmetric invalidation			24	(1988)723
Asynchronous VLSI systems			24	(1988)347
Automatic protocol generation			24	(1988)111
Autonomous mobile robot			24	(1988)411
B-HIVE multicomputer			24	(1988) 79
Backtracking				(1988)607
Behavioral testing techniques				(1988) 63
Behavioural simulation			24	(1988)213
Binary search algorithm			24	(1988) 95
Boolean equation				(1988)767
Branch execution			24	(1988)573
Branch instructions execution			24	(1988)565
Branch target buffer			24	(1988)573
Butterfly network			24	(1988)353
BW-monitor design			24	(1988)717
Cache parameters			24	(1988)541
CAD tools			24	(1988)767
CAE-workstations			24	(1988)541
Chip image			24	(1988) 29
CMOS	24	(1988)	23,	233, 339
CMOS technology		. ,		(1988)637
Co-processor for unification			24	(1988)607
Communication circuit				(1988)103
Communication memory				(1988)503
Communication strategy				(1988) 11
Communication virtual processor				(1988)511
Compiler				(1988)471
Computational model				(1988)599
Computer architecture				(1988)309
Computer audio research laboratory				(1988)443
Computer music				(1988)443
Computer network				(1988)103
Computer networks				(1988)197
Computer science				(1988)835
Computer vision				(1988)403
Concurrent primitives				(1988)257
Concurrent program design		24 (8) 51, 809
Concurrent program flow checking				(1988)783
Concurrent system specification				(1988)483
Control				(1988)681
Controlled reduction model				(1988)607
Coprocessor				(1988)599
CPU-coprocessor communication and synchronization				(1988)695
of the coprocessor communication and symmetrication				(2000)
Data acquisition automation			24	(1988)549
Data searching				(1988)655
Database modeling				(1988)249
Databases				(1988)599
Dataflow				(1988)663
Deadline				(1988)491
Decision mechanisms				(1988)801
Declarative programming				(1988)663
2002-1-0 P-00				•

Deductive databases	24 (1988)599
DEOS	24 (1988)241
Design for testability	24 (1988)233
Design language	24 (1988) 35
Design methodology	24 (1988) 43
Design process	24 (1988)219
Design specification	24 (1988)219
Design tools	24 (1988) 29
Digital systems design	24 (1988)227
Distributed associative network	
_	24 (1988)613
Distributed computation	24 (1988)491
Distributed control	24 (1988)607
Distributed databases	24 (1988)249
Distributed operating system	24 (1988)169, 179, 187, 519
Distributed programs	24 (1988) 85
Distributed software fault-tolerant real-time	
Distributed systems	24 (1988)511, 731, 793
DSP	24 (1988)325
Education	24 (1988)757
Embedded ASICs	24 (1988)449
Embedded control systems	24 (1988)663
Embedded control systems programming	24 (1988)663
Embedded multiprocessor systems	24 (1988) 85
Embedded systems	24 (1988) 51, 775, 809
Error detection	24 (1988) 629
Error retracing	24 (1988)461
nite in the interest of the in	24 (1300)401
Fast Fourier transform	24 (1988)353
Fault diagnosis	24 (1988)527
Fault localization	24 (1988)461
	, ,
Fault modeling	24 (1988)233
Fault propagation	24 (1988)723
Fault recovery	24 (1988)775
Fault tolerance	24 (1988)519
Fault-tolerant	24 (1988)629
Fifth compiler	24 (1988) 73
Fixed point processor	24 (1988)695
Floating point coprocessor	24 (1988)695
Formal description language	24 (1988)363
Formal description techniques	24 (1988) 51, 809
Function language	24 (1988)227
Functional microprocessor architecture	24 (1988)149
Functional programs	24 (1988)613
Fuzzy C-means algorithm	24 (1988)647
Fuzzy clustering	24 (1988)647
Galileo nets	24 (1988)483
Global famout correction	24 (1988) 43
Global optimization	24 (1988)339
Grinding machine control system	24 (1988)663
J	_ (,
Hardware description languages	24 (1988)219, 371
Hardware realization of systolic algorithms	24 (1988)527
Hardware simulation environment	24 (1988)291
Hexagonal arithmetic array processors	24 (1988)629
Hierarchical design	24 (1988) 29
Hierarchical physical design system	24 (1988)131
High level debugger	24 (1988)153
High level language	24 (1988) 57, 85
HLL directed architecture	24 (1988) 37, 83
Hough transform	
Human interfaces	24 (1988)403 24 (1988)153
	24 (1988)153
Hypercube architectures	24 (1988)213
IBM-PC	0/ /1000\005
	24 (1988)325
Image processing	24 (1988)309
Image registration system	24 (1988)315

Industrial controllers	24 (1988)775
Instruction set processor	24 (1988)149
Interactive programming tool	24 (1988) 73
Interconnection networks	24 (1988)731
Intermodel transformation	24 (1988)249
International banking	24 (1988) 11
Kernel	24 (1988)161
Knowledge based systems	24 (1988)315
Language coprocessor	24 (1988)701
Laser radar	24 (1988)411
Learning	24 (1988)681
Local area networks	24 (1988) 95, 761
Local optimization	24 (1988)339
Logic design	24 (1988) 35
Logic synthesis	24 (1988) 43, 125, 339
LOTOS	24 (1988)483
	0/ /1000\/01
Matrix equations	24 (1988)681
Memory management unit	24 (1988)709
Message passing	24 (1988)519
Message-passing hardware	24 (1988)503
Microprocessor simulation	24 (1988)149
Micro systems architecture	24 (1988)103
Microcode	24 (1988) 371
Microcode compaction	24 (1988) 17
Microprocessor debugging	24 (1988)149 24 (1988) 85
Microprocessor network	24 (1988) 371, 775
Microprocessors	24 (1988)371, 773
Modula-2 Multi level design verification system	24 (1988)137
Multi-microprocessor environment	24 (1988)581
Multi-transputer architectures	24 (1988)281
Multi-transputer net	24 (1988)291
Multilevel digital simulation	24 (1988)299
Multilevel system software	24 (1988) 63
Multiple fault diagnosis	24 (1988)731
Multiplication algorithm	24 (1988)637
Multiprocessor	24 (1988)103, 389, 503, 599
Multiprocessor architecture	24 (1988)673
Multiprocessor environment	24 (1988)819
Music experiment	24 (1988)443
Music processing	24 (1988)435
Music publishing	24 (1988)549
Musical instrument simulator and workstation	24 (1988)555
Numerical stability	24 (1988)527
-1	A. (4000: C
Oberon	24 (1988) 3
Object-oriented program design	24 (1988)257
Object-oriented system	24 (1988)241
	205, 291, 397, 491, 673, 757
Optimization	24 (1988)541
OR-ed dataflow	24 (1988)663
Davida alamiaha	24 (1000)(55
Parallel algorithms	24 (1988)655
Parallel architectures	24 (1988)197
Parallel computer architectures Parallel computing	24 (1988)363 24 (1988)535
Parallel computing Parallel event administration	24 (1988)687
Parallel neural network simulation	24 (1988)621
Parallel parsing	24 (1988)819
Parallel processing	24 (1988)291, 747
Parallel processing system	24 (1988)511
Parallel programming	24 (1988)757
Parallelism	24 (1988)599
Parallelizing compiler	24 (1988) 79
•	, , ,

PCTE	24 (1988)161
Performance	24 (1988)519
Performance evaluation	24 (1988)197
Performance studies	24 (1988)281
Petri net	24 (1988)471
Petri net semantics	24 (1988)267
Petri net models	24 (1988)197
Pipelined architecture	24 (1988)709
Polymorphic-torus architecture	24 (1988)737
Power applications	24 (1988)589
Predicate/transition sets	24 (1988)267
Process structure	24 (1988)187
Process technology	24 (1988) 29
Processor array	24 (1988)655
Processor interconnections	24 (1988)197
Programming paradigms	24 (1988)535
Prolog machine	24 (1988)607
Protocol description	24 (1988)205
Protocol design	24 (1988)483
Prototyping language	24 (1988)471
Quantitativa analysis	2/ /1000\107
Quantitative analysis	24 (1988)197
Racah coefficients	24 (1988)535
Reachability analysis	24 (1988)809
Real-time	24 (1988)491, 519, 757
Real-time applicationa	24 (1988)717
Real-time process control and supervision	24 (1988)829
Real-time programming	24 (1988) 73
Real-time speech-synthesis	24 (1988)325
Real-time structured analysis	24 (1988)449
Real-time system design	24 (1988)379
Real-time systems	24 (1988)257, 687
Real-time systems education	24 (1988)835
Real-time data processing	24 (1988)411
Recognition system	24 (1988)425
Reduction machine	24 (1988)389
Relational database system	24 (1988)555
Risc architecture	24 (1988)565
Risc biprocessor architecture	24 (1988)589
Risc for image generation	24 (1988)581
Risc processor	24 (1988)573
Robot-arm control	24 (1988)673
Robotics	24 (1988)419
Robust software design	24 (1988)793
Cofety exities and instinct	2/ /1000\271
Safety-critical applications Scheduling	24 (1988)371 24 (1988)681
Self-checking capability	24 (1988)637
Server-oriented systems	24 (1988)179
Simulation	24 (1988)205, 681
Simulation results evaluation	24 (1988)461
Single process multitasking system	24 (1988) 3
Single-chip microcomputers	24 (1988)783
Software development	24 (1988)179
Software engineering environment	24 (1988)161
Software training environment	24 (1988)761
Sparse matrix techniques	24 (1988)621
Specification and verification	24 (1988)371
Speech processing	24 (1988)397
State space modelling	24 (1988)793
Static and dynamic links	24 (1988)281
Stochastic force sensing	24 (1988)419
Strategical level	24 (1988)681
String processor architecture	24 (1988)747
Supercomputing	24 (1988) 17
Symbolic processing	24 (1988)599

Synchronization System behaviour System faults diagnosability System level design System level language System logic System programming System structure System structuring System timing Systems modelling and analysis Systolic arrays	24 (1988)187 24 (1988)227 24 (1988)723 24 (1988)449 24 (1988) 57 24 (1988) 29 24 (1988) 57 24 (1988) 27 24 (1988)187 24 (1988)347 24 (1988) 51, 809 24 (1988)527
Tactile data Test pattern generation Testability Text and graphics integration Timing correction Timing correction algorithm Timing verification Topology Transformation rules Translent fault tolerance Translation lookaside buffer Transputer Transputer network Transputers	24 (1988)425 24 (1988)233 24 (1988)275 24 (1988)455 24 (1988) 43 24 (1988)125 24 (1988)125 24 (1988)275 24 (1988)397 24 (1988)775 24 (1988)775 24 (1988)709 24 (1988)607 24 (1988) 73, 169, 389 24 (1988)299 24 (1988)275, 397, 403, 535, 673, 757
UNIX Unlimited vocabulary	24 (1988)761 24 (1988)325
Validation and verification Vector processing Vectorized superminicomputer Verification VLSI VLSI CAD tools VLSI design VLSI design methodology VLSI implementation VLSI serial multiplier VLSI systolic architecture VLSI-/370 microprocessor	24 (1988)809 24 (1988)743 24 (1988)137 24 (1988)209 24 (1988)455 24 (1988)371 24 (1988)371 24 (1988)737 24 (1988)637 24 (1988)637 24 (1988)647 24 (1988) 23, 29, 35, 43, 125, 131, 137
Wisdom Workstations WP-calculus	24 (1988)169 24 (1988) 3, 11 24 (1988)491