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# Microstructure Evolution and Device Performance in Solution-Processed Polymeric Field-Effect Transistors: The Key Role of the First Monolayer

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**S** Supporting Information

**ABSTRACT:** Probing the role of the first monolayer in the evolution of the film polymer microstructure is essential for the fundamental understanding of the charge carrier transport in polymeric field-effect transistors (FETs). The monolayer and its subsequent microstructure of a conjugated polymer [poly(2,5-bis(3-alkylthiophen-2-yl)thieno[3,2-*b*]thiophene), PBTtT] film were fabricated via solution deposition by tuning the dip-coating speed and were then studied as accumulation and transporting layers in FETs. Investigation of the microstructure of the layers prepared at different coating velocities revealed that the monolayer serves as an important base for further development of the film. Significant improvement of the charge carrier transport occurs only at a critical multilayer network density that establishes the required percolation pathways for the charge carriers. Finally, at a low dip-coating speed, the polymer chains are uniaxially oriented, yielding pronounced structural anisotropy and high charge carrier mobilities of  $1.3 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$  in the alignment direction.

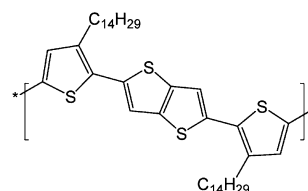
During the past decade, organic field-effect transistors (OFETs) have attracted considerable attention because of their potential applications in large-area, low-cost, flexible electronic devices, such as flexible electronic paper, radio-frequency identification (RFID) tags, postage stamps, and backplane circuitry for active-matrix displays.<sup>1</sup> Particularly, ultrathin-film OFETs with few semiconducting monolayers are of vital importance, because charge carriers are directly transported to conduction channels without diffusion through a dense film. For small conjugated molecules<sup>2a–i</sup> and very recently for a conjugated polymer,<sup>2j</sup> it has been proven that the main charge carrier transport in transistors occurs in a few molecular layers near the dielectric surface. This is also in line with the theory that predicts a high density of charges and thus high charge carrier mobility in the first few nanometers of the active film.<sup>2k</sup> Therefore, particular emphasis was put on the molecular order within this thin accumulation layer processed, for example, by vacuum deposition,<sup>2b,f</sup> Langmuir–Blodgett deposition,<sup>2c</sup> and electrostatic force-based self-assembly.<sup>2d</sup> Little is known, however, about the influence of solution processing (considered to be the future process in roll-to-roll

fabrication of electronic devices) on the molecular organization in ultrathin films after solvent evaporation.

There have been only few studies on conjugated polymers in ultrathin-film FETs, consisting of multilayers of poly(3-hexylthiophene)<sup>3a,b,d</sup> and polydiacetylene.<sup>3c</sup> Both cases showed a performance far inferior to the corresponding thick-film OFETs, proving that potential applications are a long way off. The solution processing of conjugated polymers into one single monolayer and its subsequent layers directly on the surface in a FET channel is a great challenge and therefore has been rarely reported to date. Especially, technical questions concerning precise bottom-up solution growth of a conjugated polymer from monolayer to multilayer still need to be answered. This would allow a fundamental study of the role of the first monolayer on the evolution of the bulk polymer microstructure and the charge carrier transport in the transistor.

This communication reports the early stages of polymer film formation that is precisely controlled via a facile dip-coating process by tuning the pulling speed of the substrate from the solution reservoir. As a model compound, the well-known high-performance p-type polymer poly(2,5-bis(3-alkylthiophen-2-yl)thieno[3,2-*b*]thiophene) (PBTtT, Scheme 1) was used.<sup>1d</sup> A

**Scheme 1. Molecular Structure of PBTtT**



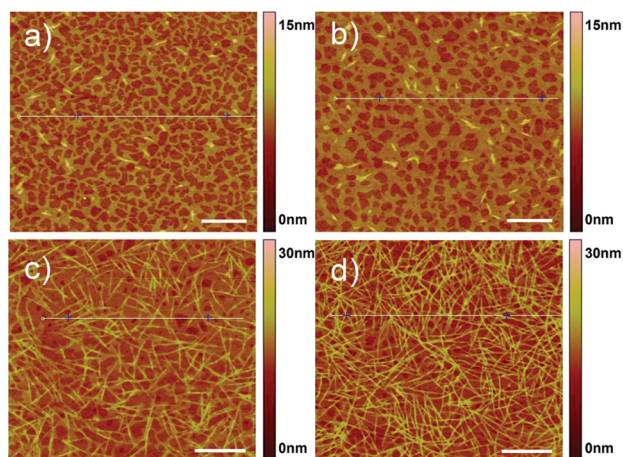
monolayer and the subsequent microstructure of a conjugated polymer on a rigid OFET surface were successfully obtained from solution. We prove that the first monolayer has essential importance for the bulk microstructure evolution, whereby a critical multilayer network is necessary for creating the required percolation pathways for the charge carriers in thin-film polymer OFETs.

During the dip-coating process, the pulling speed was gradually changed and had a great impact on the growth of

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PBTTT, mainly on the network density in the corresponding molecular layers as well as on their different morphology development. Remarkably, the monolayer and a subsequent network were formed over large areas by dip-coating, as proven by atomic force microscopy (AFM) images (Figure 1).



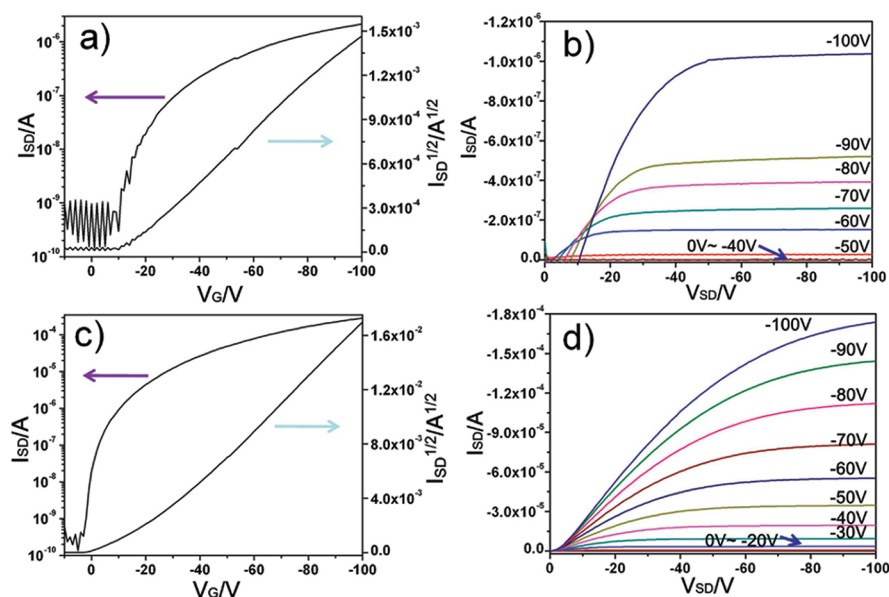
**Figure 1.** Tapping-mode AFM images of PBTTT on a SiO<sub>2</sub> surface obtained by dip-coating under different pulling speeds: (a) 1 mm/s; (b) 200 μm/s; (c) 40 μm/s; (d) 10 μm/s. The white lines in the AFM images correspond to the height profiles in Figure S1. Scale bars: (a, b) 500 nm; (c, d) 1 μm.

As shown in Figure 1a and Figure S1a in the Supporting Information (SI), a discontinuous monolayer network of PBTTT with a thickness of 1.8 nm was formed on SiO<sub>2</sub> surface when the substrate was pulled at the highest speed of 1 mm/s. This height is in agreement with the chain-to-chain distance determined by X-ray scattering for a bulk film (see below). On top of this 2D monolayer, 1D fiber nuclei (thickness of 4–6 nm) started to grow only on top of the PBTTT monolayer and not on the SiO<sub>2</sub> surface (Figure 1a). This is noteworthy since the subsequent microstructure began to develop while the first monolayer was not fully established

(~30% coverage). Moreover, a striking difference between the first layer and the subsequent microstructure was obvious and is attributed to the different surface tensions of PBTTT molecules and SiO<sub>2</sub>. The differences in the contact angle were <10° for SiO<sub>2</sub> and 102 ± 1° for the PBTTT layer. More precisely, in comparison to bare SiO<sub>2</sub> surface, the long alkyl chains of PBTTT lead to stronger hydrophobic interactions between the deposited polymer and the first layer.<sup>4</sup> As a result, PBTTT self-assembles into isolated anisotropic 1D fibers on top of the first monolayer. The phenomenon of different morphologies for the first monolayer and the subsequent microstructures is analogous to the previous findings on small molecules.<sup>5,2f</sup> For instance, in the case of pentacene, the first monolayer shows a higher nucleation density than the subsequent monolayers, since the diffusion coefficient of pentacene molecules on SiO<sub>2</sub> is lower than that on pentacene.<sup>5</sup>

The well-controlled growth of the polymer film allowed a systematic investigation of the charge carrier transport in the charge accumulation and transporting layer of an OFET. Details of the device fabrication are described in the SI. The OFET devices were studied only in a top-contact configuration to exclude differences in surface energy between bottom Au electrodes and the SiO<sub>2</sub> surface and to avoid any discontinuities in the film. For instance, in the case of untreated SiO<sub>2</sub>, nucleation of pentacene takes place preferentially at the electrodes, which causes discontinuities and clustering in the pentacene film.<sup>2f</sup> Almost no field-effect characteristics (<10<sup>-6</sup> cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup>) were observed for the monolayer shown in Figure 1a. It must be emphasized that a prerequisite for efficient charge carrier transport is a continuous long-range percolation path between the source and drain electrodes with intimately connected molecules. However, this is not the case for the layer in Figure 1a.

Decreasing the pulling speed to 200 μm/s led to coverage of a larger area by the first monolayer and therefore a more continuous film (Figure 1b and Figure S1b). In addition, the subsequent fiber nuclei (thickness does not change) became longer but were still not interconnected. The extended first monolayer and grown subsequent fiber nuclei are attributed to



**Figure 2.** (a, c) Transfer and (b, d) output curves corresponding to the PBTTT films processed by dip-coating at (a, b) 40 and (c, d) 10 μm/s.

the lower substrate speed, which ensures the availability of more molecules for building up the microstructures on the SiO<sub>2</sub> surface. It is also known from the literature that only an ineffective connection between the electrodes and a single layer of molecules can be achieved, limiting the charge carrier injection.<sup>6</sup> These are the reasons for a low charge carrier mobility of  $1.6 \times 10^{-4} \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$  and on/off ratio of  $\sim 10^2$  for the corresponding transistor (Figure S2). A further proof of ineffective charge carrier injection is the relatively high threshold voltage of  $-20 \text{ V}$ . These results are in agreement with previous findings showing that a monolayer of a small molecular semiconductor is not sufficient for high performance.<sup>6</sup> Despite the low performance, we define this mobility as an onset value for a minimal film thickness/microstructure of PBTtT for a working transistor.

With a further decrease in the pulling speed to  $40 \mu\text{m/s}$ , the PBTtT monolayer almost completely covered the SiO<sub>2</sub> surface (Figure 1c). In comparison to Figure 1b, the nucleation centers of the subsequent microstructure further developed into long fibers that formed a dense network with interconnections. As a result, the charge carrier mobility was 1 order of magnitude higher,  $2.0 \times 10^{-3} \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ , with an on/off ratio of  $\sim 10^4$  in comparison to the previous film. (Figure 2a,b) Additionally, the threshold voltage was reduced from  $-20 \text{ V}$  to  $-12 \text{ V}$ . The effect of contact resistance and problems with charge injection were clearly indicated by both the transfer and output curves. (Figure 2a,b)

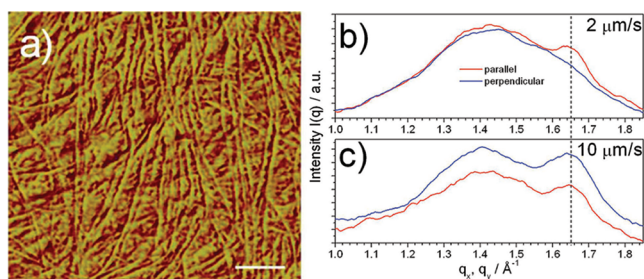
When the speed of the substrate was further reduced to  $10 \mu\text{m/s}$ , a closed first layer and a continuous subsequent fibrous multilayer were obtained (Figure 1d). As shown in Figure 2c,d, a mobility of  $0.36 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$  with an on/off ratio of  $1 \times 10^6$  were determined. Moreover, the threshold voltage was again lowered, from  $-12 \text{ V}$  to  $-5 \text{ V}$ . The significantly improved mobility and threshold voltage of this multilayer are attributed to the formation of a dense fibrous network layer. Such a network structure is ideal for OFET applications because the interconnected fiber structure enhances the charge carrier transport within the FET channel.

Finally, at the lowest speed of  $2 \mu\text{m/s}$ , long-range-aligned, continuous polymer fibrous layers were obtained for films with a total thickness of  $\sim 15 \text{ nm}$  over a macroscopic scale (Figure 3a

pulling speed), conjugated donor–acceptor polymer chains are directed in the processing direction, in which the maximum charge carrier mobility was recorded.<sup>7</sup> Previously, PBTtT was oriented via a flow-coating method.<sup>8</sup> In the case of dip-coating at  $2 \mu\text{m/s}$ , the long-range-aligned PBTtT films yielded a high average mobility of  $0.7 \pm 0.2 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$  with a highest mobility of  $1.3 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$  and a current on/off ratio of  $5 \times 10^6$  measured parallel to the coating direction. It must be emphasized that this value is the highest published to date for PBTtT-based FETs. Remarkably enough, the dip-coated films were simply deposited on untreated SiO<sub>2</sub> dielectric, which is known as serious charge carrier trap.<sup>9</sup> Furthermore, the films were annealed far below the liquid-crystalline phase transition, above which typically the molecular packing improves significantly and for PBTtT leads to better device performance. In this case, however, the film dewetted at an annealing temperature of  $180^\circ\text{C}$  because of the low thickness, forming a discontinuous network (Figure S5) and revealing much lower charge carrier mobility of  $0.02 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ . Such a film topography of PBTtT after heating to the liquid-crystalline phase is in agreement with the literature.<sup>10</sup> Another remarkable effect is the pronounced anisotropy of the electronic properties. The FET measurement perpendicular to the alignment direction gave charge carrier mobilities of only  $6.2 \times 10^{-2} \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ . This is a significantly larger anisotropy ratio of  $\sim 20$  in comparison with the value of  $\sim 2$  in our previous work on a donor–acceptor polymer.<sup>7</sup> To prove the structural isotropy for the layers processed at  $10 \mu\text{m/s}$  or faster relative to the anisotropy obtained by dip-coating at  $2 \mu\text{m/s}$ , the films were studied by grazing-incidence wide-angle X-ray scattering (GIWAXS). All of the patterns revealed reflections on the  $q_z$  line having identical positions characteristic of a chain-to-chain distance of  $2.2 \text{ nm}$  between edge-on arranged PBTtT polymer chains (Figure S4). However, differences were obvious for the reflections attributed to the  $\pi$ -stacking of  $0.38 \text{ nm}$ , which were located on the  $q_y$  line. Because of the uniaxial orientation of the conjugated polymers along the drawing direction of the substrate at the processing speed of  $2 \mu\text{m/s}$ , the scattering intensity was significantly higher for the measurement parallel to the dip-coating direction, while it disappeared when the sample was investigated perpendicular to this direction (Figure 3b). In contrast to this, the intensities of the  $\pi$ -stacking reflections in the two directions were the same for the film processed at  $10 \mu\text{m/s}$  (Figure 3c), confirming the structural isotropy. This isotropy is in good agreement with the microstructure in the AFM image in Figure 1d.

The GIWAXS analysis allowed us to develop the following model for the organization in the various layers of the thin film, which is presented schematically in Figure 4. The monolayer at the dielectric surface consists of edge-on arranged polymer chains, as suggested by the AFM images. On top of this first layer, fibers are formed in which the edge-on polymer backbones are oriented along the fiber axis. This corresponds also to the growth fiber direction and the faster charge carrier migration. In these  $4\text{--}6 \text{ nm}$  thick fibers, the polymer out-of-plane chain-to-chain spacing is  $2.2 \text{ nm}$ , which is in a range identical to the monolayer thickness. This means that in the fiber two or three polymer chains are arranged on top of each other as illustrated in Figure 4.

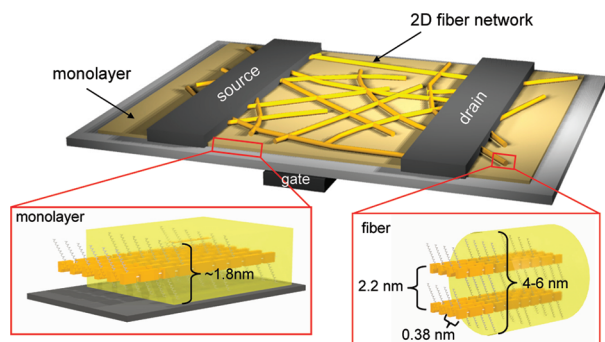
In conclusion, a monolayer and the subsequent microstructure of a conjugated polymer on a rigid surface can be precisely controlled by dip-coating. These results point toward an essential role of the first polymer monolayer on the



**Figure 3.** (a) AFM image of the uniaxially oriented film dip-coated at  $2 \mu\text{m/s}$  (the scale bar corresponds to  $1 \mu\text{m}$ ). (b, c)  $q_x/q_y$  integrations for the scattered intensities recorded perpendicular (blue) and parallel (red) to the dip direction. Films were obtained at (b)  $2 \mu\text{m/s}$  and (c)  $10 \mu\text{m/s}$ . The dashed lines indicate the position of  $\pi$ -stacking reflections.

and Figure S3). This pronounced orientation was induced by the low pulling speed and by the gradient established in the meniscus at the solvent–substrate interface. We previously proved that under optimized dip-coating conditions (e.g., low





**Figure 4.** Schematic illustration of the molecular organization in the thin film deposited at 10  $\mu\text{m/s}$  in an OFET.

microstructure evolution in the bulk film during solution processing and on the charge carrier transport in the transistor. For a sufficient charge carrier transport, a critical multilayer network is necessary to create the required percolation pathways. Our approach opens a new pathway for the bottom-up fabrication of conjugated polymer ultrathin films and provides new insights into the fundamental understanding of solution-processable OFETs based on polymer thin films. Currently, we are clarifying the question of the degree to which these findings can be generalized to other conjugated polymers. Since the self-assembly of polymers strongly depends on the design of the backbone and the attached substituents, differences in the development of the film structure could be expected for other systems. The same question arises concerning the solution deposition technique with different processing parameters. Our preliminary AFM images (Figure S6) for spin-coated films indicate that a mechanism identical to that for dip-coating takes place during the microstructure evolution. Long, thick fibers were formed on top of a layer of PBTTT. Unfortunately, spin-coating does not allow defined control of the process conditions as in the case of dip-coating. Our future work will focus on the verification of these conclusions for other conjugated, solution-processed systems and different processing techniques.

## ■ ASSOCIATED CONTENT

### Supporting Information

Experimental procedures, dip-coating process, AFM image for the annealed film, AFM height profiles, and GIWAXS patterns. This material is available free of charge via the Internet at <http://pubs.acs.org>.

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### Notes

The authors declare no competing financial interest.

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