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## High-k Tm<sub>2</sub>O<sub>3</sub> Sensing Membrane-Based Electrolyte-Insulator-Semiconductor for pH Detection

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For high-sensitive pH sensing, an electrolyte—insulator—semiconductor (EIS) device with a  $Tm_2O_3$  sensing membrane fabricated by reactive sputtering and the succeeding postdeposition annealing treatment was proposed. In this study, the influence of thermal annealing (700, 800, and 900 °C) on the structural characteristics of the  $Tm_2O_3$  sensing membrane was investigated by X-ray diffraction, atomic force microscopy, and X-ray photoelectron spectroscopy. The observed structural properties were then correlated with the resulting pH-sensing performance. Results revealed that the EIS device with a  $Tm_2O_3$  sensing film annealed at 800 °C exhibited a higher sensitivity of 58.02 mV/pH (within the pH range of pH 2–12), a lower hysteresis voltage of 2 mV (measurement cycle, pH =  $7\rightarrow4\rightarrow7\rightarrow10\rightarrow7$ ), and a lower drift rate of 1.04 mV/h (at the condition of pH 7) compared with those at other annealing temperatures. These results are attributed to the formation of a well-crystallized  $Tm_2O_3$  structure, a thinner low-k interfacial layer at the oxide/Si interface, and the higher surface roughness at such thermal annealing conditions. As a whole, this study provided some fundamental data regarding the application of  $Tm_2O_3$  sensing membrane-based EIS devices for pH detection.

#### Introduction

Ion-sensitive field-effect transistor (ISFET) devices have been studied extensively for the chemical sensing of ion concentrations in solution (e.g., H<sup>+</sup>, Cl<sup>-</sup>, Na<sup>+</sup>, K<sup>+</sup>, NH<sub>4</sub><sup>+</sup>, Ca<sup>2+</sup>, etc.) because of their distinctive advantages of compact size, low cost, rapid response, ease of fabrication, and, particularly, the high compatibility for integration with complementary metal oxide semiconductors (CMOSs).1 Metal oxide semiconductor field-effect transistor (MOSFET) devices consist mainly of the metal, the oxide, the source/drain, and the semiconductor substrate; the difference between an ISFET and an MOSFET device is that no metal gate electrode is employed in the former. The original SiO<sub>2</sub> material used as a pH-sensitive membrane for ISFETs was introduced by Bergveld<sup>2</sup> in 1970. Si<sub>3</sub>N<sub>4</sub> film is the most popular material for a sensing membrane because of its easy process and high compatibility for CMOS integration.<sup>3</sup> The disadvantage of Si<sub>3</sub>N<sub>4</sub> film is the degradation of pH-sensing characteristics due to the surface oxidation. High-k dielectric materials, including Al<sub>2</sub>O<sub>3</sub>, Ta<sub>2</sub>O<sub>5</sub>, TiO<sub>2</sub>, HfO<sub>2</sub>, ZrO<sub>2</sub>, and Gd<sub>2</sub>O<sub>3</sub>,<sup>4-9</sup> have been proposed as a sensing membrane for pH-ISFET application because of their higher pH response. To improve the quality of the interface between the high-k material and the silicon substrate, growing a thin SiO<sub>2</sub> film on the Si substrate imparts the pH-sensing membrane with a lower density of the interfacial state, smaller stress, and good adhesion.<sup>10</sup>

Recently, rare-earth oxide thin films have been investigated for semiconductor applications due to high dielectric constants, high resistivities, and large energy band gap characteristics. <sup>11,12</sup> A thulium oxide (Tm<sub>2</sub>O<sub>3</sub>) film can be considered as a sensing

film in the pH-ISFET devices because of its dielectric constant of ~13, energy band gap of >5 eV, and predicted chemical and thermal stability with Si. 13,14 Furthermore, Paivasaari et al. 15 showed the electrical characteristics and physical properties of Tm<sub>2</sub>O<sub>3</sub> thin film grown onto a silicon(100) substrate using atomic layer deposition. In this article, we describe the physical and sensing characteristics of thin Tm<sub>2</sub>O<sub>3</sub> sensing films deposited on Si substrates by means of reactive sputtering. We have used X-ray diffraction (XRD) to monitor the growth directions and crystallinity of the films and X-ray photoelectron spectroscopy (XPS) to determine the chemical structure of the Tm<sub>2</sub>O<sub>3</sub> films after annealing at various temperatures. The surface roughness of Tm<sub>2</sub>O<sub>3</sub> sensing films after different annealing temperatures was characterized with atomic force microscopy (AFM). Furthermore, we discuss the effects of annealing on the sensing characteristics of Tm<sub>2</sub>O<sub>3</sub> electrolyte-insulator-semiconductor (EIS) devices.

#### **Experimental Section**

The EIS devices incorporating  $Tm_2O_3$  sensing membranes were fabricated 4 inch p-type Si(100) wafers. Before the deposition of  $Tm_2O_3$ , the wafers were cleaned with a standard RCA and then they were treated with 1% HF to remove the native oxide. An ~25 nm  $Tm_2O_3$  film was deposited on a Si substrate by reactive sputtering from a thulium target with a base pressure of about  $10^{-6}$  Torr. Samples were annealed at different temperatures by rapid thermal annealing (RTA) in  $O_2$  ambient for 30 s. The backside contact (a 400 nm thick Al film) of the Si wafer was deposited using a thermal coater. The sensing membrane size was defined through photolithographic processing under a photosensitive epoxy (SU8-2005, Micro-Chem) that behaved as an antiacid polymer. EIS devices were then fabricated on the Cu lines of a printed circuit board using

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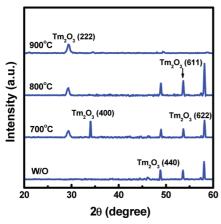


Figure 1. XRD of Tm<sub>2</sub>O<sub>3</sub> dielectric films annealed at different temperatures.

a Ag gel to form conductive lines. A handmade epoxy package was used to encapsulate the EIS structure and the Cu line.

The film structure and composition of  $Tm_2O_3$  dielectric films with postdeposition annealing (PDA) at various temperatures were analyzed by XRD and XPS. The surface morphologies of the  $Tm_2O_3$  sensing dielectrics were analyzed by AFM. The pH sensitivities of the  $Tm_2O_3$  sensing membranes were determined by measuring capacitance—voltage (C-V) curves of the EIS devices. The C-V curves were measured using buffer solutions of various values of pH (Merck Inc.), a Ag/AgCl reference electrode, and a Hewlett-Packard 4284A LCR meter operated at an ac signal frequency of 100 Hz to maintain the electrochemical system at equilibrium. All setups were performed in a dark box to avoid interference from light and noise.

#### **Results and Discussion**

To study the crystalline structures and orientations of Tm<sub>2</sub>O<sub>3</sub> films annealed at different temperatures, XRD measurements were conducted (Figure 1). The XRD patterns indicate that the films comprise the cubic phase of the Tm<sub>2</sub>O<sub>3</sub> film. The

as-deposited (W/O) film exhibits three (400), (611), and (622) peaks. The temperature-induced evolution of the crystalline structure of the Tm<sub>2</sub>O<sub>3</sub> film can be followed by XRD analysis. The sample annealed at 700 °C exhibited three small (222), (440), and (611) peaks, and two large (400) and (622) peaks, whereas the sample prepared at 800 °C had a strong (622) peak and three weak (222), (440), and (611) peaks. This suggests a preferential orientation of the crystallites with the (622) planes of cubic Tm<sub>2</sub>O<sub>3</sub> parallel to the substrate for the 800 °C annealed case. The intensities of these peaks, except for the (400) peak, increase upon the RTA temperature. This is due to crystallization and/or grain growth during high-temperature annealing. The film annealed at 900 °C showed only a preference for the (222) crystallographic orientation, as shown in Figure 1. This result suggests the formation of a low-k interfacial layer at the Tm<sub>2</sub>O<sub>3</sub>/ Si interface due to the diffusion of thulium and oxygen from the film to the Si substrate.

The surface roughness of the  $Tm_2O_3$  sensing films after RTA at different temperatures is shown in Figure 2. The as-deposited  $Tm_2O_3$  film exhibited a smaller surface roughness than the annealed film. Moreover, the surface roughness of the  $Tm_2O_3$  films clearly increased upon increasing the RTA temperature but suddenly decreased at 900 °C. The availability of defect sites due to higher density can enhance self-diffusion of thulium and oxygen, causing larger grain-boundary velocity during annealing and, therefore, large grains. It is worth mentioning that the sample annealed 900 °C shows a lower surface roughness compared with 800 °C. This is attributed to the fact that the thulium atoms removed from the oxide film mostly migrated to the oxide/Si interface during high-temperature annealing, thus resulting in a small roughness surface.

We used XPS to analyze the structural and compositional changes in the  $Tm_2O_3$  films as a function of the annealing temperature. Figure 3a shows the  $Tm\ 4d_{5/2}$  XPS spectra of the  $Tm_2O_3$  films annealed at different RTA temperatures. The  $Tm\ 4d_{5/2}$  peak of the  $Tm_2O_3$  reference is located at 175.6 eV. <sup>16</sup> The as-deposited film shifted the peak position to a higher binding energy by approximately 0.6 eV relative to the  $Tm_2O_3$  reference

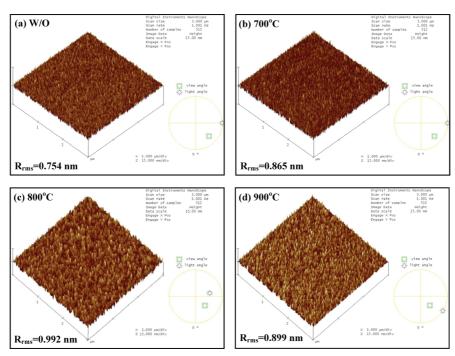


Figure 2. AFM surface image of Tm<sub>2</sub>O<sub>3</sub> sensing films for (a) as-deposited and annealed at (b) 700 °C, (c) 800 °C, and (d) 900 °C.

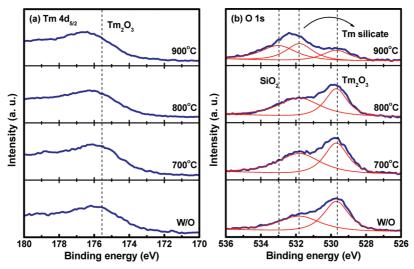


Figure 3. (a) Tm 4d and (b) O 1s XPS spectra of Tm<sub>2</sub>O<sub>3</sub> films annealed at different temperatures.

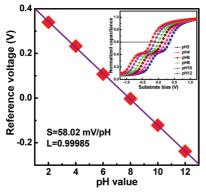


Figure 4. Reference voltage of Tm<sub>2</sub>O<sub>3</sub> EIS device prepared through RTA at 800 °C plotted as a function of pH at room temperature. Inset: C-curves displaying the response of the Tm<sub>2</sub>O<sub>3</sub> sensing films annealed at 800 °C when submerged into solutions at values of pH ranging from 2 to 12.

TABLE 1: Surface Roughness of AFM and pH Response Parameters for All Samples, Including Sensitivity, Hysteresis Voltage, and Drift Rate

PDA temperature (°C)	surface roughness (nm)	sensitivity (mV/pH)	hysteresis voltage (mV)	drift rate (mV/h)
W/O	0.754	48.63	29	17.4
700	0.865	53.40	5	2.80
800	0.992	58.02	2	1.04
900	0.899	55.90	10	3.60

position, suggesting Tm silicate (TmO<sub>x</sub>Si<sub>y</sub>) formation at the interface during film deposition. At annealing temperatures up to 800 °C, it was found that the Tm 4d peak position was almost no change in binding energy and that there was a sudden increase in the shift after annealing at 900 °C. This is due to the presence of a thicker low-k interfacial layer. Figure 3b displays the O 1s energy level region in the XPS spectra of the samples after RTA at different temperatures. A multipeak Lorentzian-Gaussian deconvolution procedure was used to extract the exact line position and intensities. When peak fitting was necessary to locate peak position or integrate area, Lorentzian-Gaussian functions were generated by minimizing the misfit error. The O 1s signal comprised three peaks at 529.7, 531.8, and 533 eV, which we assign to Tm<sub>2</sub>O<sub>3</sub>, <sup>16</sup> Tm silicate, and SiO<sub>2</sub>, <sup>17</sup> respectively. The O 1s peak intensity corresponding to Tm<sub>2</sub>O<sub>3</sub> is rather constant up to 800 °C but suddenly decreases

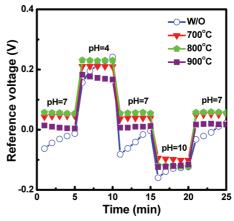


Figure 5. Hysteresis voltage of Tm<sub>2</sub>O<sub>3</sub> EIS devices annealed at different temperatures during the pH =  $7 \rightarrow 4 \rightarrow 7 \rightarrow 10 \rightarrow 7$ .

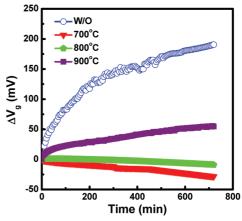


Figure 6. Drift characteristics of Tm<sub>2</sub>O<sub>3</sub> EIS capacitors annealed at different temperatures as a function of time in the pH = 7 solution.

at 900 °C, while the O 1s peak intensity corresponding to SiO<sub>2</sub> suddenly increases at 900 °C. For annealing performed at 900 °C, the Si, O, and Tm atoms that diffused from the Tm<sub>2</sub>O<sub>3</sub> film reacted with the Si substrate to form silica and Tm silicate, as suggested by the sudden increase in the O 1s peak intensity corresponding to SiO2 and/or Tm silicate and the decrease in the O 1s peak intensity corresponding to Tm<sub>2</sub>O<sub>3</sub>. The SiO<sub>2</sub> layer grew monotonically at the film/Si interface owing to the oxygen diffusion toward the interface from the crystal defect and nonstoichiometric structure with the high-temperature RTA.<sup>18</sup>

A group of C-V curves as a function of various pH solutions were measured to estimate the pH sensitivity of the Tm<sub>2</sub>O<sub>3</sub> sensing membrane. Changing the pH of the solution led to a shift in the flat-band voltage of the C-V curves. As a result of the shift, a group of C-V curves can be obtained as a function of pH value. This behavior can be explained by using the sitebinding model<sup>19</sup> to describe the ionic absorption processes at the electrolyte/oxide interface. The reference voltages are obtained from the C-V curves for achieving the normalized capacitance of 0.6. The results indicate the shift of reference voltage for Tm<sub>2</sub>O<sub>3</sub> sensing films during a cycle of experiments from pH 2 to pH 12, as shown in Figure 4. These C-V curves were shifted as a result of the surface potential modification by the formation of a hydrogen ion dipole on the sensing film. It is found that Tm<sub>2</sub>O<sub>3</sub> EIS devices annealed at 800 °C exhibited a high pH sensitivity of 58.02 mV/pH and good linearity. The inset of Figure 4 illustrates one group of C-V curves of the EIS device incorporating a high-k Tm<sub>2</sub>O<sub>3</sub> sensing film after PDA at 800 °C in different pH buffer solutions. It is found that a Tm<sub>2</sub>O<sub>3</sub> EIS device annealed at 800 °C exhibited a better pH sensitivity than those of the devices performed at the other RTA temperatures, as shown in Table 1. This result could be attributed to the higher surface roughness of the Tm<sub>2</sub>O<sub>3</sub> sensing membrane annealed at 800 °C, as observed in the previous AFM analysis. With the higher surface roughness, the  $N_s$  will increase accordingly, which, therefore, contributes to higher detection sensitivity, as previously reported in the site-binding model.<sup>7</sup>

Hysteresis can be due to the defects of a dielectric film, causing the formation of porous structures. The interior sites of these porous defects could react with the ions existing in the tested solution and thus cause a hysteresis response. Another possible cause is the interaction of ions in the solution with the responding sites along the boundaries of grains on an insulator film.<sup>20</sup> We subjected the EIS capacitors using a Tm<sub>2</sub>O<sub>3</sub> sensing film to the pH loop of  $7\rightarrow4\rightarrow7\rightarrow10\rightarrow7$  over a period of 1500 s. A smaller hysteresis voltage of the Tm<sub>2</sub>O<sub>3</sub> sensing films after RTA at 800 °C is 2 mV in the pH loops of  $7\rightarrow4\rightarrow7\rightarrow10\rightarrow7$ , as shown in Figure 5. This may be due to the well-crystallized Tm<sub>2</sub>O<sub>3</sub> structure.

The drift rate of pH-ISFET is described using a hopping and/ or trap-limited transport mechanism, also known as dispersive transport, 21,22 to evaluate the hydration rate of the insulator. The phenomena of dispersive transport can be found in a broad class of disordered materials. In an amorphous material, for example, dispersive transport may result from hopping motion through localized states (hopping transport), trap-limited transport in the presence of traps possessing an exponential energy distribution (multiple-trap transport), or a combination of the aforementioned transport mechanisms (trap-controlled hopping transport).<sup>22</sup> Dispersive transport causes a decay in the density of sites/traps occupied by the species going through transport.<sup>23</sup> The hypothesis that hydration of the gate dielectric is controlled by a dispersive transport mechanism is sustained by the presence of buried surface sites and/or the presence of traps (specifically electrically active silicon dangling bonds). The variations in the chemical composition of the insulator surface give rise to the change in number of surface sites with time, thus causing a monotonic temporal increase in the threshold voltage. Figure 6 displays the drift rate of a high-k Tm<sub>2</sub>O<sub>3</sub> sensing membrane after RTA at temperatures as a function of time, measured in the pH 7 solutions for 12 h. The EIS capacitor annealed 800 °C showed the best long-term stability (1.04 mV/h); in contrast, the EIS device without PDA treatment featured a serious drift of 17.4 mV/h. The improvement in the drift characteristics was accomplished on the formation of a silicate layer at the oxide/ Si interface and well-crystallized Tm<sub>2</sub>O<sub>3</sub> structure. However, a higher drift rate was considered to be caused by the presence of a high density of crystal defects.

#### Conclusion

In this article, we studied the structural and sensing characteristics of Tm<sub>2</sub>O<sub>3</sub> sensing films grown on the Si(100) substrate by means of reactive sputtering. The presence of a Tm<sub>2</sub>O<sub>3</sub> structure in the EIS device was confirmed by means of XRD, AFM, and XPS analysis. We find that the EIS device incorporating a high-k Tm<sub>2</sub>O<sub>3</sub> film with subsequent RTA at 800 °C exhibited a high sensitivity of 58.02 mV/pH in the solutions from pH 2 to pH 12, a low hysteresis voltage of 2 mV in the pH =  $7\rightarrow4\rightarrow7\rightarrow10\rightarrow7$ , and a small drift rate of 1.04 mV/h in the pH 7 buffer solution. This can be explained by the presence of a well-crystallized Tm<sub>2</sub>O<sub>3</sub> structure, a large surface roughness, and a thin Tm silicate layer. EIS devices incorporating a high-k Tm<sub>2</sub>O<sub>3</sub> sensing membrane show promise for use in pH-ISFET applications.

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