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Characteristics of field-effect transistors using the one-dimensional extended hydrocarbon [7]phenacene

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Field-effect transistors (FETs) with solid gate dielectrics are fabricated with thin films of the one-dimensional (1D) extended hydrocarbon [7]phenacene, which contains seven benzene rings. p-channel FET characteristics are observed for these FETs, with a mobility of 0.75 cm² V⁻¹ s⁻¹ at 100 Torr of O_2 . The O_2 gas-sensing effect is examined for the [7]phenacene FET and for the 1D hydrocarbon picene FET. These FETs' trap density and contact resistance are investigated with the multiple shallow trap and release model and the transfer line method. Unlike picene FETs, [7]phenacene FETs have few charge traps and are therefore air-stable. © 2011 American Institute of Physics. [doi:10.1063/1.3540648]

Field-effect transistors (FETs) based on the one-dimensional (1D) extended hydrocarbon picene show attractive p-channel FET characteristics. ^{1–5} The field-effect mobility μ is as high as 3 cm² V⁻¹ s⁻¹. The absolute switch-on drain current $|I_D^{\rm on}|$ and field-effect mobility μ of a picene thin film FET are drastically increased by O_2 exposure; $|I_D^{\rm on}|$ represents the drain current at a gate voltage V_G above the threshold voltage $V_{\rm TH}$. This phenomenon is explained by a decrease in the number of in-band traps under O_2 . Furthermore, it was found that the hysteresis in transfer curves was increased by the presence of H_2O . We attempted to develop a practical O_2 gas sensor based on the above experimental results. ^{4,5}

In 1D extended hydrocarbons, [n]acene consists of a linear fusion of benzene rings, while [n]phenacene consists of a zigzag fusion of benzene rings, and [11]phenacene is currently the phenacene with the largest number of benzene rings that has been synthesized.⁶ The band gap of an [n]phenacene is substantially wider than that of an [n]acene such as pentacene, and the highest occupied molecular orbital (HOMO) of an [n]phenacene is deeper. This guarantees low reactivity and high stability for an [n]phenacene molecule, in contrast to an [n]acene such as pentacene; a pentacene FET shows degradation in air. Therefore, [n]phenacenes may be important test materials for future stable and high-performance organic FETs. In this letter, we report the characteristics of a [7]phenacene FET with solid gate dielectrics; the molecular structure of [7] phenacene is shown in Fig. 1(a). The O₂ sensing effects are investigated at various O₂ concentrations, and the channel traps and the contact resistance of [7] phenacene are clarified based on the multiple shallow trap and release model (MTR) and the transmission line method (TLM), respectively.

[7]phenacene was synthesized as reported previously, and a purified sample (99.9% purity) was obtained by sub-

liming the crude sample at 573 K and 4×10^{-5} Torr. An SiO₂/Si wafer was used as the substrate for an FET device, with SiO₂ as gate dielectric. SiO₂ layers were 400 or 40 nm thick. One SiO₂ surface (400 nm) was coated with hexamethyldisilazane, while the other SiO₂ surface (40 nm) was coated with 1.8wt% of polystyrene to suppress leakage of gate current. Ta₂O₅ was also used as a gate dielectric and the Ta₂O₅ films were formed on the Si substrate by magnetron RF sputtering. The Ta₂O₅ was 200 nm thick, and 1.8 wt% of polystyrene was coated on the Ta₂O₅ surface to suppress gate current leakage. [7]phenacene thin film FETs were fabricated with the structure shown in Fig. 1(a). The [7]phenacene thin films were 30 nm thick, and the Au source/drain electrodes 50 nm. Thin films of [7]phenacene and the Au electrodes were formed by thermal evaporation at 10^{-7} Torr. The C_0

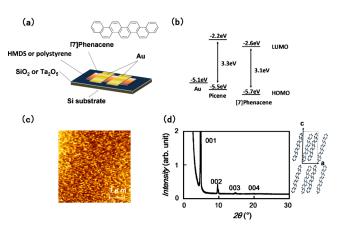


FIG. 1. (Color online) (a) Molecular structure of [7]phenacene and the structure of the [7]phenacene FET, and (b) energy diagram of [7]phenacene drawn by using the ionization potential determined by photoelectron yield measurement and band gap determined by optical absorption, which were measured in this study. In (b), the energy diagram of picene is shown for reference. (c) Atomic force microscope image and (d) x-ray diffraction pattern of [7]phenacene thin films. In (d), schematic representation of molecular stacking of [7]phenacene on SiO₂ surface is shown.

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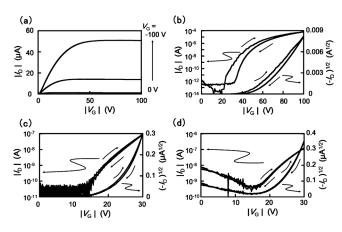


FIG. 2. (a) Output and (b) transfer curves of [7]phenacene thin film FET with 400 nm thick SiO_2 gate dielectric. Transfer curves of [7]phenacene thin film FETs with (c) 40 nm thick SiO_2 and (d) 200 nm thick Ta_2O_5 gate dielectrics. All measurements were performed in an Ar-filled glove box.

values of the 400 and 40 nm SiO_2 gate dielectric were 8.1 and 16 nF cm⁻², respectively. The C_0 of the Ta_2O_5 gate dielectric was 23 nF cm⁻². The L and W were 30 μ m and 2.0–3.5 mm, respectively.

The energy diagram of [7]phenacene is shown in Fig. 1(b). The band gap of [7]phenacene, 3.1 eV, is almost the same as that of picene, 3.3 eV. [7]phenacene and picene possess deep ionization potentials (or deep HOMOs) and shallow lowest unoccupied molecular orbitals (LUMOs). [7]phenacene shows a deeper HOMO ($\varepsilon_{\text{HOMO}}$ =-5.7 eV) than picene ($\varepsilon_{\text{HOMO}}$ =-5.5 eV), suggesting that a [7]phenacene FET with Au source/drain electrodes has a large injection barrier height even for holes. Nevertheless, the electronic structure of [7]phenacene is similar to that of picene and may produce an FET with characteristics similar to a picene FET.

An atomic force microscope image of [7]phenacene thin films shows dense packing of grains [Fig. 1(c)]. The root mean square thickness of the thin film was 4.8 nm. The x-ray diffraction pattern of the [7]phenacene thin films shows only (00l) reflections [Fig. 1(d)], indicating that the ab layer, with molecules stacked in herringbone fashion, is parallel to the SiO_2 surface in the same manner as in picene. The lattice constant, c, of the thin films was determined to be 18.1(2) Å. The van der Waals distance of the long axis of [7]phenacene indicates that the molecule is inclined by 20° from the normal direction on the SiO_2 surface. The molecular stacking is shown schematically in Fig. 1(d). The topological features, i.e., that the grains are densely packed in [7]phenacene and the ab plane is parallel to SiO_2 surface, are also effective for channel conduction in the FET, as in picene FET. 1.4°

The output and transfer curves of the [7]phenacene thin film FET with a 400 nm thick SiO_2 gate dielectric are shown in Figs. 2(a) and 2(b), which were measured in an Ar-filled glove box. The p-channel FET characteristics are clearly observed and the maximum μ was 0.75 cm² V⁻¹ s⁻¹ at 100 Torr of O_2 . The μ decreases gradually when the FET is stored in an Ar-filled glove box for a few days. This suggests an O_2 gas-sensing ability for [7]phenacene FET, similarly to a picene FET. Before the storage of the [7]phenacene FET in an Ar-filled glove box, the channel region of [7]phenacene should contain significant amounts of O_2 because the FET was exposed to air in transferring the device from the

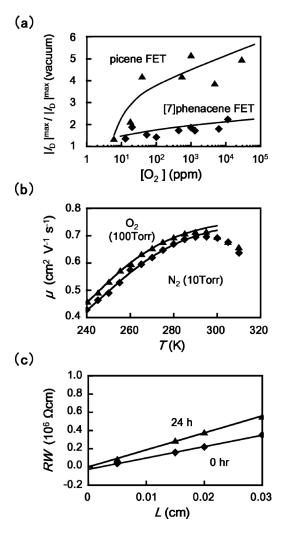


FIG. 3. (a) Ratio of $|I_{\rm D}|^{\rm max}$ with respect to the minimum $|I_{\rm D}|^{\rm max}$ measured under vacuum, $|I_{\rm D}|^{\rm max}/|I_{\rm D}|^{\rm max}$ (vacuum), for [7]phenacene and picene FETs as a function of ${\rm O}_2$ concentration. The eyes guides are drawn in (a). (b) μ -T plots of the [7]phenacene thin film FET with 400 nm thick SiO₂ gate dielectric at 100 Torr of O₂ and 10 Torr of N₂, and the curves fitted with Eq. (1) (see text). (c) RW-L plots of the [7]phenacene thin film FET with 400 nm thick SiO₂ gate dielectric in 0 h storage and 24 h storage (see text), and the fitted lines.

films and Au source/drain electrodes) to the glove box for FET characteristics measurements. The FET device operates at a value higher than the $|V_{\rm G}|$ of 60 V, again similarly to a picene FET. ^{1–3,5}

For low-voltage operation, we fabricated [7]phenacene FETs with 40 nm thick SiO_2 and 200 nm thick Ta_2O_5 gate dielectrics. The transfer curves of the [7]phenacene FET with SiO_2 and Ta_2O_5 gate dielectrics are shown in Figs. 2(c) and 2(d), respectively. These FETs show p-channel operation. The μ values of these FETs were as high as 10^{-3} cm² V⁻¹ s⁻¹ but the $|V_{TH}|$ values were as low as 22 V in an Ar-filled glove box. Thus, low-voltage operation could be achieved by use of very thin SiO_2 (40 nm) or Ta_2O_5 (200 nm) with a high dielectric constant.

The maximum absolute I_D , $|I_D|^{\max}$, for the [7]phenacene FET increases with its exposure time to O_2 , and the value decreases after dynamical pumping at 10^{-6} Torr. As seen from Fig. 3(a), the responsiveness of the [7]phenacene FET to O_2 is lower than that of the picene FET; the data for picene FET are taken from our previous report. The picene

a factor of 4 at 1000 ppm of O_2 , while in the [7]phenacene FET, the $|I_D|^{\rm max}$ increases by a factor of 1.5 at 1000 ppm. Thus, the [7]phenacene FET can be a sensor for O_2 gas, but with lower sensitivity than the picene FET, suggesting the possible utility of a stable organic FET under O_2 , or at least in dry air.

Here it is important to comment on the stability of the [7]phenacene FET to $\rm H_2O$. Previous investigations found the criterion for stability to $\rm H_2O$ to be $\epsilon_{\rm LUMO}$ =-3.98 eV. ^{9,10} Materials with $\epsilon_{\rm LUMO}$ >-3.98 eV are unstable in an $\rm H_2O$ -rich environment, indicating that [7]phenacene ($\epsilon_{\rm LUMO}$ =-2.6 eV) is not very stable. Nevertheless, the hysteresis emerging in transfer curves, which is closely related to $\rm H_2O$, is very small for [7]phenacene FETs with polystyrene-coated gate dielectrics, implying that the influence of $\rm H_2O$ can be ameliorated by using hydrophobic polymer-coated gate dielectrics to fabricate air-stable organic FETs. Thus a [7]phenacene FET can be a candidate for a stable transistor under ambient atmosphere even in a high-humidity environment.

To explore the reason why the sensitivity of [7]phenacene is lower than picene, the temperature dependence of μ in [7]phenacene FET was investigated. As seen in Fig. 3(b), both μ values at 10 Torr of N_2 and 100 Torr of O_2 increase with increasing temperature up to 290 K, and then decrease above 300 K. The μ values at 100 Torr O_2 are slightly higher than those at 10 Torr of N_2 in the temperature region below 290 K, indicating a weak O_2 sensing effect in the [7]phenacene FET. We fitted the $\mu-T$ plots by using the expression of the MTR model, $^{2,11-16}$

$$\mu(T) = \frac{\mu_0}{1 + \frac{N_t}{N_v} \exp\frac{\varepsilon_t - \varepsilon_v}{k_B T}},\tag{1}$$

where μ_0 , N_t , N_v , k_B , ε_t , and ε_v are intrinsic mobility, number of traps, total density of the valence band, Boltzmann constant, the mean trap energy and the edge energy level of the valence band, respectively. This expression was previously applied to the picene FET.² The values of μ_0 , N_t/N_v and ε_t $-\varepsilon_{\rm v}$ were determined to be 0.49 cm² V⁻¹ s⁻¹, 5.8×10⁻⁷, and 314 meV by least-squares fitting with Eq. (1) for the [7] phenacene FET at 10 Torr of N₂, while these values were $0.52~{\rm cm^2~V^{-1}~s^{-1}}$, 5.1×10^{-7} , and 310 meV at 100 Torr of O₂. Here it is important to note that these parameters are almost the same for N₂ and O₂. In the picene FET the ratio $N_{\rm t}/N_{\rm v}$ decreases by two orders of magnitude between vacuum and an O₂ atomosphere.² Thus, the traps in the [7] phenacene FET are not effectively reduced by the exposure of the [7]phenacene FET to O₂, in contrast to the picene FET.² This may be due to the extremely small number of traps $(N_t/N_v \sim 5 \times 10^{-7})$ observed in the [7]phenacene FET under N_2 , i.e., the [7] phenacene thin films are essentially trap-free; $N_t/N_v \sim 5 \times 10^{-5}$ under vacuum in the picene FET.² The difference in the number of traps in [7]phenacene and picene remains to be clarified, but it may be due to the lower reactivity expected for the [7]phenacene molecule owing to its deeper HOMO level.

The product of resistance and channel width, RW, is plotted as a function of L in Fig. 3(c). The RW-L plots were analyzed within the framework of TLM. The R was deter-

mined from output curves in the linear region ($|V_{\rm DS}| < |V_{\rm G}|$ $-V_{\rm TH}|$) at $V_{\rm G}$ =-100 V with Ohm's law. The RW measured just after introducing the [7]phenacene FET into an Ar-filled glove box (0 h storage) shows a linear relationship, and the slope is smaller than the RW measured 24 h after introducing it (24 h storage). Since the [7]phenacene FET device was once exposed to O_2 after forming the [7] phenacene thin films and Au electrodes in the vacuum chamber, the channel region may have significant O₂ after 0 h storage, as mentioned previously. The increase in the slope of the RW-L plot after 24 h storage implies that the sheet resistivity, $\rho_s(=RW/L)$, of [7] phenacene becomes large owing to the removal of O₂ caused by the 24 h storage in an Ar-filled glove box. The RW value at L=0 is the same in both RW-L plots (0 h storage and 24 h storage), as seen from Fig. 3(c). The value is nearly equal to zero. Therefore, the contact resistance is very small in the [7] phenacene FET, and is not affected by O_2 gas. Even in the picene FET the contact resistance is very small, and is not affected by O_2 (figure not shown). Thus, the O_2 sensing properties of [7]phenacene and picene FETs are strictly related to channel resistance.

In conclusion, the [7]phenacene FET showed p-channel FET characteristics with a μ of 0.75 cm² V⁻¹ s⁻¹, and low-voltage operation was achieved. The FET operation was not significantly affected by O₂, indicating its possible use in an air-stable organic FET device. We stress that these fundamental characteristics show that [7]phenacene may open an avenue for high-performance hydrocarbon FETs.

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