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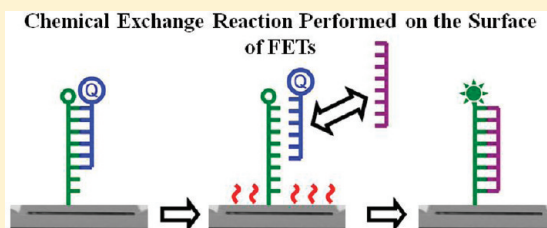
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S Supporting Information

ABSTRACT: We demonstrate the temperature mediated applications of a previously proposed novel localized dielectric heating method on the surface of dual purpose silicon field effect transistor (FET) sensor-heaters and perform modeling and characterization of the underlying mechanisms. The FETs are first shown to operate as electrical sensors via sensitivity to changes in pH in ionic fluids. The same devices are then demonstrated as highly localized heaters via investigation of experimental heating profiles and comparison to simulation results. These results offer further insight into the heating mechanism and help determine the spatial resolution of the technique. Two important biosensor platform applications spanning different temperature ranges are then demonstrated: a localized heat-mediated DNA exchange reaction and a method for dense selective functionalization of probe molecules via the heat catalyzed complete desorption and reattachment of chemical functionalization to the transistor surfaces. Our results show that the use of silicon transistors can be extended beyond electrical switching and field-effect sensing to performing localized temperature controlled chemical reactions on the transistor itself.



Silicon field-effect transistors have found many applications, including their prolific use as the fundamental building block of electronics,¹ their capability as chemical sensors,² and their more recent uses as ultrasensitive, label-free biological sensors with targets ranging from DNA or protein assays to early cancer diagnosis.^{3,4} These sensing elements are potentially envisioned as integrated components for lab-on-a-chip applications. Because nearly all biochemical reactions are highly temperature dependent, these lab-on-a-chip devices often require precise control of temperature profiles in fluid. A localized, precise heating element capable of the heating of fluids in the picoliter to microliter range would be important for a variety of applications including DNA amplification by polymerase chain reaction (PCR),⁵ patterning of distinct protein probes with high spatial resolution,⁶ localized cell lysis via heat,⁷ investigation of reaction kinetics, localized biochemical reactions, and temperature enhanced molecular synthesis.

Most currently reported heating techniques have focused on conduction based heating, using such devices as resistive heaters in close proximity to the heated area,^{8,9} Peltier elements,¹⁰ or Joule heating with electroosmotic flow.¹¹ Typically, these techniques require a large heated surface or element that is in close proximity to the areas to be heated. This results in large volumes of fluid being heated, which can limit the achievable spatial resolution of the applications. In addition, the fabrication processes for these elements are usually highly specialized, which is not amenable to integration with other lab-on-a-chip components. Microwave dielectric heating can be preferable to such methods because it is a noncontact method of heating using microwave

energy that is delivered directly to the intended fluidic environment with little interference from the substrate.¹² In this case, ions in the fluid itself are used to increase the temperature only where electric fields are present, allowing for much higher spatial resolution. Furthermore, because microwave heating does not rely on thermal diffusion, heat can be applied with much higher thermocycling rates and reduced reaction times.¹³ Much work has been reported using microwave heating for many applications,^{14–16} but these cases either use macroscale microwaves that will be difficult to scale down to on chip devices or demonstrate low spatial resolution even with complicated microfluidics or waveguiding for enhancement.

We recently reported a technique for the use of silicon based field effect biosensors as localized microwave heating elements capable of heating fluid with high spatial resolution within a few nanometers of the insulator surface of the devices.¹⁷ These dual use devices could potentially remove the need for separate components on chip by combining two critical needs of lab-on-a-chip devices, localized heaters and label free sensors, into a single element. In addition, the top down fabrication process employing standard CMOS techniques can be easily integrated with other essential components, such as control electronics. The broad applications for such a heating technique, which introduces a new dimension to the standard transistor sensing element, could

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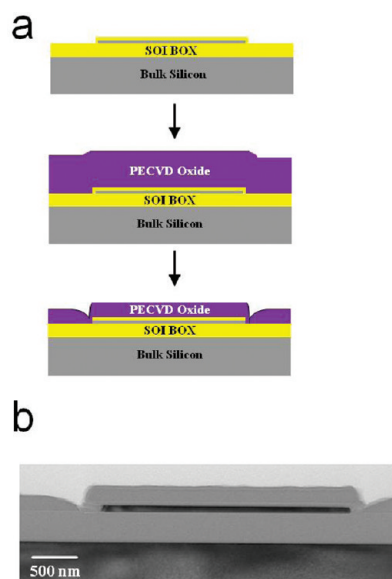


Figure 1. Illustration of the device structures: (a) key steps in the fabrication process, including the etchback step to expose the surface of the devices to the fluid and (b) SEM micrograph cross section of a typical nanoplate sensor-heater.

extend from ultralocalized biochemical reactions on chip to densely packed integrated sensors targeted at the simultaneous detection of many distinct analytes. In this paper, we first demonstrate that these previously presented devices can be used as pH sensors in fluid. We then further investigate the heating technique by using device cross sections and experimentally observed heating profiles to optimize numerical simulation results to quantitatively determine the achievable spatial resolution of the technique in both the lateral and vertical directions. Next, we present two such applications as proofs of concept. We performed a temperature-mediated exchange reaction performed on the silicon transistor surface with high spatial confinement. This application demonstrates the first steps toward elaborate biochemical reactions on chip, such as local polymerase chain reaction (PCR) systems on chip with integrated sensing.⁵ We also demonstrate a method for the self-aligned patterning of distinct probes on two sensing elements in close proximity to one another. The precise patterning of a selective functional layer with nanoscale spatial resolution in a self-aligned fashion is critical for the realization of sensitive and dense arrays of biosensors. Significant progress has been made toward this front but techniques reported to-date are not self-aligned,^{18–22} lack high spatial resolution,²³ are limited to specific chemistries on conductive surfaces only^{24,25} with unknown reliability,²⁶ or are limited in their operational environment.⁸ A versatile, self-aligned, and high-spatial resolution technique is needed.

MATERIALS AND METHODS

Device Fabrication. The fabrication flow and preparation of devices as well as techniques for heating and imaging were presented previously.¹⁷ Briefly, a top-down fabrication was used, starting with Silicon on Insulator (SOI) wafers. The top silicon was thinned down to approximately 300 Å via dry oxidation and buffered oxide etch, the active area was lithographically defined, and the top silicon was etched using deep reactive ion etch (DRIE). After source and drain doping, a gate oxide of around

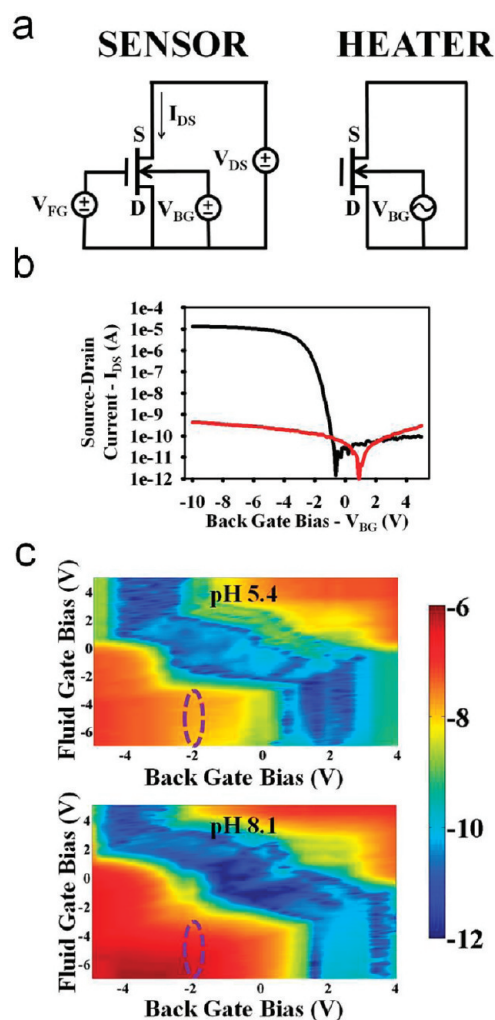


Figure 2. Electrical sensing results: (a) electrical configurations for the use of the devices as electrical label-free sensors (left) and as localized heaters (right); (b) transfer characteristics of the devices while operating as sensors. The source-drain current is shown as a function of applied back gate voltage (black). Also plotted in red is the gate leakage current. (c) Two dimensional plots showing the source-drain current as a color plot as a function of both the applied back gate and fluid gate biases. Normal transistor operation is seen using both of the gates. When the pH is changed from 5.4 to 8.1, substantial increases in currents are observed, especially in regions of optimal biasing, such as the one circled in purple.

300 Å was grown and metal contacts (200 Å titanium/800 Å platinum) to the FET devices were defined via liftoff. As part of this step, an on chip platinum fluid gate electrode was defined, which is important for fluidic measurements. A plasma enhanced chemical vapor deposited (PECVD) oxide was deposited over the entire surface as a passivation layer to reduce leakage currents during fluid measurements (Figure 1a). The passivation layer in the area directly over the sensor regions was then removed using a dilute BOE etch, resulting in the cross section shown in Figure 1b. Final devices were 300 Å thick, 2 μm in width, and 10 μm in length.

Electrical Measurements and pH Sensing. The electrical configuration for both electrical measurements (sensor) and temperature mediated applications (heater) are shown in Figure 2a. Electrical contact to the devices was made via probing with micromanipulators. Back gate contact was achieved using a conductive chuck in contact with the backside substrate of the

Table 1. Molecules Used in all Experiments

sequence description and name	sequence
DNA molecular beacon probe (probe 1)	5'-FITC-C6-CCAACGGTTGGTGTGTGGTTGG-C6-DABCYL-amine-3'
DNA target for molecular beacon probe (target 1)	5'-CCAACGGTTGGTGTGTG-C6-ROX-3'
DNA hairpin loop probe sequence a (sequence A)	5'-HEX-C6-CCAACGGACGTCGAATGGTTGG-C6-amine-3'
DNA hairpin loop probe sequence b (sequence B)	5'-FITC-C6-CCAACGGTTGGTGTGTGGTTGG-C6-amine-3'
DNA linear probe sequence c (sequence C)	5'-FITC-C6-CCAACGGTTGGTGTGTG-C6-amine-3'
DNA linear probe sequence d (sequence D)	5'-HEX-C6-CCAACGGACGTCGAATG-C6-amine 3'
PNA8-mer	N-FAM-OO-GCATCGTA-Lysine-C
LNA6-mer	5'-CGATGC-BHQ1-3'
LNA8-mer	5'-TACGATGC-3'

chips. Fluid gate contact was made using the on-chip platinum fluid gate electrode mentioned previously. All current measurements and applied biases were controlled by a semiconductor parameter analyzer (Kiethley 4200). Sodium phosphate buffers (10 mM) were used as pH solutions. The solution was confined to the area over the sensors using a custom-made well constructed of polydimethylsiloxane (PDMS). The color plots shown in Figure 2c were generated by measuring the source drain current (I_{DS}) while sweeping the back gate voltage (V_{BG}) and stepping the fluid gate voltage (V_{FG}).

Surface Functionalization. Unless otherwise noted, all chemicals were purchased from Sigma-Aldrich and were used without further purification. Peptide nucleic acid (PNA) was purchased from and purified via high-performance liquid chromatography (HPLC, >95%) by Applied Biosystems. PNA was characterized by mass spectroscopy; theoretical, (m/z) 2961.90; observed, (m/z) 2961.2. Locked nucleic acid (LNA) was purchased from and purified via HPLC by Sigma-Proligo. LNA was characterized by MALDI-MS; LNA6-BHQ theoretical, (m/z) 2622; observed, (m/z) 2542; LNA8 theoretical, (m/z) 2662; observed, (m/z) 2660. All DNA was purchased from and purified via HPLC by Sigma Genosys, a part of Sigma Aldrich. All fluorophore and amine modifications were performed by Sigma Genosys as well. A list of all molecules used in the experiments is shown in Table 1.

DNA Attachment. Chips were cleaned with a Piranha solution ($H_2O_2-H_2SO_4$, 3:1), rinsed with DI water, then dried using a stream of nitrogen immediately before the start of the chemistry. Subsequent steps were performed in a glovebox purged with high-purity nitrogen.

Samples were silanized in a 3% 3-aminopropyltrimethoxysilane (purchased from Sigma-Aldrich) in a methanol-DI (19:1) solution for 30 min at room temperature. Subsequently, the samples were rinsed with methanol and DI water and dried with nitrogen. Later, chips were cured using a hot plate at 110 °C for 5 min. Chips were placed into a *N,N*-dimethylformamide (DMF) solution containing 10% pyridine and 1 mM 1,4-phenylene diisothiocyanate (PDITC) for 2 h for surface activation. Chips were then rinsed with DMF and 1,2-dichloroethane and dried with nitrogen. The chips were then inserted in a solution of amine-modified probe DNA at a 1 μ M concentration in a 1.0 M tris-HCl [pH 7.0] with 1% vol/vol *N,N*-diisopropylethylamine and 20% vol/vol dimethylsulfoxide (DMSO) buffer and allowed to incubate overnight. Later, chips were rinsed with methanol and DI water and dried with nitrogen. In order to prevent nonspecific adsorption, the remaining unreacted PDITC were deactivated by immersing the chip in 50 mM 6-amino-1-hexanol and 150 mM *N,N*-diisopropylethylamine in DMF for a minimum

of 2 h. Chips were then rinsed with DMF, methanol, and DI water and dried with nitrogen.

Individual devices were heated in $10^{-2} \times$ TBE (Tris-borate EDTA) buffer solution (pH 7.5, ionic strength, 0.09 mM) by using the setup outlined in the electrical setup for the heating section. Typically an ac voltage of 10 MHz at 18 V_{rms} for 5 min was applied to the back gate for the removal of the molecules unless otherwise specified.

PNA Attachment and Exchange Reaction Procedures. Lysine modified FAM-PNA8 molecules were immobilized on the oxide surface using an epoxysilane cross-linker. Chips were activated with 1:1 $H_2O_2-H_2SO_4$ solution for 15 min, rinsed with copious amounts of DI water, dried under high-purity N_2 flow, and then transferred into a glovebox purged with nitrogen for protection from humidity. Next, the activated chips were immersed in a 2.5% solution of (3-glycidioxypropyl)trimethoxysilane in anhydrous toluene and were allowed to react at room temperature for 24 h. Then chips were rinsed with toluene and methanol to remove excess of silane not covalently bound to the surface and placed in an oven at 120 °C for 30 min. We have observed that this coating retains functionality and reactivity at least a week after the procedure. The 10 μ M lysine modified PNA molecules were suspended in a 150 mM sodium phosphate buffer (pH 8.5) and were attached on the surface by spotting 5 μ L of the solution on the chip and incubating in a high humidity chamber set at 42 °C for about 2 h. The chip was then immersed in a prehybridization buffer, consisting of bovine serum albumin (BSA) 1% w/w in $3 \times$ saline sodium citrate (SSC) buffer for about 1 h at room temperature to minimize the nonspecific binding of species in further steps. Chips were rinsed with DI water and dried under N_2 flow. PNA immobilization was confirmed by fluorescence imaging and subsequently chips were immersed in a solution of 10 μ M of LNA6-BHQ suspended in a $4 \times$ SSC solution. Chips were then allowed to incubate overnight at 55 °C. The chips were then rinsed with DI and dried with N_2 . After incubation, the attachment was confirmed by the lack of fluorescence due to the fluorescence quenching effect of BHQ. The chips containing the devices and the blank oxide chips were prepared using this procedure.

The localized exchange reaction was performed by applying 12 V_{rms} for heating with 10 μ M LNA8 in $10^{-4} \times$ TBE buffer for 20 min. The device was imaged before and after the application of heat.

RESULTS AND DISCUSSION

FET Devices as Sensors. The electrical configuration for both sensing and heating are shown in Figure 2a. For sensing, the back gate (V_{BG}) and fluid gate (V_{FG}) biases are swept to

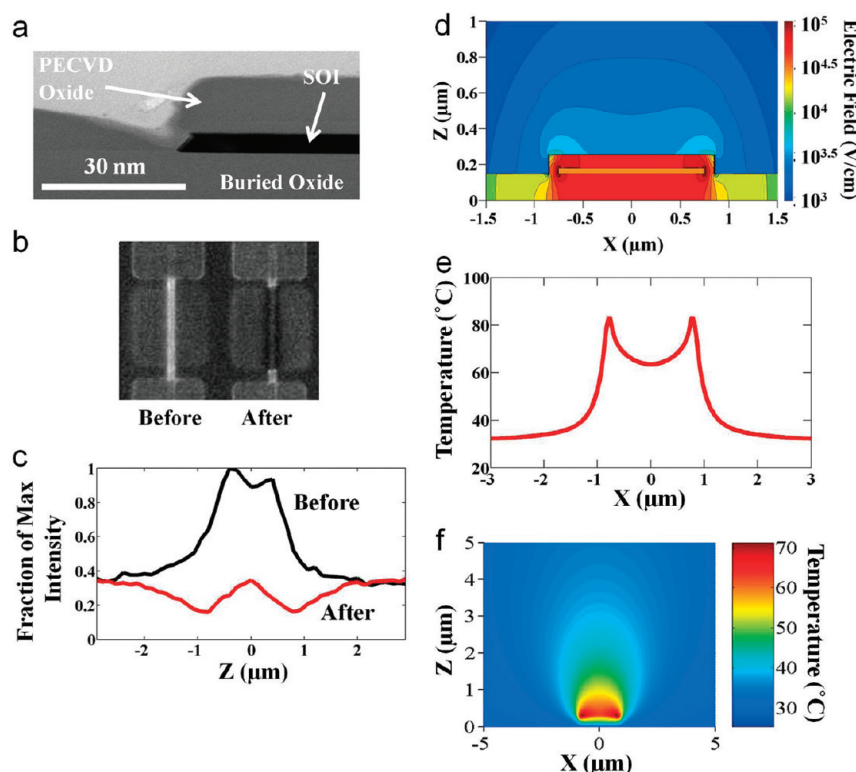


Figure 3. Investigation of the heating mechanism and determination of the spatial resolution of the technique. (a) Zoomed in cross-sectional transmission electron micrograph of the device types for experimental and simulation results after the final fabrication step. (b) Fluorescent images demonstrating the experimental heating desorption profile. A picture of the device to be heated prior to the application of voltage is shown on the left. To the right is a fluorescent picture acquired after the device was heated to an intermediary voltage to demonstrate the heating profile. (c) Quantification of the fluorescent images in part b demonstrating similar trends. (d) Simulated electric field strength around the device showing that higher fields are concentrated around the sidewalls of the device in fluid. (e) One-dimensional simulated temperature distribution depicting the localized nature of ac heating technique as a function of lateral position through the device cross section. (f) Two-dimensional simulated temperature distribution showing that the spread is less than $\sim 3 \mu\text{m}$ in both the horizontal and vertical directions.

modulate the carrier concentration in the channel while the source-drain current (I_{DS}) is measured with a constant source-drain voltage (V_{DS}). As changes in the charge occur at the surface of the devices (due to changes in pH or the binding of biomolecular analytes), the surface potential at the fluid/gate dielectric interface changes, modulating the carrier concentration and thus the source-drain current. For heating, the source and drain nodes of the device to be heated are shorted and an ac bias is applied between this node and the back gate with the fluid gate held floating.

We first demonstrated normal transistor operation of the devices in a dry state. The source-drain current was measured as the back gate voltage was swept (Figure 2b). The devices exhibited subthreshold slopes of around 300 mV/decade, with threshold voltages around -2 V . In addition, the leakage current consistently stayed below 1 nA, showing that the device was stable and functional. Next, 10 mM solutions of sodium phosphate buffers at two different pH values, 5.4 and 8.1, were placed over the devices, and the double gate characteristics were measured (Figure 2c). Both the fluid gate and back gate biases could be seen to control the channel concentration and thus the source-drain current. In addition, when the pH of the solution is changed from 5.4 to 8.1, increases in current were seen for nearly all biases, which is expected for p-type devices biased in accumulation. However, as has been explored previously,²⁷ certain biasing regions lend themselves for optimized sensitivity, such as the region circled in the figure, where current increases of approximately 2 orders of

magnitude could be observed. The devices were thus seen to be sensitive to changes in charge at the surface in fluid and with further optimization could be used as sensors of pH, DNA, or protein.

Determination of the Spatial Resolution of Heating. The heating technique, mechanism, and the measurement of the temperature at the surface was described in detail previously.¹⁷ Briefly, we introduced a technique to locally heat fluid within 2 nm of an oxide surface by using radio frequency Joule heating of mobile ions in the Debye layer close to the device surfaces. We also demonstrated a measurement technique to extract the temperature at the surface of the devices using the temperature modulated decay rate of the observed fluorescence while exposing the devices to high intensity light. Using this technique, we were able to correlate applied ac biases with the achievable temperature on the surface. Furthermore, we demonstrated several control experiments further validating that the observed fluorescence behavior was a result of changes in temperature, as opposed to other mechanisms that could modulate fluorescence.

To further establish the physical basis of the heating at the sensor surface and determine the spatial resolution of the heating technique, we measured the heating profile directly over the devices. Texas Red labeled DNA was attached to the surface of the sensors (see Materials and Methods). After the application of heat to the device, partial removal of the DNA oligomers was observed, resulting in a decrease of fluorescence in areas where the temperature was the highest (Figure 3b,c). Temperature-mediated

removal of self-assembled monolayers (SAMs) has been observed previously; earlier work has reported that SAMs on oxidized silicon substrates and glass slides are unstable over 80 °C.²⁸ This comparison is not intended as an approximation of the temperature on the surface but rather as an indication that these films will indeed become unstable at higher temperatures. This work used thiol modified DNA oligomers to attach to the SAMs as opposed to the amine modified DNA used in this work, but the attachment of the SAMs to the oxidized silicon substrates employs the same silane chemistry.

We then explored the heating of buffer solution due to the applied ac field through simulations. A stack similar to the TEM cross section in Figure 3a was used. In general, the electric field should be calculated by solving the Poisson equation exactly; i.e., $\nabla(\epsilon \nabla \Phi) = Q_s + \rho_{\text{ion}}$, where ϵ is the dielectric constant, Φ is the total potential, Q_s is the surface charge-density which consists of the ionized silanol (Si—OH) bonds and any target–receptor pair on the surface, and ρ_{ion} is the ion concentration of the buffer solution. Because of the presence of surface charge Q_s , the counterion density decays exponentially to its bulk value. The potential $\Phi = \Phi_s + \Phi_{\text{ac}}$ is the sum of the potential created by the surface charge and the potential due to ac voltage. Because of the relatively high frequency (10 MHz) used, negligible electrostatic screening occurs in the buffer solution. Therefore, Poisson's equation may be solved by solving two partial differential equations ($\nabla \cdot (\epsilon \nabla \Phi_{\text{ac}}) = 0$, and $\nabla(\epsilon \nabla \Phi_s) = Q_s + \rho_{\text{ion}}$) in two dimensions for the sensing region. As expected, the maximum electric field intensity ($E_{\text{ac}} = -\nabla \Phi_{\text{ac}}$) occurs near the edges of active silicon region due to the fringing effects (Figure 3d) and hence will result in higher temperature at the edges. The heat transport in the system was modeled using the thermal diffusion equation $\nabla(\kappa \nabla T) = \sigma(\rho)E^2/2 \approx \sigma(\rho_{\text{ion}})E^2/2$, where κ is the thermal conductivity, T the temperature, σ the electrical conductivity of the medium ($\sigma = \omega \epsilon''$), and E the local electric field obtained by numerical solution of Poisson's equation. Here we assume that dielectric heating of counter-ions at nanoscale regime also follows the classical $P = \sigma E^2/2$ dependence, where P is the power dissipated in an ac field. $E \approx E_{\text{ac}}$ because the magnitude of the ac field ($\sim 20\text{--}30\text{ V}_{\text{rms}}$) is significantly greater compared to the potential due to the surface species ($\sim 0.25\text{ V}$). Because of the presence of surface charge, the counterion density and hence the local conductivity (σ) near the sensor surface is much higher than in the bulk buffer. Because of the high fringing electric fields, maximum heat generation occurs close to the edges of the sensing region (Figure 3e,f). These results compare well to the experimental observations shown in Figure 3b,c. From these results, we can conclude that the spatial spread in the heat profile is less than 1 μm in the lateral direction (from both experimental observations and simulation results) and less than 3 μm in the vertical direction (from the simulation results). Thus, a spatial resolution in the heat using this technique is easily below a few micrometers in all directions.

We also used numerical simulations to indicate that dielectric heating of the counter-ions is indeed the dominant mechanism. On applying an ac bias, heat generation could occur due to a combination of the following mechanisms: (i) dielectric loss in SiO₂ (the BOX and the oxide which covers Silicon active area), (ii) dielectric loss in buffer solution, especially due to the counter-ions. The total amount of power (P) converted into thermal energy in SiO₂ is given by $P = 1/2 \sigma |E|^2$, where consider $\sigma = \epsilon'' \omega$. We use $\epsilon'' = 3.9$ for SiO₂ and σ for counter-ions is taken to be proportional to the counterion density, the multiplicative

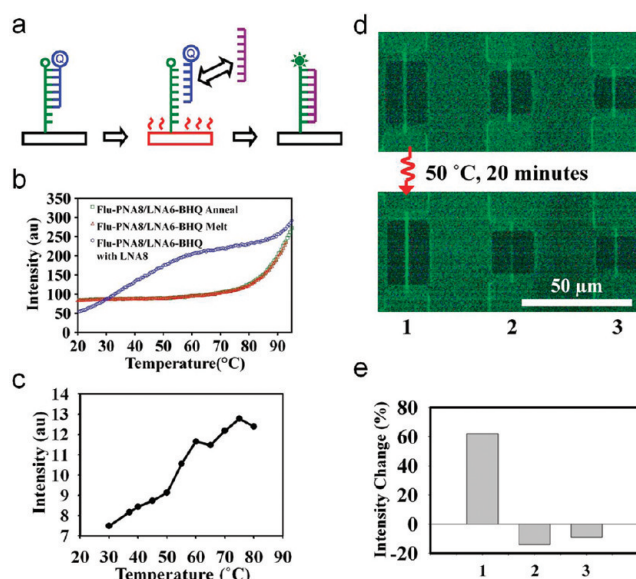


Figure 4. Heat mediated localized exchange reaction. (a) Schematic demonstrating the concept of the exchange reaction. (b) Fluorescence intensity as a function of temperature at different conditions. Melting of the PNA–LNA duplex happens at relatively high temperatures (red and green) when compared to competing LNA molecules (blue curve). The presence of the LNA thus allows for the recovery of the fluorescence at relatively low temperatures. (c) Exchange reaction studies on a blank silicon-dioxide chip by externally heating to known temperatures. (d) Fluorescence images showing the experimental results. The top image is taken before the experiment. Below is the image after exposing the chip to the higher affinity molecules while simultaneously heating device 1 (left most, shown with a curly red line, device 5 μm in length) for 20 min. (e) Average fluorescence intensity increase for each device before and after the experiment. The slight decrease in the fluorescence intensity for the nonheated devices is due to photobleaching.

factor is used as a fitting parameter to scale simulation results to the experimental range. SFigure 1a in the Supporting Information indicates that the contribution of dielectric loss in SiO₂ is negligible. Thus, our simulations and experimental results clearly indicate that counterion heating is the dominant mechanism.

As the device width is scaled down further, the temperature uniformity and the average temperature of the fluid on the device for a given voltage also increases (SFigure 1b in the Supporting Information) as the region influenced by the fringing fields forms a larger portion of the device cross section. Aggressive scaling in size reduces the volume over which heat generation can occur and thermal diffusion limits achievable temperatures for given voltages (for widths significantly less than 1 μm). However, a biological relevant temperature range of 30–70 °C should be possible for devices with submicrometer widths. Scaling down the devices to the nanoscale should only improve the spatial resolution and uniformity of the heating technique.

Heat Mediated Exchange Reaction. To demonstrate the utility of the heating technique for biochemical applications, we have carried out a temperature-mediated exchange reaction as a model reaction on the surface of transistors (Figure 4a). The scheme involves selectively exchanging a higher affinity molecule (8-mer LNA) with a lower affinity molecule (6-mer LNA) already bound to a surface capture probe (PNA) by enhancing the local reaction rate due to the local temperature. Table 1 shows the molecules used in our studies. We monitor the reaction by modifying the PNA probe with a fluorophore and modifying the 6-mer LNA with

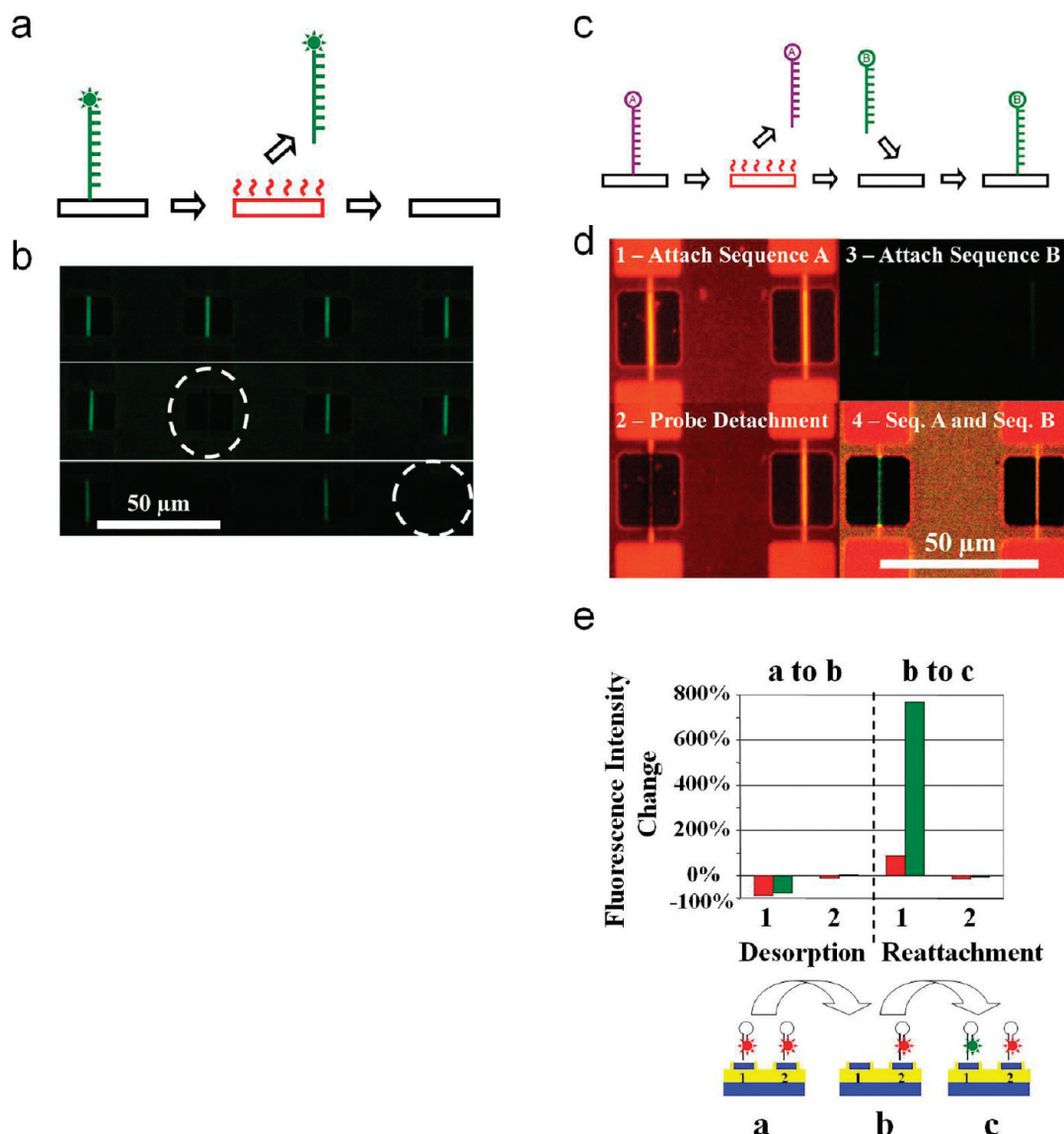


Figure 5. Localized decomposition of immobilized molecules, and subsequent refunctionalization of the surfaces. (a) Schematic summarizing the procedure for the heat mediated localized decomposition of the molecular layer. The device is represented with a black rectangle, and red indicates voltage applied to the device. After the immobilization of the appropriate functional layer on the surface, the desired surface is heated by the application of voltage for the decomposition of the molecule. (b) Fluorescent images showing the electrically addressable decomposition of the functional layer. The top image is taken before the experiment. Consecutive images (middle and bottom) are taken after heating the device encircled with dashed white lines. (c) Similar to part a but summarizing the procedure for the heat mediated localized functionalization of a device. A device can be refunctionalized with a desired probe after the desorption of the previous molecule. (d) Fluorescent images summarizing the selective functionalization procedure. Initially a fluorescently tagged (HEX sequence A) is immobilized on all the devices (1). The immobilized probe is desorbed by heating the left device (2). After this step, the chip undergoes the immobilization procedure for a distinct probe molecule containing fluorescently modified sequence B (3). The last image in the series was obtained using a triple filter spanning both wavelengths for the imaging of the fluorophores simultaneously and demonstrates the final patterning of two distinct probe molecules on adjacent devices separated by 50 μm (4). (e) Percent fluorescence intensity change using both the TRITC (red bars) and FITC (green bars) filters during each of the steps displayed in part d.

a black hole quencher (BHQ). The decrease in the fluorescence intensity indicates the presence of the 6-mer LNA–PNA duplex due to the quenching effect. The recovery of the fluorescence indicates the presence of the 8-mer LNA–PNA duplex. The model exchange reaction was studied in solution phase before being applied to the surfaces. Dynamics of the FAM–PNA8 (10 μM) and the LNA6–BHQ duplexes were studied by obtaining the melting–annealing curves from 20 to 94 $^{\circ}\text{C}$ (Figure 4b) by monitoring the relative fluorescence intensity of the FAM–PNA8 (CARY Eclipse fluorescence spectrophotometer). The melting

temperature was observed to be above 90 $^{\circ}\text{C}$. The duplex was then heated in the presence of the LNA8. The exchange reaction was indicated by an increase in fluorescence at a relatively lower temperature than observed in the melting curve. Next, the exchange reaction was studied on a blank surface using bare oxide chips and a warm stage to control the surface temperature in the presence of the 10 μM LNA8 in $10^{-4} \times$ TBE buffer. A fluorescence image was obtained at each temperature point, and the intensity was quantified as a function of the surface temperature (Figure 4c). Similar to the data obtained in bulk solution, an

increase in the overall intensity was observed with increasing temperature, indicating the execution of the exchange reaction on the surface. The fluorescence intensity did not recover to the intensity levels observed with the immobilized PNA before the hybridization of the LNA6–BHQ. We attribute the partial recovery of the fluorescence intensity to two factors: partial fluorescence quenching effect of the 8-mer LNA and increased photobleaching effects due to the high magnification objectives used.

The spatially localized reaction on the surface of a device was carried out by covalently attaching the lysine-modified PNA molecule (FAM–PNA8) and subsequently incubating with the 6-mer LNA (LNA6–BHQ) to form a duplex on the surface, resulting in decreased fluorescence compared to the PNA alone. With the application of 50 °C on the device (20 μm) for 20 min in the presence of the buffer solution containing 8mer LNA (LNA8), the heat-mediated exchange reaction took place on the devices resulting in a net increase in the fluorescence intensity on the device (Figure 4d,e) without significantly affecting adjacent devices that were not heated. These experiments show that the devices can indeed be used to carry out localized biochemical reactions via the precise application of heat. Any chemical reaction that is catalyzed by increased temperature could potentially be performed in a similar fashion, confined only around the device of interest. The observed difference between the bulk behavior shown in Figure 4b ($\sim 250\%$ increase) and that observed on the surface in Figure 4e ($\sim 60\%$) could be due to the difference between the molecules present in solution and the molecules immobilized on a surface. Because of possible steric hindrance and conformational issues for surface immobilized molecules, the efficacy of reactions on a surface is expected to be different when compared to molecules free in solution. In addition, the bleaching effects of molecules immobilized on a surface could also be different than for molecules in solution.

Desorption and Refunctionalization for Selective Functionalization. The heating technique can also be utilized for carrying out reactions at superheated temperatures by applying higher voltages. As one potential application of superheating, we demonstrate the regeneration of the surface of the sensor by decomposing the species attached on the surface via electrically addressable heat to the target device (Figure 5a,b). By attaching both the probe and the complementary target on the surface, we studied the removal of the molecules from the surface. Removal of the full duplex, rather than only the target molecule indicated the decomposition of the molecules versus a denaturing event (SFigure 2a,b in the Supporting Information). The heat-mediated desorption of the molecules can also be used to perform selective functionalization of surfaces, which is very important for the realization of dense arrays of nanobiosensors. Attachment of two distinct DNA probes was demonstrated on adjacent devices by decomposing the species attached on the surface via electrically addressable heat to the target device, followed by repeating the molecule attachment protocol with a distinct probe molecule (Figure 5c,d). Quantification of the change in fluorescence intensity showed a nearly complete removal of the first probe from initial functionalization to desorption and an increase in the fluorescence of the second probe from desorption to refunctionalization (Figure 5e). Negligible changes were observed for an adjacent nonheated device. The selectivity of the attachment to the heated device suggests that the fluorescence intensity decrease after the heating is due to the complete removal of previously immobilized molecules from the heated devices. Further, refunctionalization experiments with the omission of

the initial step of the functionalization process did not result with the immobilization of the new probe. Two sets of chips (sets A and B) were functionalized and desorbed, following an identical procedure to that outlined in the manuscript. The chips were first functionalized with a linear probe sequence D modified with a HEX fluorophore. Later, one device on the chip was heated to remove the previously immobilized probe, and the device was refunctionalized with linear probe sequence C modified with a FITC fluorophore. The refunctionalization of chips in sets A and B was carried out identically, with the exception of an omission of the 3% 3-aminopropyltrimethoxysilane in the silanization buffer for set B. The results are shown in SFigure 2c in the Supporting Information. The figure demonstrates the refunctionalization of set A with the FITC labeled probes, while set B registered no increase in the fluorescence level (some red fluorescence is observed in the heated device, and some green intensity is registered in the nonheated device as a result of bleed through of fluorescence through the filters). These experiments suggest that the surface is available for silanization after heating, since all the chemistry steps, including the silanization, are needed to refunctionalize the surface of the devices.

This technique can be used for fine patterning of different probe molecules with high spatial resolution. In final applications, a technique such as this would need to be integrated with a rougher patterning technique, such as ink jet spotting of distinct analytes, which has a spatial resolution on the order of 50 μm with state-of-the-art techniques. With further scale down of the devices, the spatial resolution can be improved down to the nanoscale as per our simulations, eventually allowing for the ultradense patterning of probe analytes for dense sensors target at many distinct analytes simultaneously.

CONCLUSIONS

Our results indicate a high potential for wide ranging applications and high amenability of these FET hybrid sensor-heaters for further scaling down both the dimension of and the separation between devices and increasing the spatial and temporal resolution of the technique. The current devices were shown to exhibit normal transistor behavior and to be sensitive to changes in pH. Furthermore, a heating spatial resolution of less than 3 μm was demonstrated in all directions, though this can be dramatically reduced as per our simulations. The devices were used to show proof of concept of the heating technique for two applications, a localized biochemical exchange reaction and the patterning of distinct probe molecules on the same chip. These field effect sensors with localized heating capability can be used to develop novel tools to be used in lab-on-a-chip systems such as highly integrated PCR systems, active sensor surfaces that can be regenerated, creating localized heat-mediated incisions on single cells, and dense sensor arrays for the multiplexed and label free detection of biomolecules.

ASSOCIATED CONTENT

S Supporting Information. Two figures referenced in this manuscript, including simulation profiles showing that dielectric relaxation of the ions is the primary mechanism for heating, simulation results showing the effect of scaling down device width, and control results from the heating and refunctionalization experiments. This material is available free of charge via the Internet at <http://pubs.acs.org>.

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