

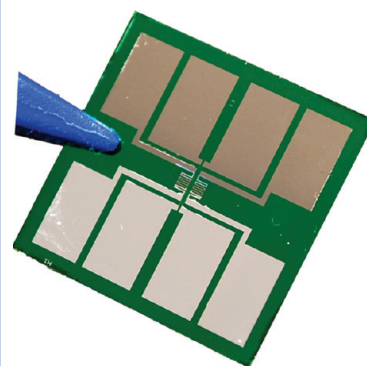
High-Throughput Measurement of the Seebeck Coefficient and the Electrical Conductivity of Lithographically Patterned Polycrystalline PbTe Nanowires

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ABSTRACT A high-throughput method for measuring the Seebeck coefficient, S , and the electrical conductivity, σ , of lithographically patterned nanowire arrays is described. Our method involves the microfabrication of two heaters and two Ag/Ni thermocouples, literally on top of an array of polycrystalline PbTe nanowires synthesized on a Si_3N_4 wafer using the lithographically patterned nanowire electrodeposition (LPNE) method. This strategy eliminates the transfer and manipulation of nanowires as a prerequisite for carrying out measurements on these wires of thermoelectric metrics. With these devices, we have measured the influence of the thermal annealing temperature on the thermoelectric properties of nine arrays of $60 \text{ nm} \times 200 \text{ nm} \times 200 \text{ }\mu\text{m}$ PbTe nanowires, and we find that at an optimum annealing temperature of 453 K, the S at 300 K is increased from $-41 \text{ }\mu\text{V/K}$ for unannealed wires to $-479 \text{ }\mu\text{V/K}$, 80% larger in magnitude than the S ($-260 \text{ }\mu\text{V/K}$) of bulk PbTe.

SECTION Nanoparticles and Nanostructures



The efficiency of a thermoelectric element is determined by its dimensionless figure-of-merit, ZT ¹

$$ZT = \frac{\sigma TS^2}{\kappa} \quad (1)$$

where σ is the electrical conductivity of the thermoelement, S is the Seebeck coefficient, κ is the thermal conductivity, and T is the mean absolute temperature. Lead telluride (PbTe), a semiconductor with a band gap of 0.31 eV (direct) at 300 K,^{2,3} is among the most efficient bulk thermoelectric materials with a ZT of 0.45 at 300 K and 0.85 at 700 K.⁴ Reducing the diameter of nanowires composed of thermoelectric materials such as PbTe to the nanometer scale is predicted to increase S ,^{5,6} and depress κ ,^{7–10} both of these effects leading, via eq 1, to an elevation in ZT .

Testing these predictions presents two experimental challenges. First, nanowires of candidate thermoelectric materials must be synthesized with control of wire diameter and length, crystallinity and crystal structure, and doping. Second, one or more of these nanowires must be incorporated into devices that enable the measurement of S , σ , and κ . A realization of the potential for nanowire-based thermoelectrics requires integrating nanowire synthesis and the measurement of S , σ , and κ in a feedback loop. This integration of nanowire synthesis with measurement of S , σ , and κ has already occurred in a few laboratories. Shi and co-workers have measured all three of these metrics for electrodeposited Bi_2Te_3 nanowires^{11,12} and for InSb nanowires prepared by vapor–liquid–solid (VLS)

growth.^{13–15} Lee and co-workers^{16,17} have measured κ for single-crystalline PbTe nanowires as a function of the wire diameter. Silicon nanowires have been prepared and their thermoelectric properties have been measured by two research groups.^{18,19} These two papers are the first to demonstrate an elevation of ZT for nanowires above the bulk value. The small number of references here testifies to the difficulty of incorporating nanowires prepared by hydrothermal synthesis,^{20–22} chemical vapor transfer,^{16,23} molecular beam epitaxy,^{24,25} or templated electrodeposition^{26–30} that, in all these cases, are tens of micrometers in total length into devices while achieving low-resistance ohmic electrical contacts to these nanowires.

We have recently developed a wafer-scale method for patterning polycrystalline PbTe nanowires onto dielectrics called lithographically patterned nanowire electrodeposition (LPNE).^{30,31} Here, we describe how LPNE can be used to “pre-position” an array of PbTe nanowires on a Si_3N_4 wafer prior to the microfabrication of a device that enables a measurement of S , σ , and parameters derived from them. This approach eliminates the requirement for manipulating free-standing nanowires while also facilitating the measurement of S for arrays of ~ 200 size-similar nanowires prepared, in place, in a single synthesis operation. The total time required to synthesize

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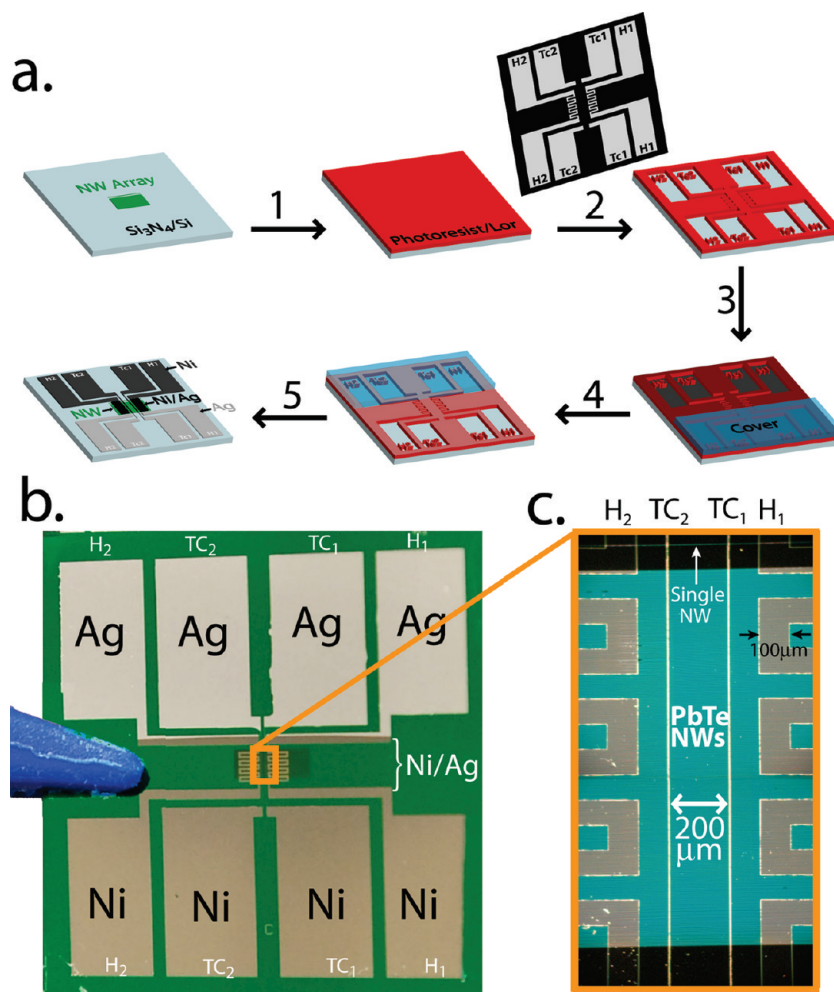


Figure 1. A device for high-throughput measurement of S and σ for nanowire arrays. (a) Five-step process flow for the fabrication, atop an array of PbTe nanowires (green), of two meander heaters and two Ag/Ni thermocouples. Step 1: A photoresist layer (Shipley 1808) is added. Step 2: Photoresist is photopatterned to enable the fabrication of heaters, thermocouples, and electrical contacts in steps 3 and 4. Step 3: Nickel is evaporated onto approximately one-half of the exposed pattern. Step 4: Silver is evaporated onto the other half. Step 5: The region at the center of the wafer, occupied by the heaters and the thermocouples, receives both nickel and silver evaporated layers in sequence. (b,c) Photograph (b) and micrograph (c) of the completed device. The blue region in (c) is an array of $60 \text{ nm} \times 200 \text{ nm}$ PbTe nanowires oriented horizontally. The two vertical lines near the center of image (c), spaced by $200 \mu\text{m}$, are the Ag/Ni thermocouples, which are $4 \mu\text{m}$ in width. Finally, a single “orphaned” nanowire that is separated laterally from the nanowire array by $\sim 100 \mu\text{m}$ is shown at the top.

PbTe nanowires arrays, construct these devices, and acquire temperature-dependent measurements of S and σ at 11 temperature points between 200 and 300 K was $\sim 36 \text{ h}$ for the first sample and 24 h for subsequent samples prepared in the same synthesis operation. The S measured in this way should provide an accurate representation of the mean S of nanowires prepared in a single LPNE operation, which, therefore, are narrowly dispersed in width, height, and chemical composition. Using this approach, we report here measurements of S and σ for nine $60 \text{ nm} \times 200 \text{ nm}$ PbTe nanowire arrays that have been subjected to thermal annealing treatments at three temperatures.

A microfabricated device for measuring S and σ was prepared starting with an array of ~ 200 PbTe nanowires synthesized using the LPNE method, as previously described.^{30,31} This five-step process (Figure 1a) permitted two meander heaters and two thermocouples to be deposited on top of this array (Figure 1b), providing the ability to heat the array from either

side while simultaneously measuring the mean temperature and the difference in temperature across a $200 \mu\text{m}$ length segment (Figure 1c). A high thermal conductivity of the support for the device is important because the mean temperature of the nanowires is controlled by thermostating a brass stage upon which this device is supported within a vacuum shroud, and expeditious measurements of S and σ across a range of temperatures require that the device remain in rapid thermal equilibrium. For this reason, wafers consisting of an electrically insulating silicon nitride (Si_3N_4) layer (thickness = 400 nm) on [100] silicon (thickness = $670 \mu\text{m}$) was used as the support in preference to glass because the thermal conductivity is more than 100 times higher. In this device configuration, the electrical contacts employed for measurement of the Seebeck voltage, V_s , are the same as those of the two thermocouples (TC₁ and TC₂) involved in the measurement of the temperature, and this confers the advantage that the temperature measurement

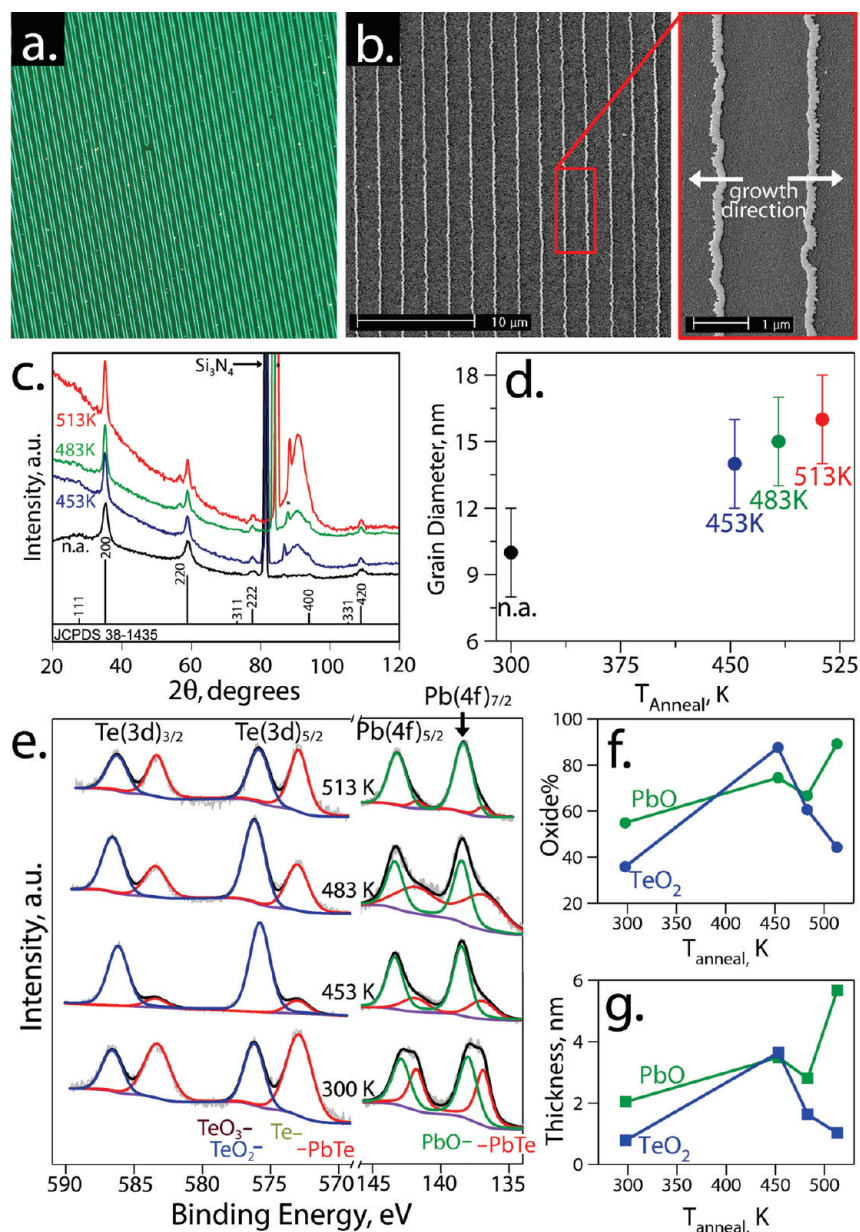


Figure 2. Characterization of as-prepared and thermally annealed PbTe nanowires. (a) Optical micrograph ($150\ \mu\text{m} \times 150\ \mu\text{m}$) of a PbTe array in which $40\ \text{nm} \times 200\ \text{nm}$ PbTe wires with lengths of a millimeter or more, prepared by LPNE, have been deposited at $2\ \mu\text{m}$ pitch. (b) Scanning electron micrographs (SEM) of $60\ \text{nm} \times 200\ \text{nm}$ PbTe nanowires. (c) Grazing incidence X-ray powder diffraction (GIXRD) of PbTe nanowire arrays. Diffraction patterns are shown for arrays that were as-prepared (not annealed, n.a.) and for which annealing was carried out for 0.5 h at the indicated temperature. (d) Grain diameter, d , estimated using the Scherrer equation and determined from the width of the 200 reflection. Error bars indicating $\pm 1\sigma$ indicate the sample-to-sample variability of d . (e) X-ray photoelectron spectra (XPS) of PbTe nanowire arrays showing the Te(3d) and Pb(4f) spectral regions for nanowires subjected to same four annealing protocols. (f) Fraction in % of the total signal for Pb and Te attributable to oxide (see text) plotted as a function of the annealing temperature. (g) Estimated thickness of the oxide (either PbO or TeO₂) plotted as a function of the annealing temperature.

occurs precisely at the point of electrical contact to the nanowires, something that is not easily achievable for temperature measurements carried out using a resistive temperature detector (RTD).^{18,19}

PbTe nanowires prepared on Si₃N₄/Si wafers were indistinguishable from PbTe nanowires synthesized earlier on glass^{30,31} both in terms of the morphology of these nanowires (Figure 2a,b) and in terms of their structure and chemical

composition revealed by GIXRD (Figure 2c) and XPS (Figure 2e). A bright-field optical image of $40\ \text{nm} \times 200\ \text{nm}$ PbTe nanowires, deposited at $2\ \mu\text{m}$ pitch, reveals the existence of defects that disrupt conduction in just 3 of the ~ 70 nanowires shown in this region, despite the fact that the LPNE fabrication process was carried out in unfiltered laboratory ambient air. The length of these nanowires is limited only by the dimensions of the photomask (Figure 1a). SEM images of

Table 1. Summary of Measured Seebeck Coefficient and Electrical Conductivity at 300 K for Bulk and Nanostructured PbTe

sample ^a	thermal treatment	grain diameter ^b	size ^c	S ($\mu\text{V/K}$)	σ (S/m)	$S^2\sigma$ ($\mu\text{W/mK}^2$)	ref ^d
NW film	353 K \times 12 h	SC	30 nm \times 100 μm	−628	133	52	Guo ²²
NW film	353 K \times 12 h	SC	20–40 nm \times 100 μm	−307	273	26	Guo ²¹
NW film	none	SC	10–30 nm \times 3 μm	410	2000	336	Yan ⁵²
NR film	none	SC	66 nm \times 0.7 μm	−263	—	—	Ramanath ⁵³
NC film	573 K \times 2 h	SC	30–60 nm	−451	28	5.7	Hu ⁵⁴
single NW	none	SC	60 nm \times 2 μm \times 2 μm	−72	0.44	0.0023	Lee ¹⁶
single NW	none	SC	83 nm \times 2 μm	—	10	—	Yang ²³
bulk	618 K \times 162–228 h	30–60 nm	—	−174–−508	500–2 \times 10 ⁴	122–648	Heremans ⁵⁵
bulk	—	SC	—	−265	2.7 \times 10 ⁵	1900	Heremans ⁵⁵
NW array	none	10 \pm 2	60 nm \times 200 nm \times 200 μm	−41	8100 \pm 1800	14	this work
NW array	453 K \times 30 m	14 \pm 2	same	−479	43 \pm 9	9.8	this work
NW array	483 K \times 30 m	15 \pm 2	same	−445	63 \pm 34	13	this work
NW array	513 K \times 30 m	16 \pm 2	same	−366	35 \pm 19	4.7	this work

^a Abbreviations: NW = nanowire, NR = nanorod, NC = nanocube. ^b SC = single crystalline. ^c Diameter \times length or width \times height \times length. ^d Corresponding author.

an array of PbTe nanowires (Figure 2b) shows the well-controlled width and two-dimensional trajectory on the substrate, which are desirable for thermoelectric property measurements.

For PbTe nanowires subjected to thermal annealing, two processing steps preceded the scheme shown in Figure 1a. First, freshly synthesized PbTe nanowires were encapsulated in a thin layer of Lift Off Resist (Microchem Lor 5A). Then, the encapsulated nanowires were heated in flowing N₂ at an annealing temperature, T_{anneal} , of 453 K (or 483 or 513 K) for 30 min. The purpose of the Lor 5A was to prevent morphology changes to the PbTe nanowires induced by grain growth that might cause wire breakage. After thermal annealing, a thin layer of photoresist (Shipley 1808) was spin-coated on the top of the Lor layer, and then, the sample was baked at 363 K for an additional 30 min prior to photopatterning, as shown in Figure 1a. Unlike photoresist, Lor is not photoreactive, but it is rapidly removed by the developer used for photoresist processing. The use of the Lor 5A preserved the morphology of the unannealed PbTe nanowires (data not shown), but GIXRD patterns for the annealed nanowires (Figure 2c) reveal line narrowing consistent with grain growth. The line width of the (200) reflection was used in conjunction with the Scherrer equation³² to estimate the grain diameter, d , which increased linearly with T_{anneal} from 10 nm for unannealed PbTe to 16 nm for PbTe wires annealed at 513 K (Figure 2d).

In addition to inducing grain growth, thermal annealing also caused changes to the surface chemical composition of PbTe nanowires revealed by XPS (Figure 2e). Deconvolution curve fitting was used to analyze the chemical states of the Pb and Te present at the nanowire surface for samples annealed for 30 min at each T_{anneal} . For Te(3d), two distinct chemical states, each represented by a doublet of peaks,³³ were present in each spectrum. For the 3d_{5/2} peak, the lower binding energy (BE) component at 573.0 eV (red) is assigned to PbTe,^{34–36} and the higher BE component at 576.0 eV (blue) is assigned to TeO₂. An analogous situation exists with the lead where, again, a pair of spin–orbit-coupled doublets are observed for all four samples.

For these spectra, the lower-energy 4f_{7/2} component with a BE of 136.9 eV is assigned to PbTe,^{34–36} and the higher-energy component with BE of 138.1 eV (green) is assigned to PbO.³⁷ The fraction of the observed Pb and Te present as PbO and TeO₂ is plotted versus the annealing temperature in Figure 2f. The presence of oxide in these spectra is not surprising since PbTe nanowires are exposed to laboratory air for approximately 30 min prior to transfer to the vacuum environment in which these XPS spectra were acquired. This air exposure is approximately the same as was experienced by nanowires during the fabrication steps required for preparation of the device shown in Figure 1. On the basis of the layered oxide structure model suggested by Bando et al.^{38,39} and the inelastic mean-free paths of the photoelectrons,⁴⁰ the equivalent thickness of the oxide layers was also estimated as a function of T_{anneal} (Figure 2g). Plots of the thickness of the oxide layers versus T_{anneal} , both PbO and TeO₂, have maxima at 453 K, and these oxide layers are progressively reduced in thickness at 483 and 513 K. Additional experiments will be required to understand this evolution with temperature. However, one possible explanation for the complex behavior seen in Figures 2f,g is the following; the volatility of TeO₂ is considerably higher than that of PbO at temperatures above 450 K.^{41,42} If the Lor layer has some permeability to residual O₂ and water vapor, then wire oxidation can occur, and the preferential loss of TeO₂ relative to PbO through the Lor layer, or the dissolution of TeO₂ into the Lor layer, might be expected to occur at 483 and 513 K. It is also possible that decomposition of the Lor, a poly(methylglutarimide), supplies the oxygen required for nanowire oxidation and that this process is optimized at 453 K. Previously,³⁰ we have shown that the formation of oxides on PbTe nanowire surfaces is associated with a reduction in σ , and we observe a similar depression of σ here (Table 1). The maximum thickness of the oxide at $T_{\text{anneal}} = 453$ K also coincides with the maximum Seebeck coefficient measured in this study. Thus, the presence of the oxide layer and its thickness may have a complex influence on the thermoelectric performance of these nanowires, and further study will be required to fully understand its effect.

The measurement of S was accomplished by placing the device shown in Figure 1 on the brass stage of a vacuum cryostat (base pressure = 5×10^{-6} Torr) that permitted the mean temperature to be varied from 200 to 300 K in 10 K intervals. At each temperature within this interval, one of the heaters was powered by a constant dc current, the temperatures at the two

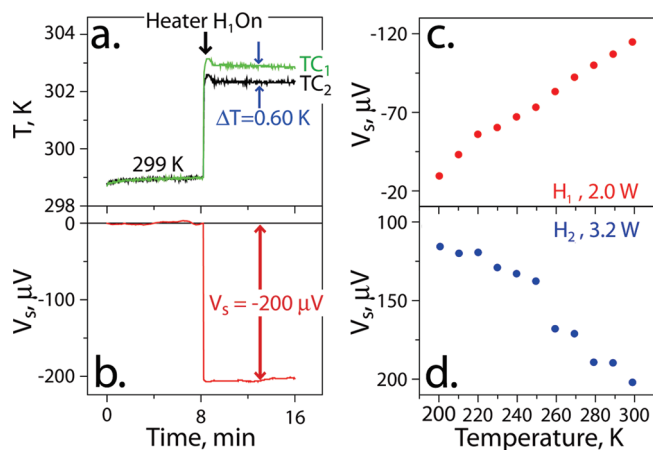


Figure 3. Raw data for the measurement of the Seebeck coefficient for an array of 250 unannealed 60×200 nm PbTe nanowires. (a) Thermocouple (TC₁ and TC₂) response before and after turning on heater 1 (H₁). (b) Seebeck voltage (V_s) response of $-200 \mu\text{V}$ to the temperature gradient produced by powering H₁, as shown in (a). (c, d) Plots of the measured V_s versus T acquired for the use of the two heaters, producing temperature gradients along opposite directions of the PbTe nanowire array. The power applied to the two heaters is as indicated.

TCs (calibrated prior to use) were simultaneously measured, and the V_s developed between the two thermocouples was measured as a function of time. The process was then repeated at the same temperature using the other heater. Typical raw data for such a single measurement (Figure 3a, b) show that the application of 2.0 W to H₁ produces a stepwise increase at both TC₁ and TC₂, but the increase at TC₁ is 0.60 K higher than that seen at TC₂, location $200 \mu\text{m}$ from TC₁ (Figure 3a). This induced ΔT produces a V_s of $-200 \mu\text{V}$ in the PdTe nanowire array (Figure 3b). The negative sign on V_s confirms that the majority of carriers in these nanowires are electrons. The two sets of measured values for V_s as a function of T (Figure 3d, e) correspond to the measurement of this quantity using the two heaters, using the indicated heater powers.

The magnitude of the measured S increases linearly with T for all four annealing temperatures, in accordance with the Mott equation^{43,44}

$$S = \frac{2\pi^2 k^2 m^*}{3(3\pi^2)^{2/3} \hbar q n^{2/3}} T \quad (2)$$

where k is Boltzman's constant, m^* is the effective mass of the electron, q is the elementary charge, and n is the carrier concentration. Using eq 2, the carrier concentration, n , can be estimated from the experimentally determined slope of S versus T from Figure 4a and the known effective mass of electrons in PbTe ($m^* = 0.25m_0$ ⁴⁵). This equation is valid for degenerate semiconductors having carrier concentrations in the range from 10^{18} – 10^{20} cm^{-3} .^{46,47} Upon the basis of our analysis of S versus T presented below, the dopant density for

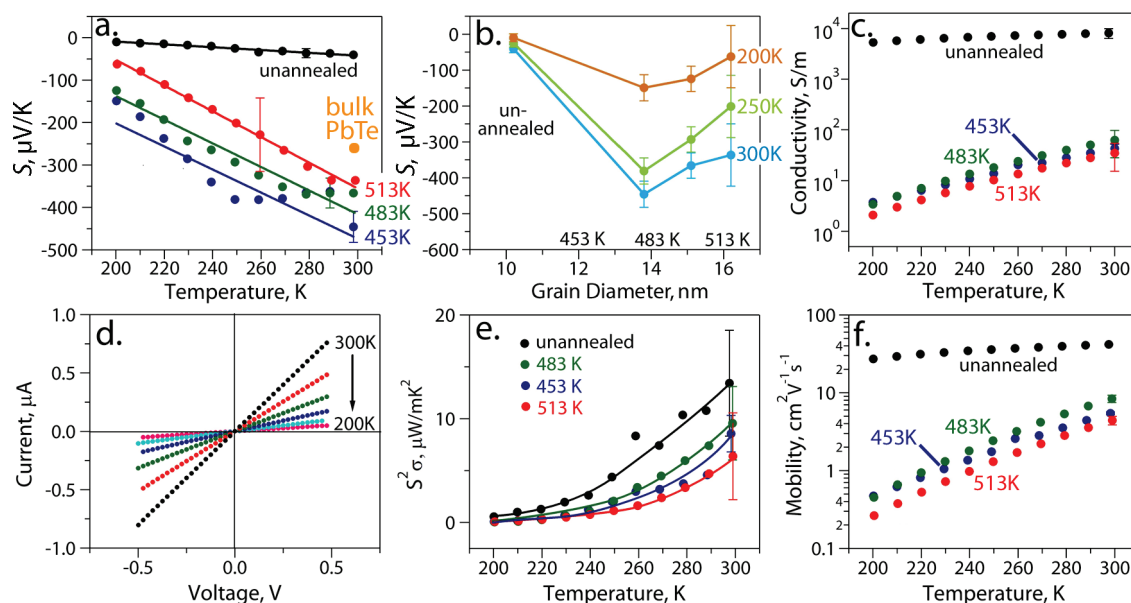


Figure 4. Influence of T_{anneal} on the S and σ of 60×200 nm PbTe nanowires. (a) Plots of Seebeck coefficient against annealing temperature, T_{anneal} , for PbTe nanowires that were unannealed (black) and for $T_{\text{anneal}} = 453$ (blue), 483 (green), and 513 K (red). For each temperature, values from three samples were averaged; error bars indicate $\pm 1\sigma$. Solid lines are least-squares fits to the data. (b) Plots of S , measured at three temperatures, as indicated, versus the mean grain diameter. (c) Plots of σ versus T for the same 12 samples measured in (a) and (b). (d) Typical current versus voltage curves obtained for an array of ~ 200 PbTe nanowires recorded as a function of T in the temperature window of 200–300 K. The linearity of these plots demonstrates that the nickel electrical contacts are ohmic. (e) The power factor, $S^2\sigma$, plotted against T_{anneal} . Error bars indicate $\pm 1\sigma$ for measurements conducted on three nanowire samples. (f) The carrier mobility versus temperature for unannealed PbTe nanowires and for those annealed at three temperatures, as indicated.

the PbTe nanowires investigated here was at the lower end of this range, $0.47(\pm 0.07) - 12(\pm 2) \times 10^{18} \text{ cm}^{-3}$.

For PbTe nanowires annealed at 453 K, we find that S (at 300 K) is increased from $-41 \mu\text{V/K}$ for unannealed wires to $-479 \mu\text{V/K}$, 80 % larger in magnitude than the S of bulk PbTe of $-260 \mu\text{V/K}$ (Figure 4b).⁴⁸ The electrical conductivity was measured as follows. First, the conductance of the nanowire array between 200 and 300 K was measured immediately after measurement of the Seebeck coefficient without breaking vacuum. From this series of measurements, a temperature coefficient of conductance, $\text{TCC} = d\sigma/dT$, specific to these nanowires was obtained. Next, all of the nanowires between TC_1 and TC_2 except the orphaned nanowire (Figure 1) were cut manually using a glass capillary. Then, the electrical conductance of the orphaned nanowire was measured at 300 K by acquiring a current–voltage trace. The conductivity of this nanowire was then calculated from its SEM-measured width (W) and AFM-measured height (H) using $\sigma = (IL)/(VWH)$, where I is the current measured at a voltage V and L is the length of the nanowire between the electrical contacts. Values for σ at temperatures other than 300 K were obtained using the temperature coefficient of conductance measured for the nanowire array.

Thermal annealing at all three T_{anneal} values (Figure 4c) reduces σ by 2–3 orders of magnitude, and no significant variation of σ is seen over the range of T_{anneal} from 453 to 513 K (Figure 4c). This depression of σ results in a net reduction in the power factor, $S^2\sigma$, despite the elevation of S at the annealed nanowire samples (Figure 4e). Finally, using the measured values of σ and n , the electron mobility, μ , is obtained using the equation $\mu = \sigma/nq$ (Figure 4f).^{48,49} Our measured μ values (Figure 4f) are much smaller than that of bulk PbTe ($\sim 1400 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$)⁵⁰ but somewhat higher than the value of $0.7 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ (300 K) measured by Yang and co-workers²³ for single-crystalline PbTe nanowires. Upon the basis of the XPS results presented above, we tentatively attribute the depression of μ caused by thermal annealing to enhanced boundary scattering of electrons induced by the oxide layer.⁵¹

Prior measurements of S and σ for PbTe are summarized in Table 1. Relative to our measurement of $S = -479 \mu\text{V/K}$ (300 K) for PbTe nanowires annealed at 453 K, larger values of S have been reported in just one previous case by Guo et al.,²² who found $S = -628 \mu\text{V/K}$ for a film of single-crystalline, 30 nm diameter PbTe nanowires annealed at 353 K for 12 h. What is responsible for the huge Seebeck coefficient enhancement of these annealed PbTe nanowires? One can gain some insight from an alternative form of the Mott equation⁴⁶

$$S = \frac{2\pi^2 k^2}{3q} \left\{ \frac{1}{n} \frac{dn(E)}{dE} + \frac{1}{\mu} \frac{d\mu(E)}{dE} \right\} \quad (3)$$

where $n(E)$ and $\mu(E)$ are the energy-dependant carrier concentration and carrier mobility, respectively, and q is the elementary charge. Equation 3 predicts that an elevation of S can be associated with a decrease in n and/or μ , both of which we have observed here (Figure 4f, Table 1), but we are unable to estimate the values of $dn(E)/dE$ and $d\mu(E)/dE$ and therefore to predict S using our measured values of n without additional information.

In summary, a new approach for measuring S and σ for nanowires involves the fabrication of electrical contacts on top of a lithographically patterned array of nanowires, prepared using the LPNE method. At an optimum annealing temperature of 453 K, the S at 300 K is increased from $-41 \mu\text{V/K}$ for unannealed wires to $-479 \mu\text{V/K}$, 80 % larger in magnitude than the S ($-260 \mu\text{V/K}$) of bulk PbTe. In future work, the surface oxidation of these PbTe nanowires during the annealing process must be suppressed in order to achieve higher carrier mobilities, leading to higher electrical conductivities and enhanced power factors.

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