

Instruction	RegSrc	RegWrite	ImmSrc	ALUControl	AluSrc	ShiftEnb	MemWrite	MemtoReg	NoWrite
ADD	0	1	XX	000	0	0	0	00	0
SUB	0	1	XX	001	0	0	0	00	0
AND	0	1	XX	100	0	0	0	00	0
ORR	0	1	XX	101	0	0	0	00	0
LSR	0	1	XX	000	0	1	0	10	0
LSL	0	1	XX	000	0	1	0	10	0
CMP	1	0	XX	010	0	0	0	XX	1
STR	1	0	01	000	1	0	1	XX	0
LDR	X	1	01	000	1	0	0	01	0