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## JEDEC Kernel Mode, Code 92

<kernel>::=<STX><design spec><min. fuse information><ETX><xmit checksum>

<design spec>::={<field character>}\*\*

<min. fuse information>::=<fuse list>{<fuse list>}

You may use the JEDEC kernel format if you wish to send only the minimum data necessary to program the logic device; for example, if you do not want to send any test vectors. If you specify format code 92, the programmer will ignore everything except the design specification field and the fuse information field. The following fields will be ignored if format 92 is specified: C, F, G, Q, V, and X. Also, the security fuse will be set to zero and the transmission checksum will be ignored.

Figure 1-19 shows an example of a kernel JEDEC transmission.

*Figure 1-19  
An Example of JEDEC Kernel  
Mode Format*

```
<STX>
Acme Logic Design  Jane Engineer    Feb. 29 1983
Widget Decode  756-AB-3456 Rev C Device Mullard 12AX7*
L0000 1111111011 1111111111 1111000000 0000000000
      0000000000 0000000000 0000000000 0000000000
      0000000000 0000000101 1111111111 1111111111
      0000000000 0000000000 0000111101 1111111111
      1111111111 1111110111 1111111111 1111111111*
L0200 1110101111 1111110000 0000000000 0000000000
      1111111111 1111011011 1111111111 1111111110
      0111111111 1111111111 1111111110 1111111111
      1111111111 1111101111 1111111111 1111101111
      0000000000 0000000000 0000*
<EXT>0000
```

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